motherboard

motherboard

X 2

motherboard

motherboard X 6

motherboard

motherboard

X 2

front end
electronic

X10
buses

addon board

FIFO(1)

FIFO (2)

FIFO (3)

X 10
small
FIFOs

FIFO(event collector)

LVL2 algorithm

TRB board

I take for example the DUBNA chamber: 14 short motherboards
(8 TDC each) and 2 long moterboards (12 TDC each).
At the end, I have 16 motherboards to read out or 136 TDCs