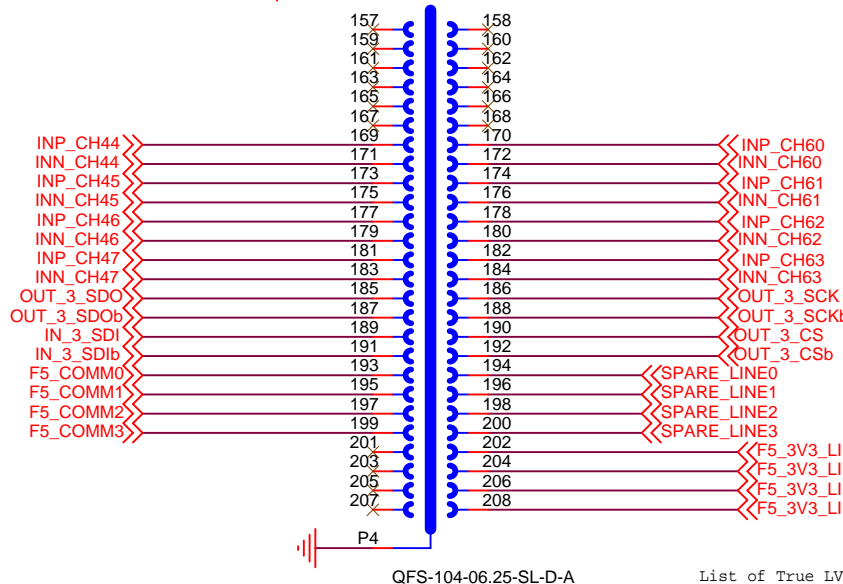
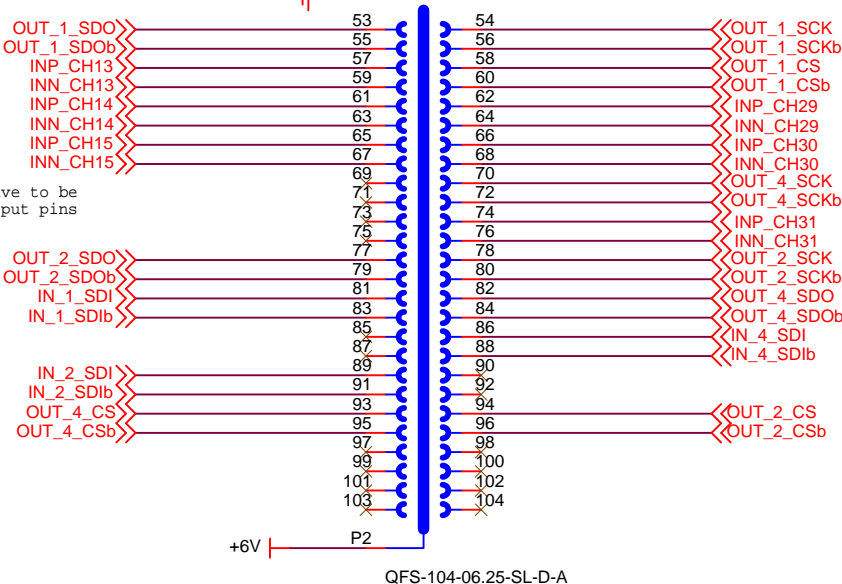
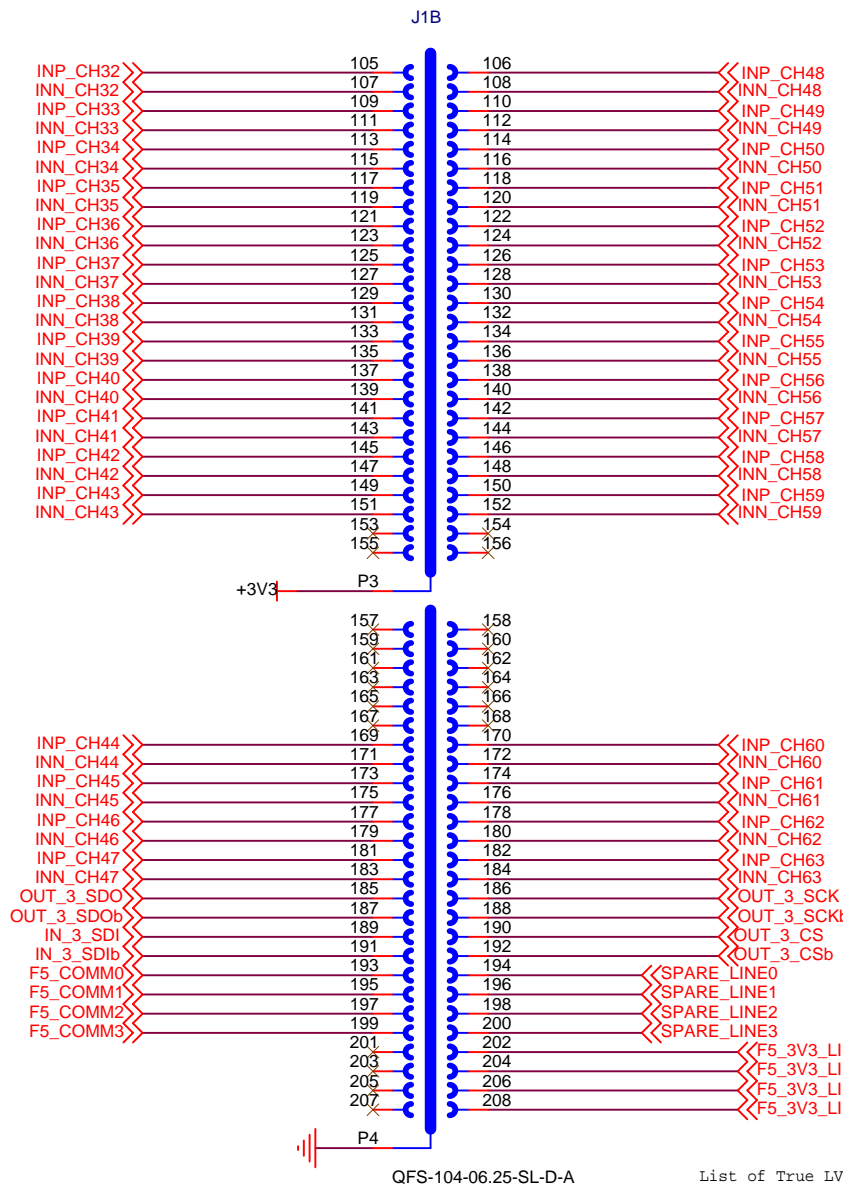
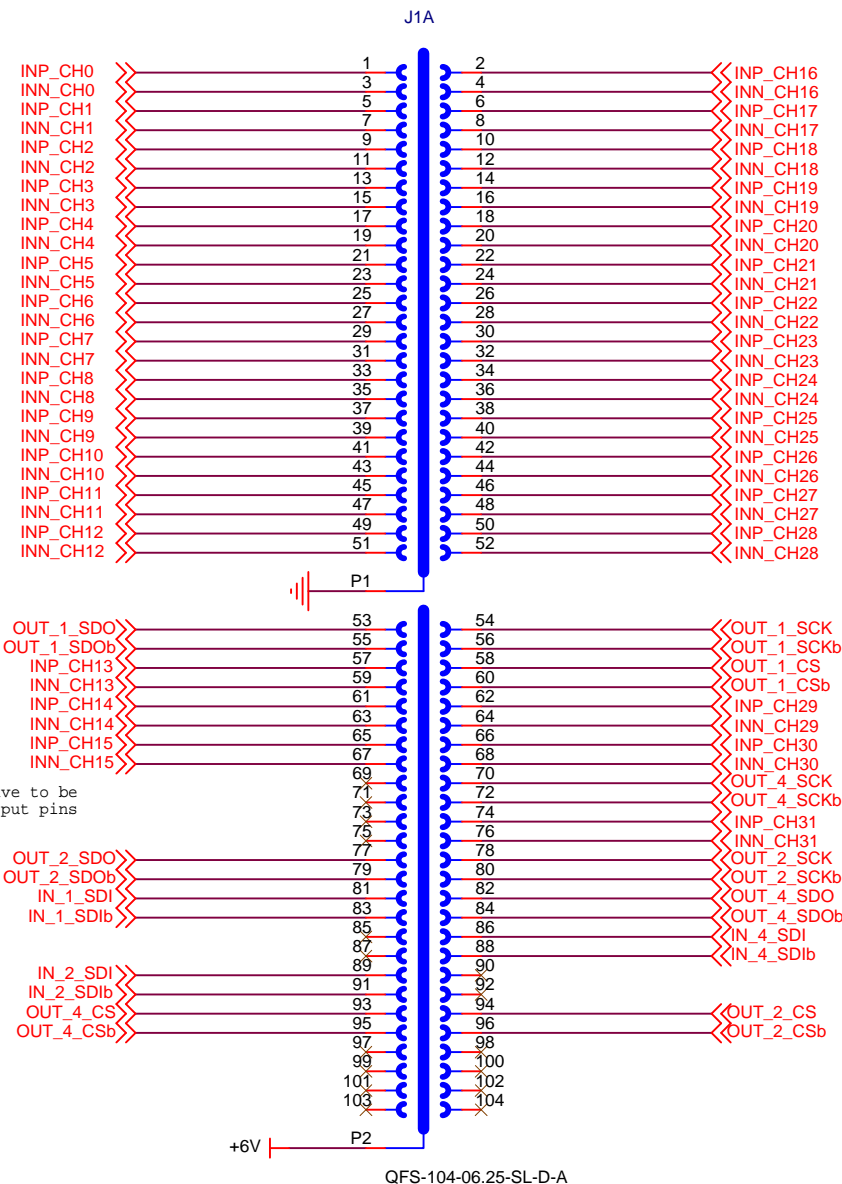


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01_CABLE_SIDE

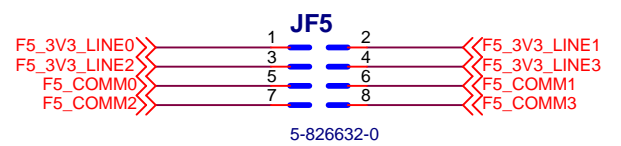
Design: K:\GSIJOB\HADES\TRBV3\ADDON_4CONN2\ADDON_4CONN2.DSN	
Modified: Monday, July 23, 2012	Size: A4 Page: 1 / 2
Designer: M.Traxler	Layouter: S.Voltz



true LVDS outputs have to be used for the SPI-output pins

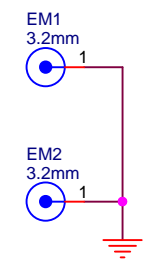
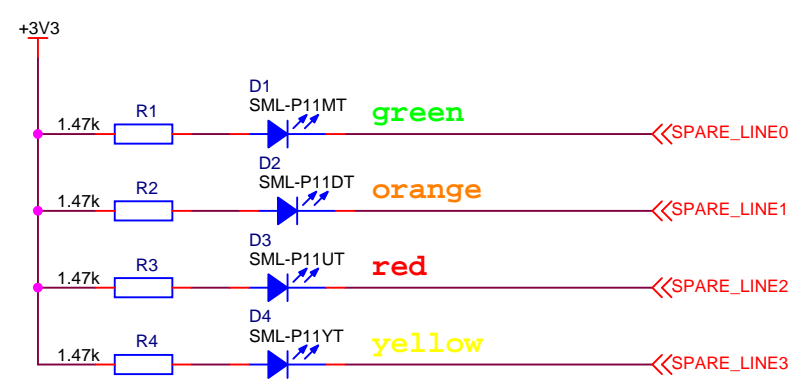
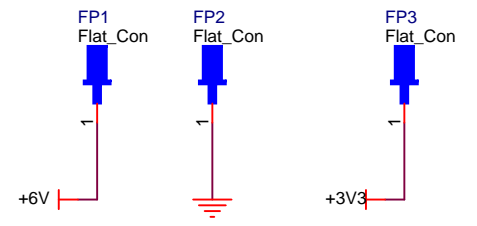
List of True LVDS pins on P2

- dgUL0: 78, 80, 82, 84, 94, 96
- dgUL1: 77, 79, 89, 91, 93, 95
- dgUL2: 54, 56, 58, 60, 70, 72
- dgUL3: 53, 55



List of True LVDS pins on P4

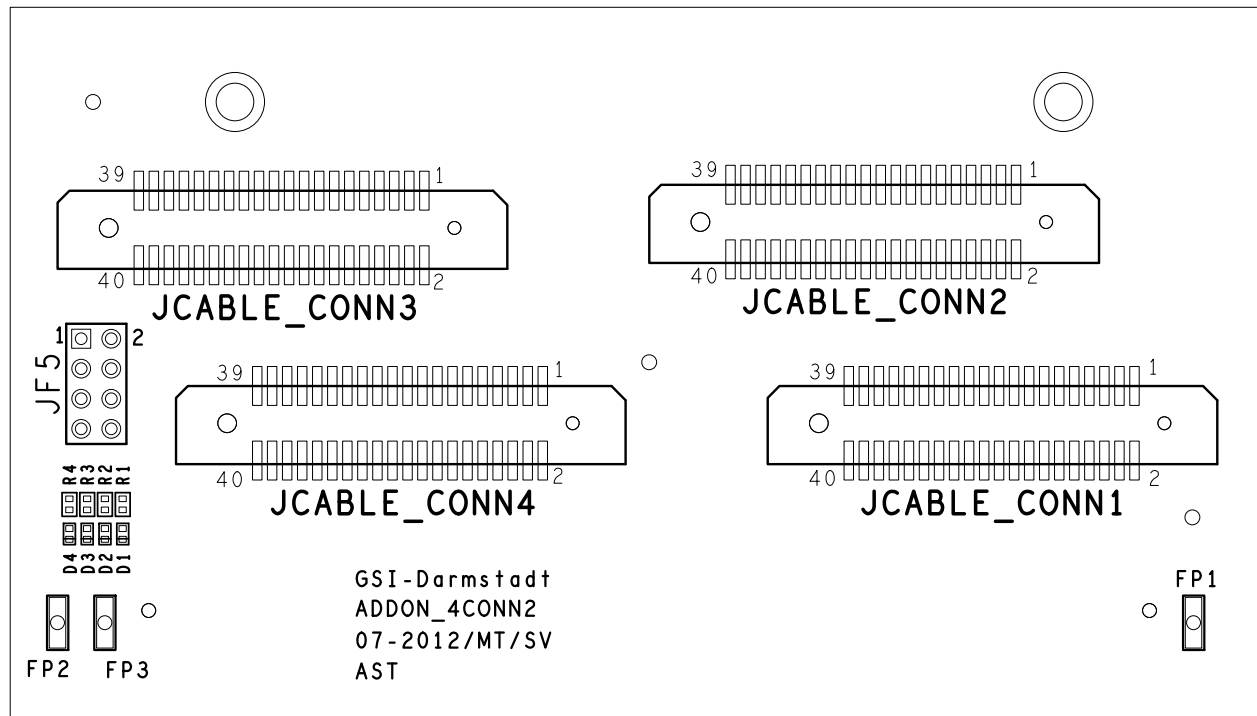
- dgLR1: 169, 171, 177, 179, 185, 187
- dgLR2: 174, 176, 186, 188, 190, 192



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02_TRB_SIDE

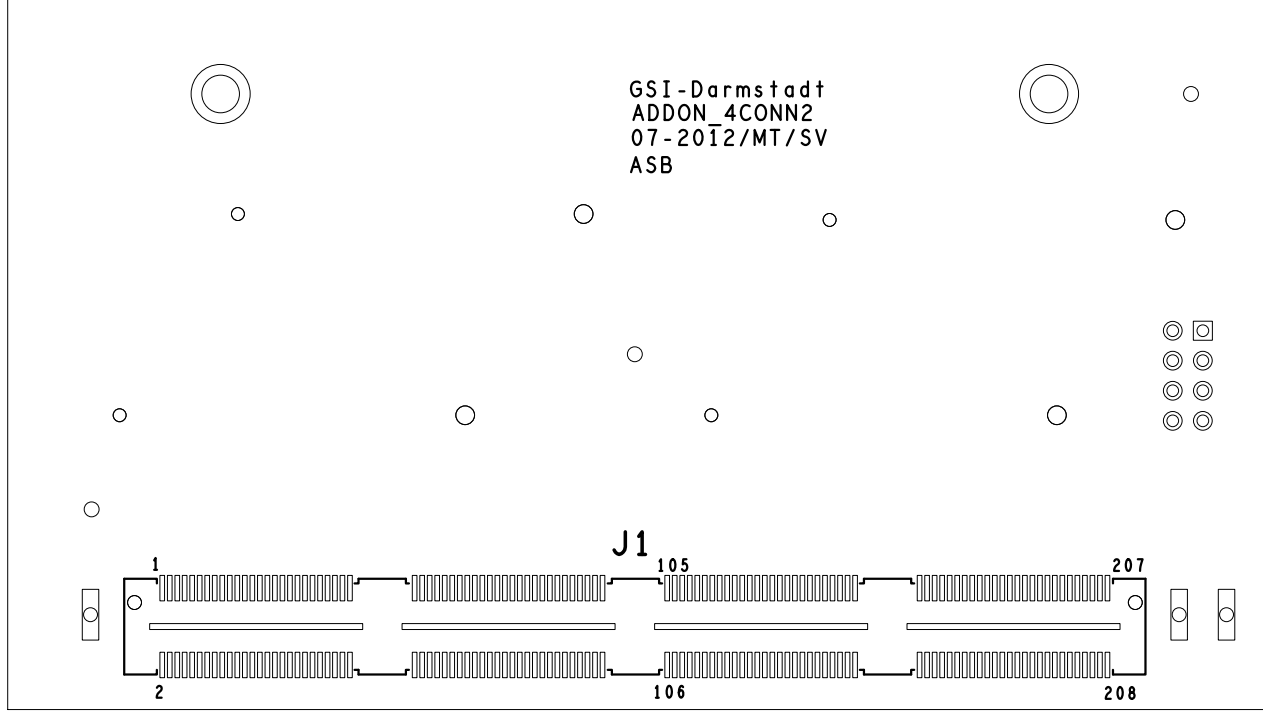
Design: K:\GSI\JOB\HADES\TRBV3\ADDON_4CONN2\ADDON_4CONN2.DSN		
Modified: Monday, July 23, 2012	Size: A3	Page: 2 / 2
Designer: M. Traxler	Layouter: S. Voltz	



Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

Ast

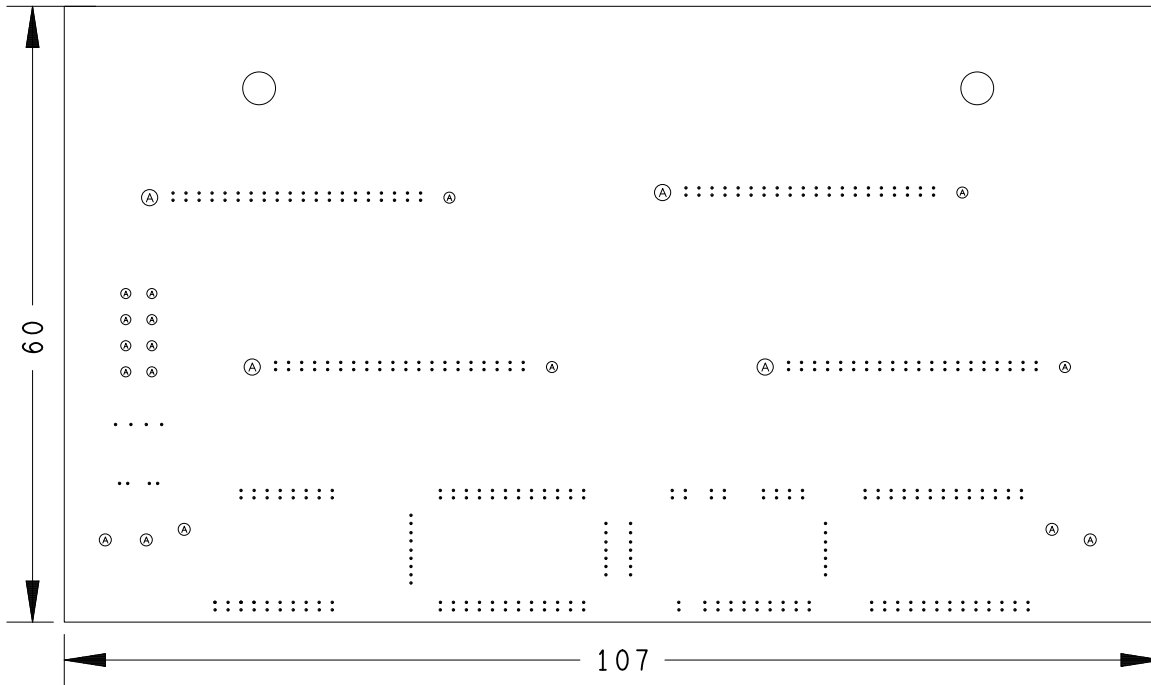


Layouter
S.Voltz

Designer
M.Traxler

Date
07.2012

Jobname
ADDON_4CONNS



DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	SIZE	PLATED	QTY
.	0.25	PLATED	374
°	0.4	PLATED	1
⊙	1.0	PLATED	8
⊗	1.2	PLATED	3
○	3.2	PLATED	2
⊙	1.1	NON-PLATED	4
⊗	1.2	NON-PLATED	2
⊗	1.6	NON-PLATED	4

TOTAL HOLES: 398

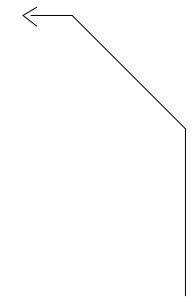
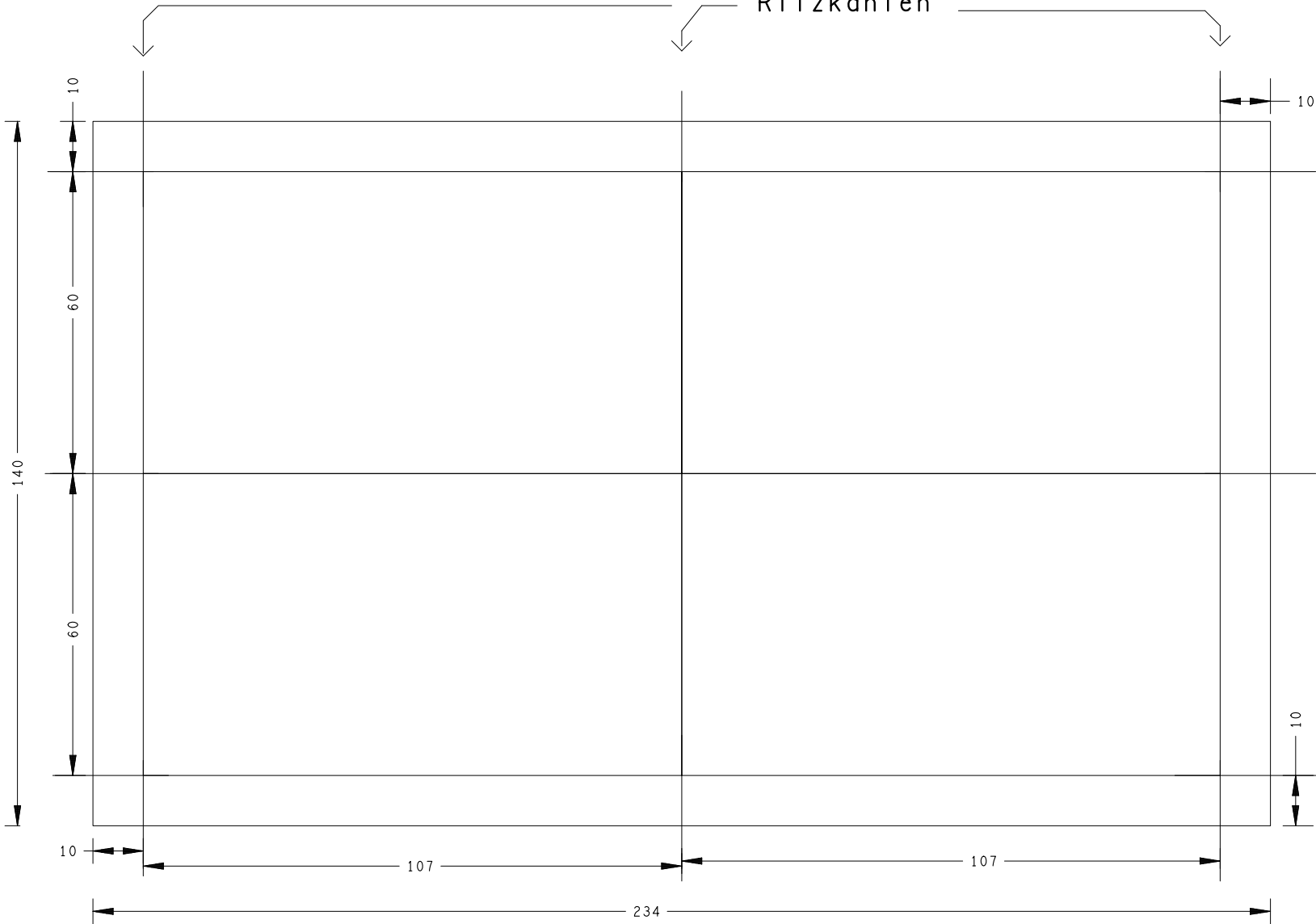


Jobname Date Designer Layouter
 ADDON_4CONN2 07.2012 M.Traxler S.Voltz

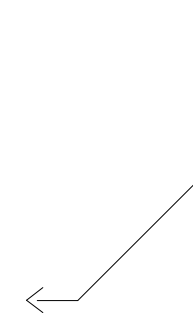
Layer Nickname

Dr d

Ritzkanten



Ritzkanten

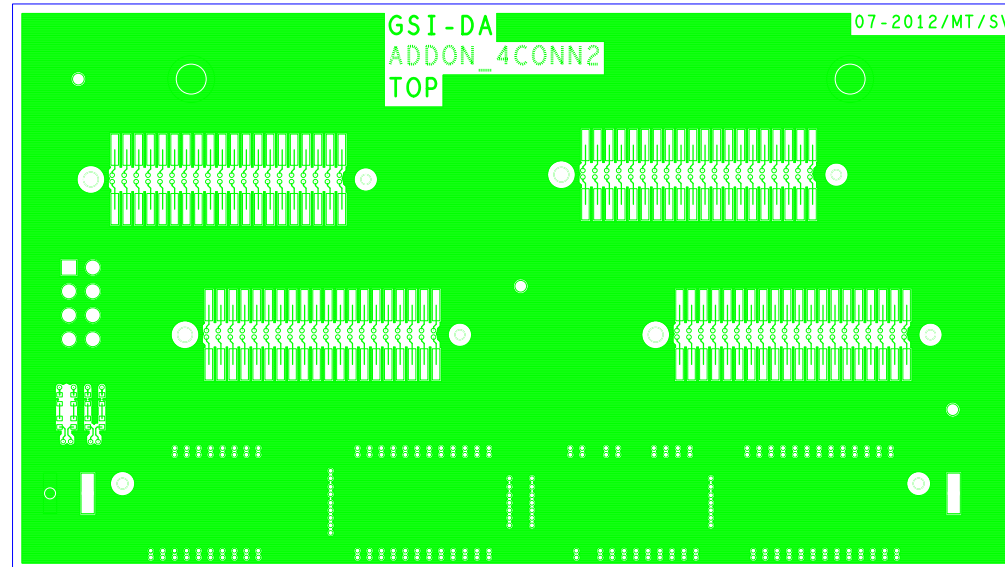


Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

Fab

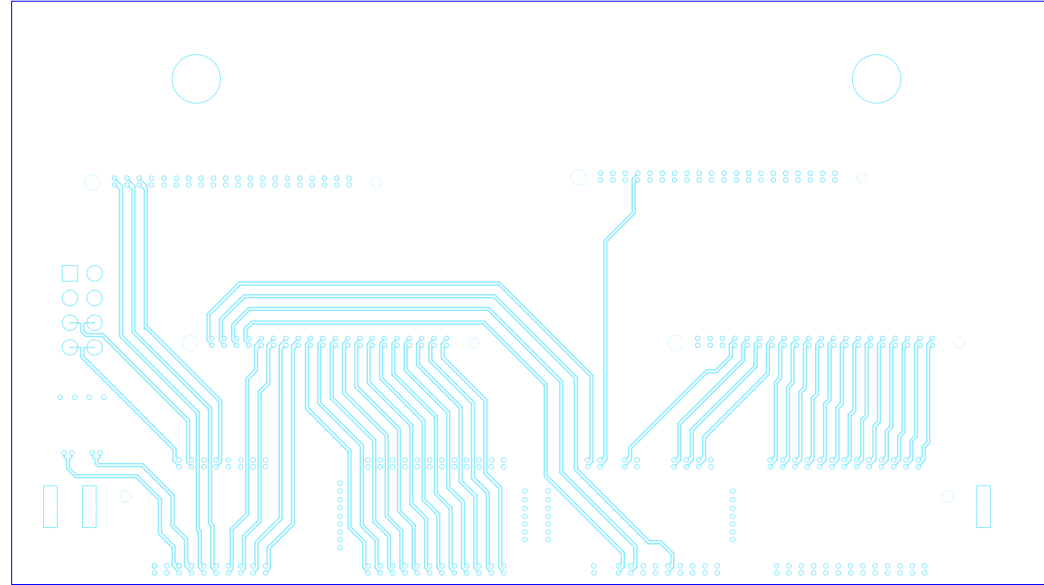
TOP- Signals,GND-Plane



Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname
Top

IN1- Diff.Pairs

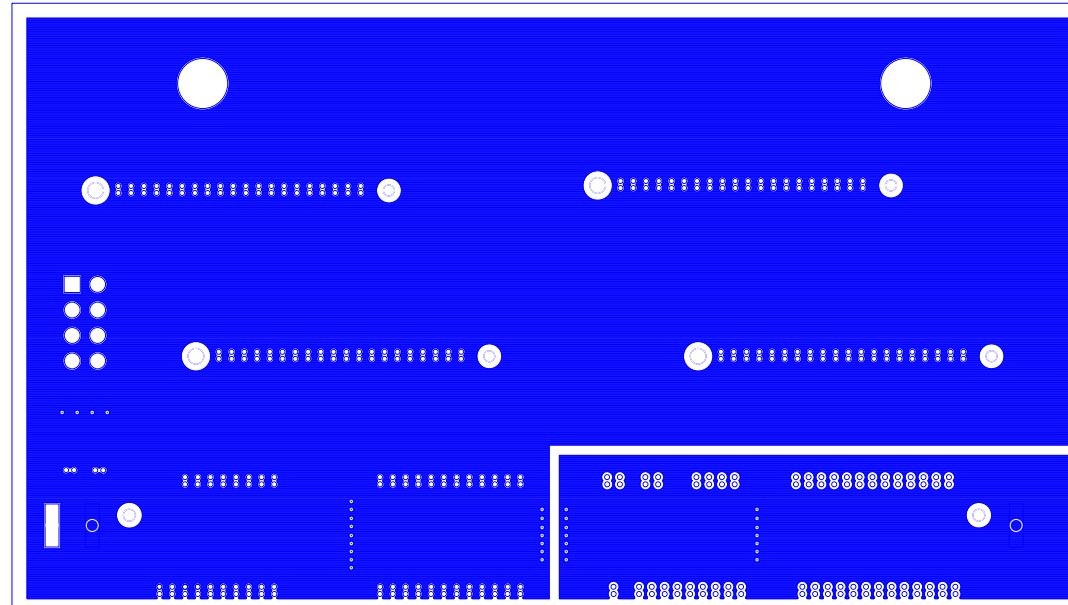


Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

I01

IN2- Planes: +3V3,+6V

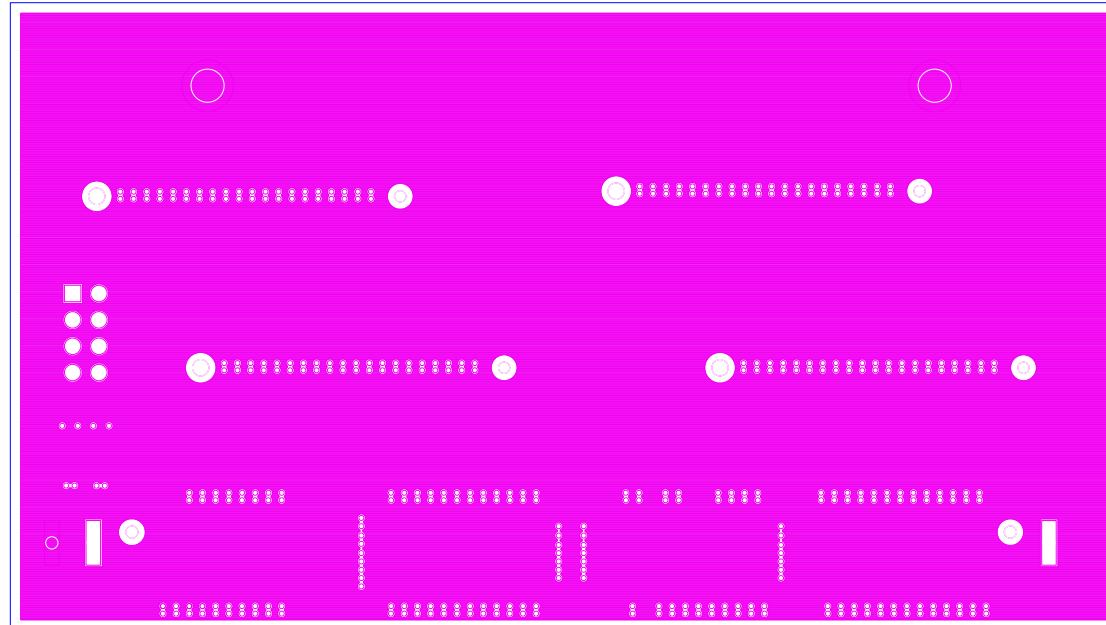


Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

102

IN3- GND-Plane

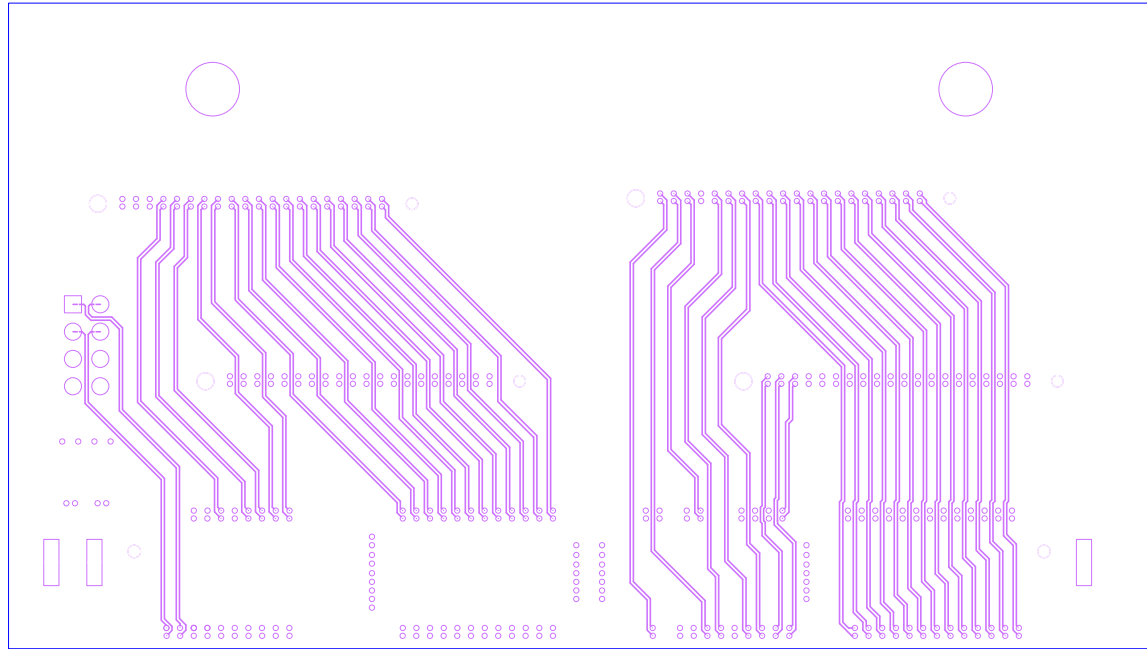


Jobname Date Designer Layouter
ADDON_4CONN2 07.2012 M.Traxler S.Voltz

Layer Nickname

I03

IN4- Diff.Pairs

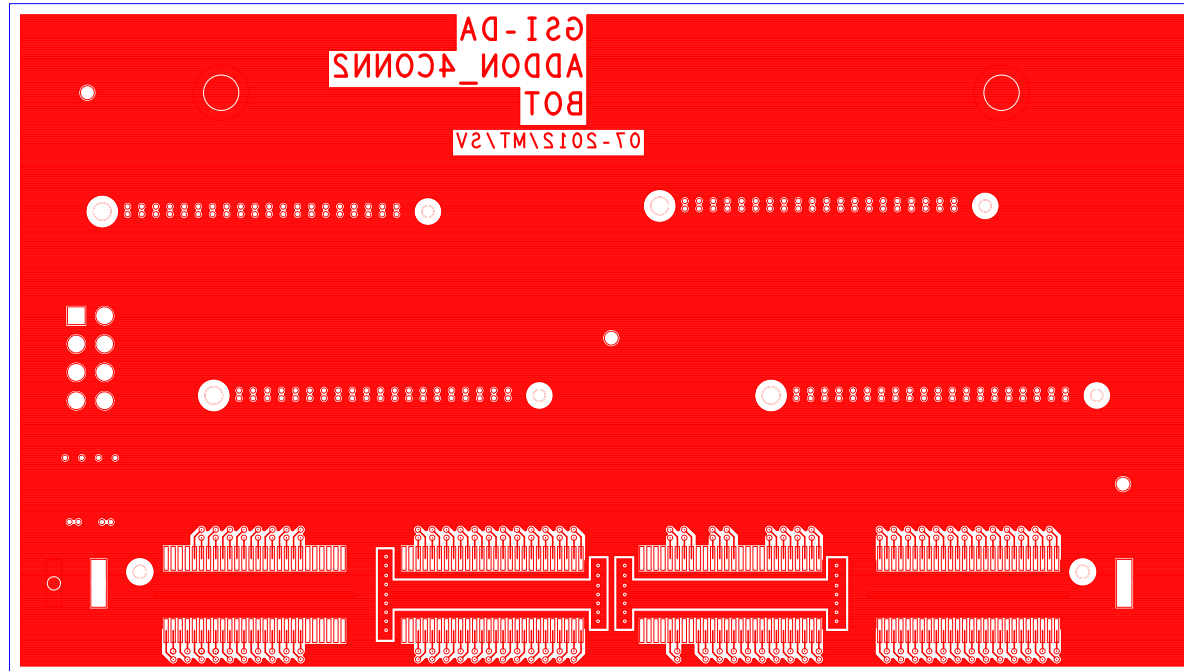


Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

I04

BOT- Signals, GND-Plane

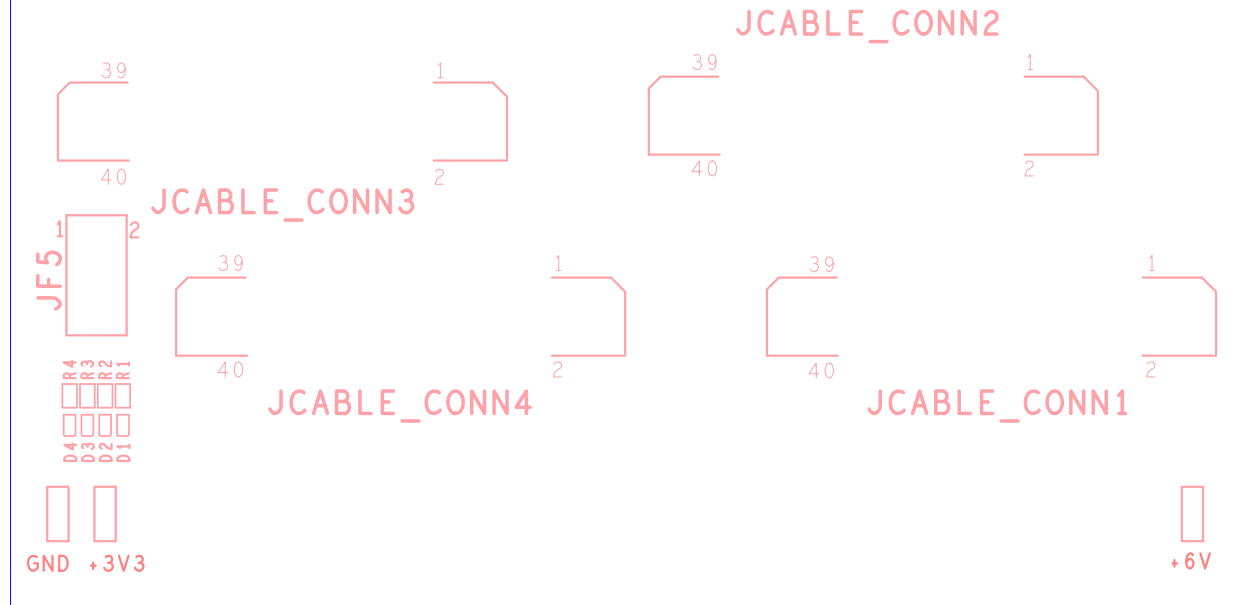


Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer	Bot
Nickname	

GSI-DA
ADDON_4CONN2

07-2012/MT/SV



Jobname
ADDON_4CONN2

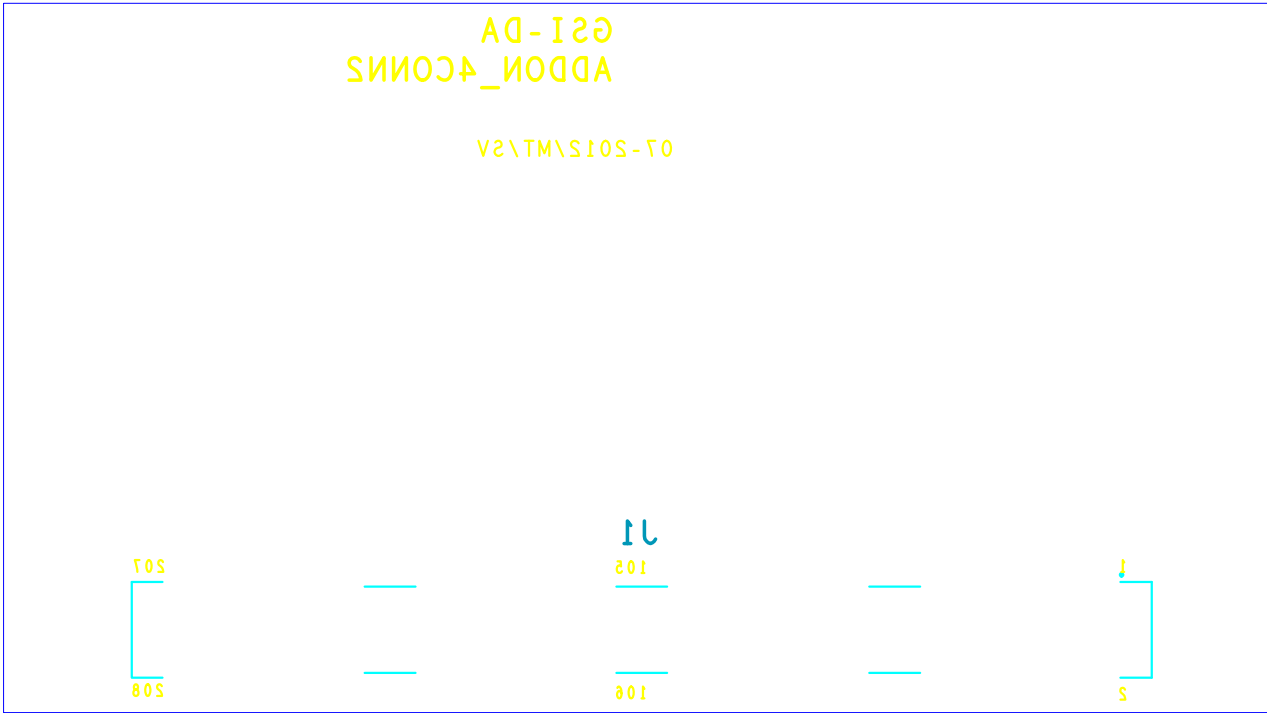
Date
07.2012

Designer
M.Traxler

Layouter
S.Voltz

Layer Nickname

Sst



Jobname	Date	Designer	Layouter
ADDON_4CONN2	07.2012	M.Traxler	S.Voltz

Layer Nickname

Ssb