

LVTTL

JTAG

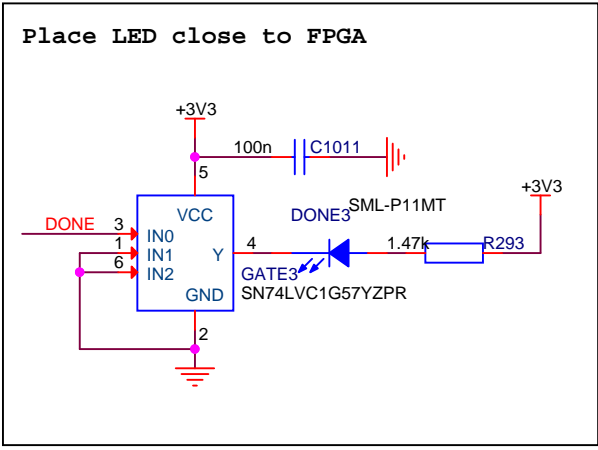
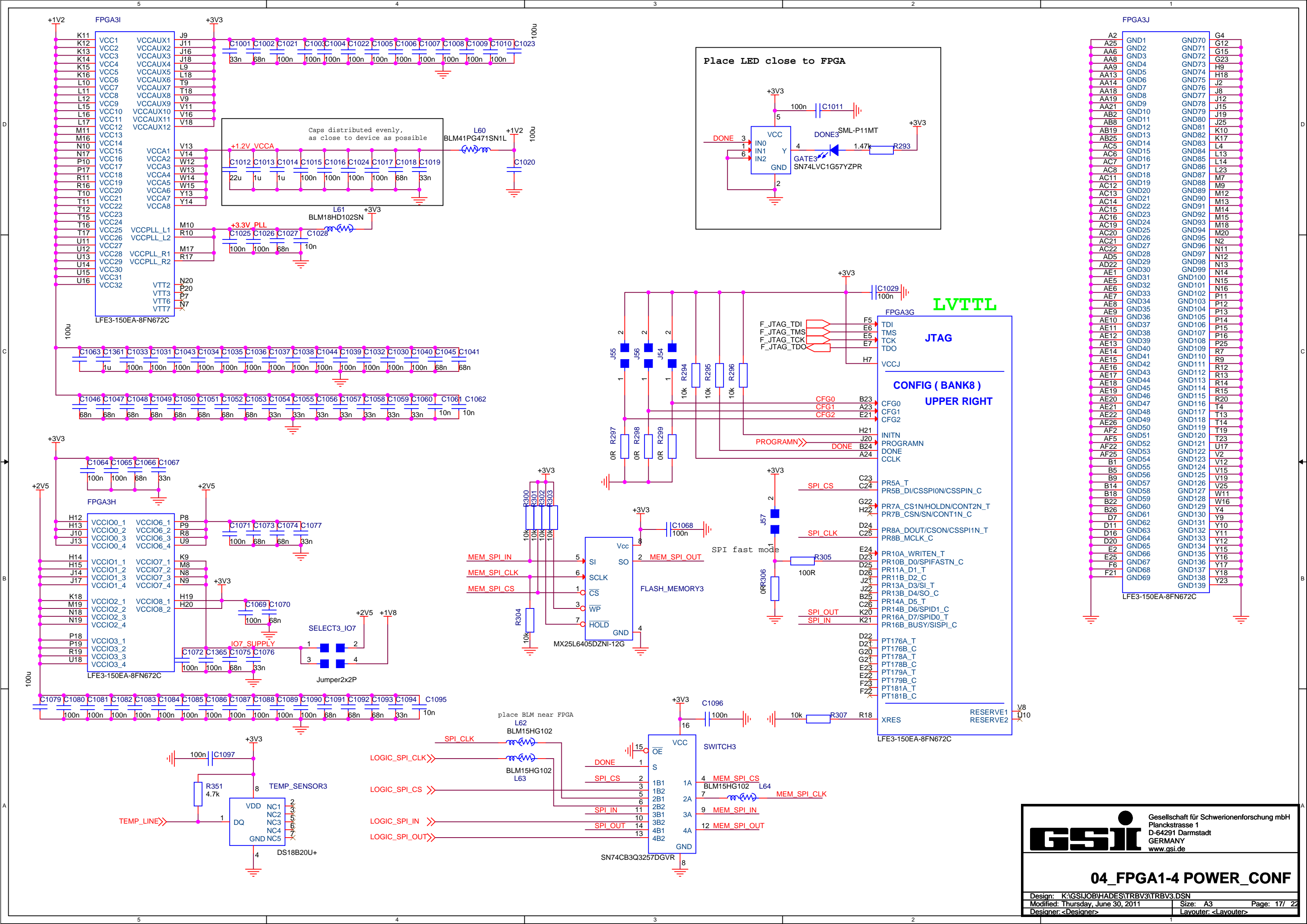
CONFIG (BANK8)  
UPPER RIGHT



Gesellschaft für Schwerionenforschung mbH  
Planckstrasse 1  
D-64291 Darmstadt  
GERMANY  
www.gsi.de

### 04\_FPGA1-4 POWER\_CONF

Design: K:\GSI\JOHANNES\TRBV3\TRBV3.DSN	Size: A3	Page: 16/ 22
Modified: Thursday, June 30, 2011	Layou: <Layou>	
Designer: <Designer>		



LVTTL

JTAG  
CONFIG (BANK8)  
UPPER RIGHT

RESERVE1  
RESERVE2



