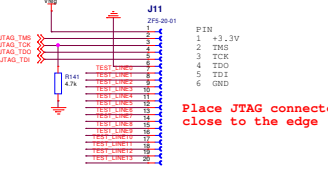
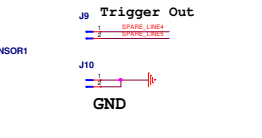
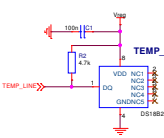
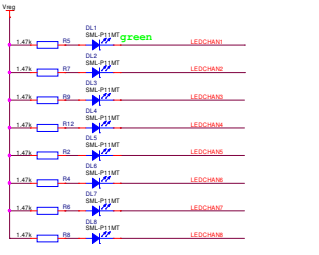
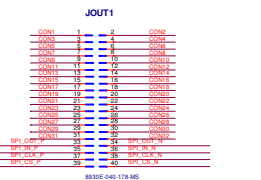
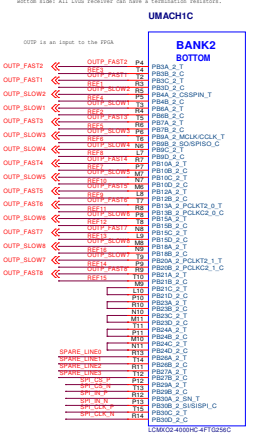
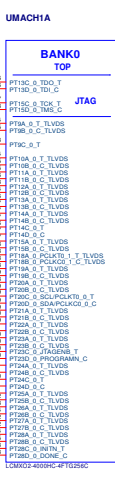
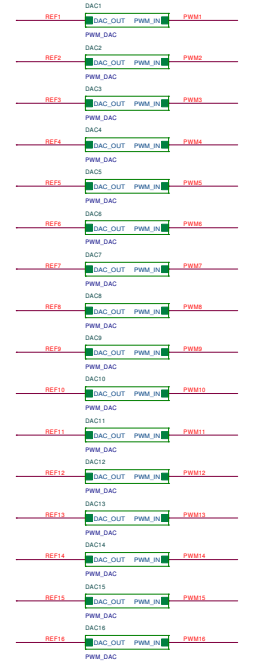
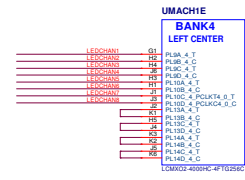
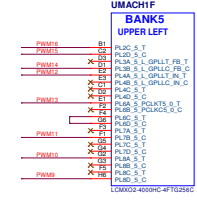
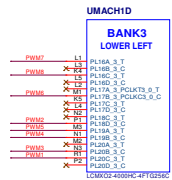


GSI
 01_ERNI-Connector

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 Planckstrasse 1
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 GERMANY
 www.gsi.de

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 Modified: Wednesday, May 16, 2018
 Designer: B. Gröbe

Size: A3
 Paper: 1 / 2
 Drawn: S. Voitz

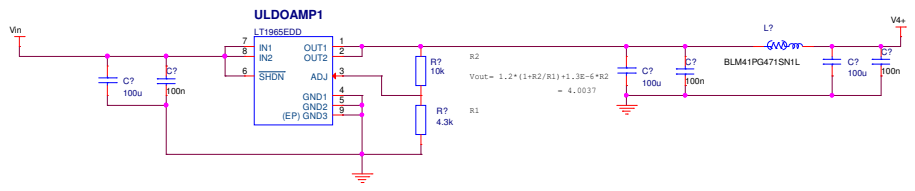
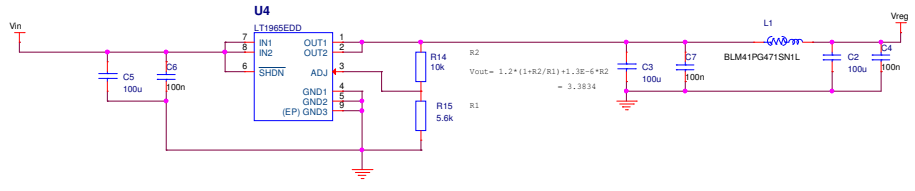
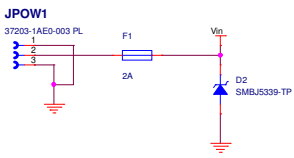
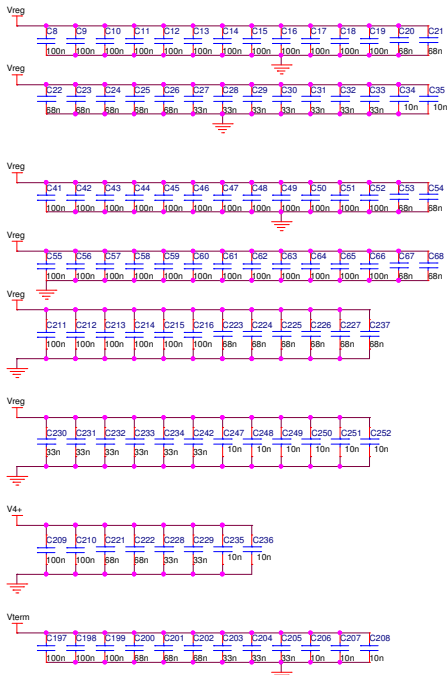


Place JTAG connector close to the edge

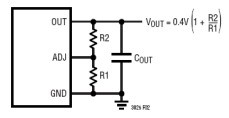
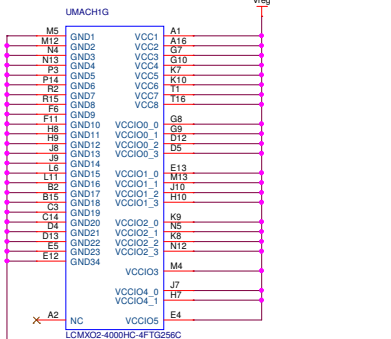
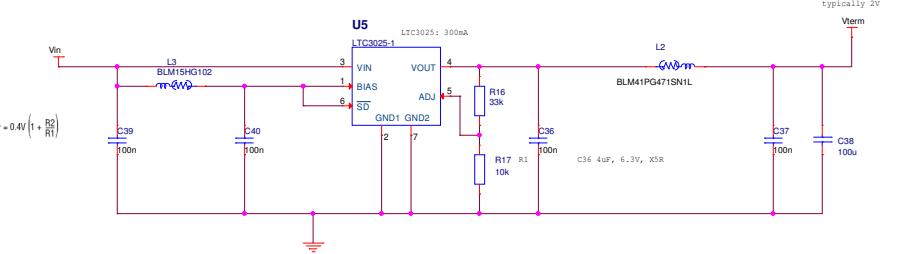
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Gesellschaft für Schichtenforschung mbH
Postfach 1
D-63229 Darmstadt
(GERMANY)
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Name: KOSMIN/DOMAR 140 105027401-K001 (AK937301) 03.02.2010
Autor: D. Sauerbrey, M. J. H. 14.01.10
Rev.: 1.00



R15 und R16 Werte ändern



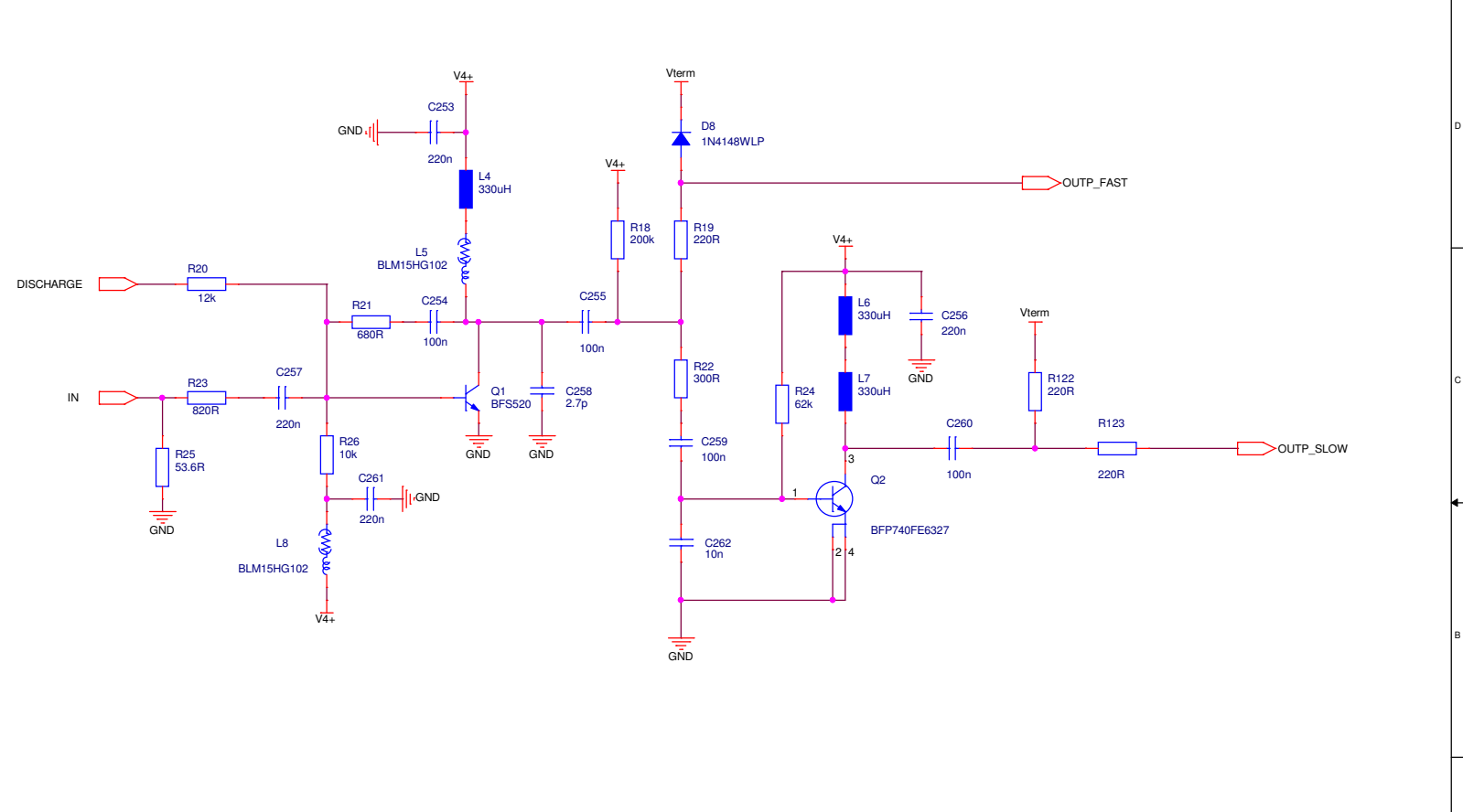
GSI

03_FPGA-Power

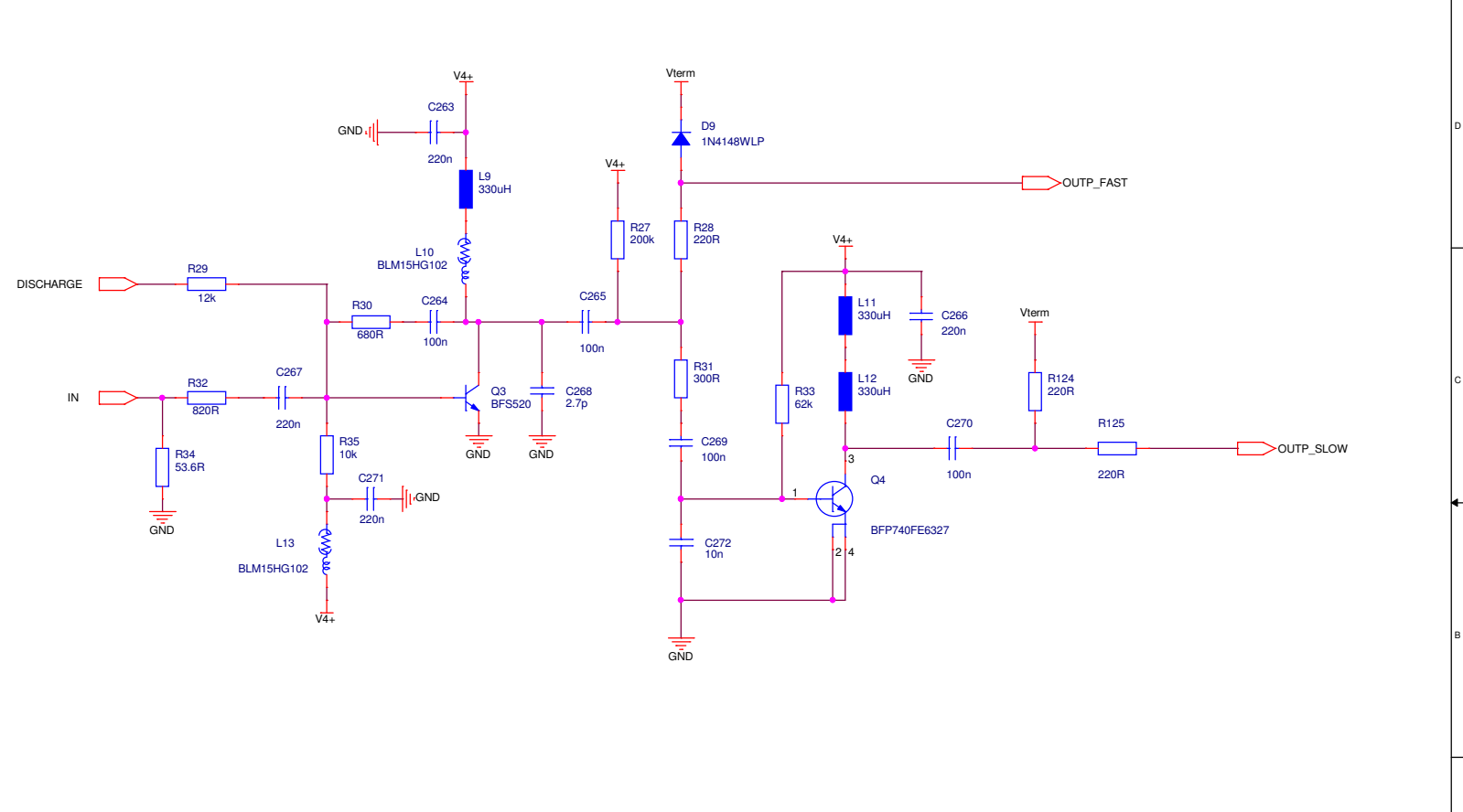
Gesellschaft für Schwerionenforschung mbH
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 GERMANY
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Design: KISSJOB/COME AND KISS/PANDA-DIRC/WASAPADIWA/AMPSTIPAL
 Modified: Wednesday, May 16, 2018
 Designer: B. Gröbe

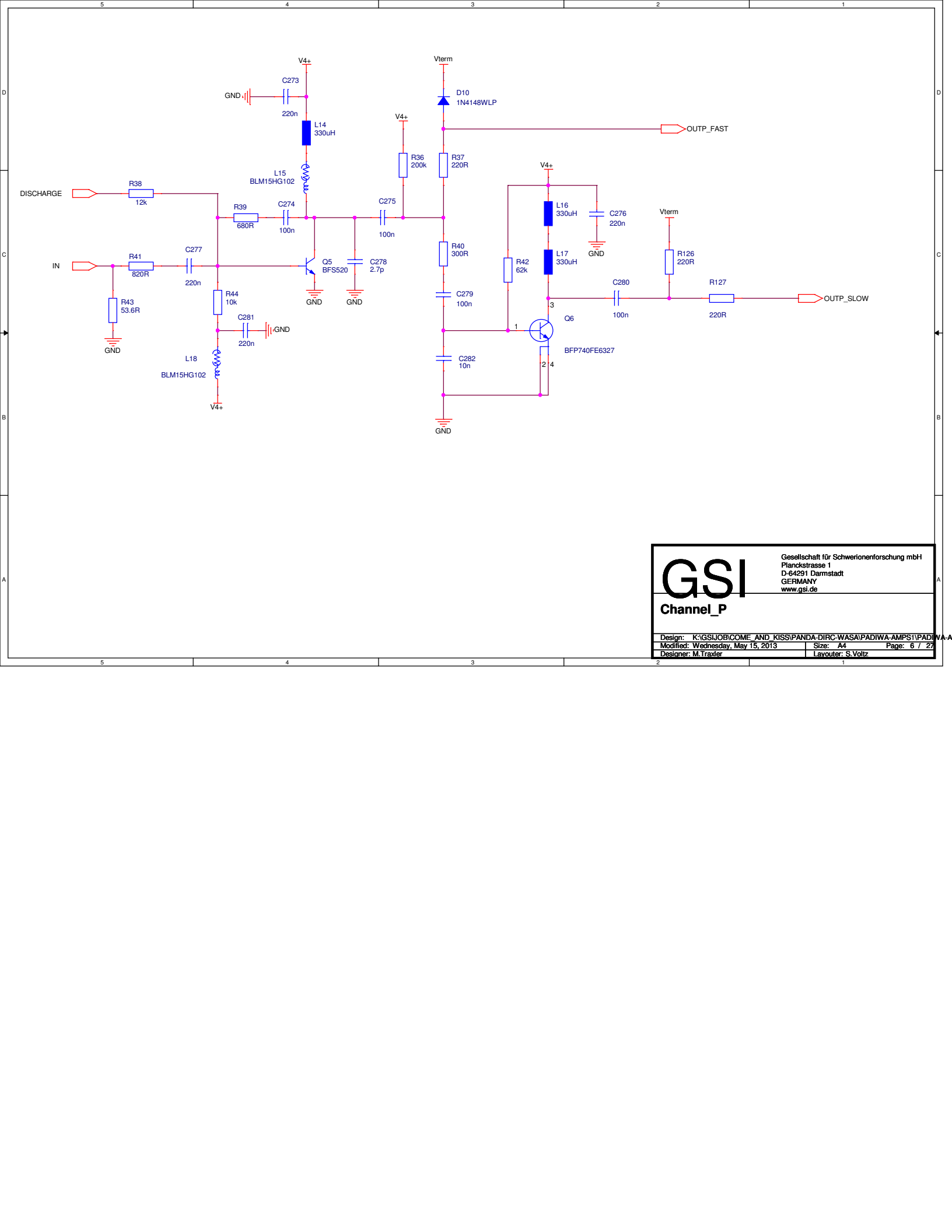
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 Ullrich & Voigt



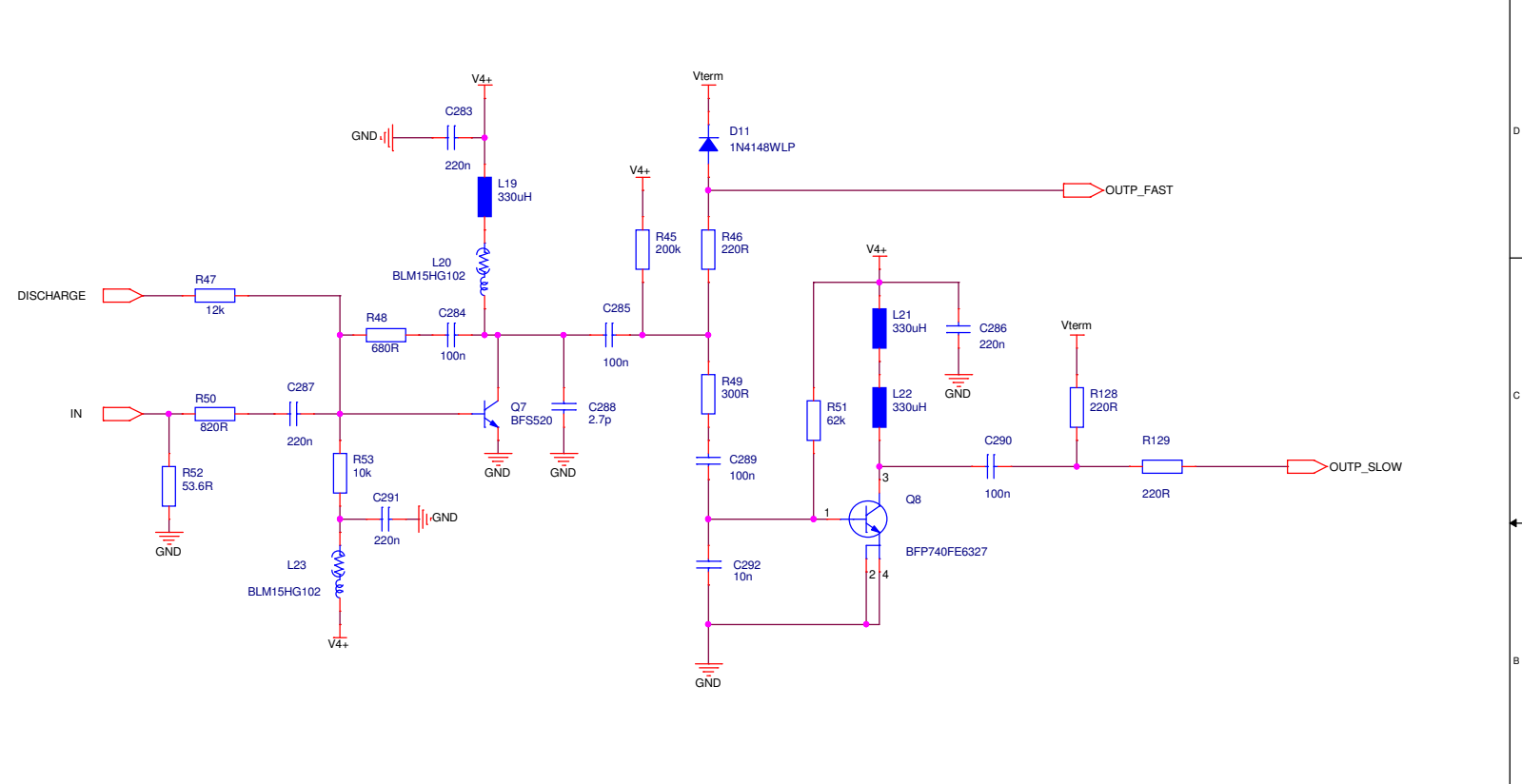
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Designer: M.Traxler		Layouter: S.Voltz



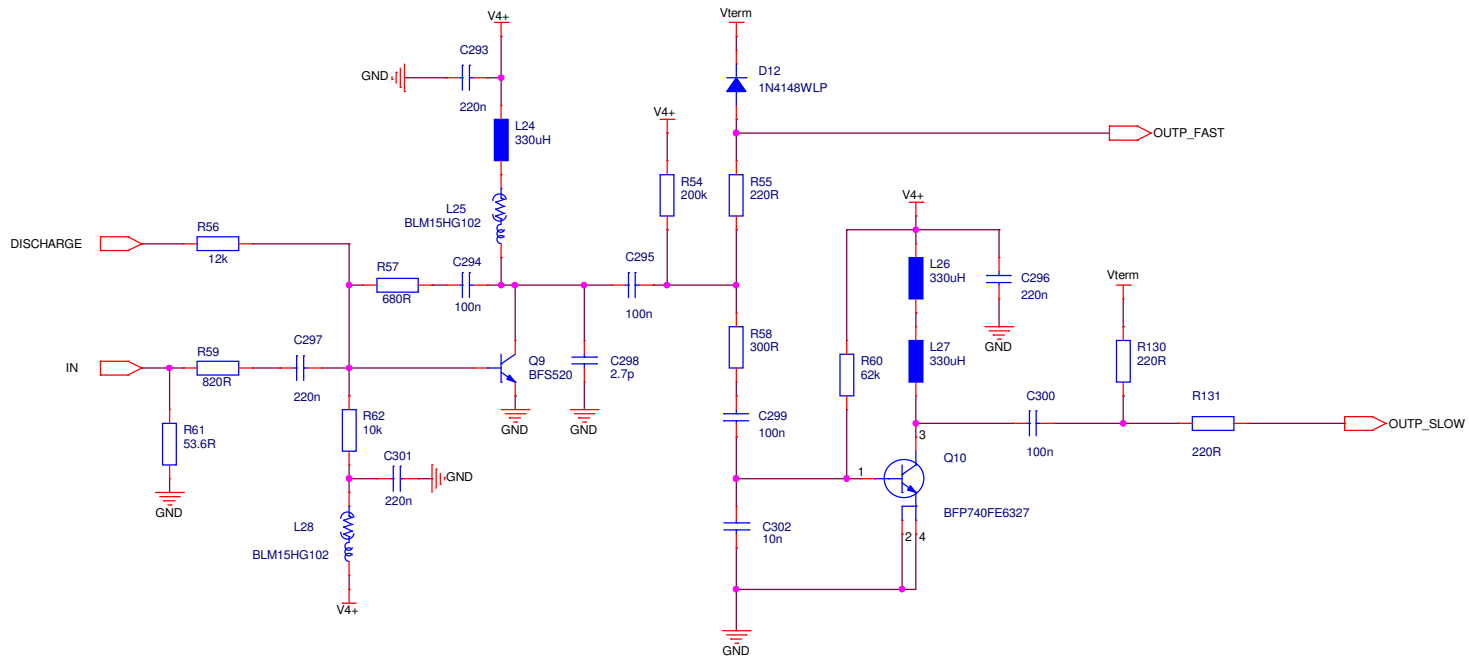
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Designer: M.Traxler	Layouter: S.Voltz	



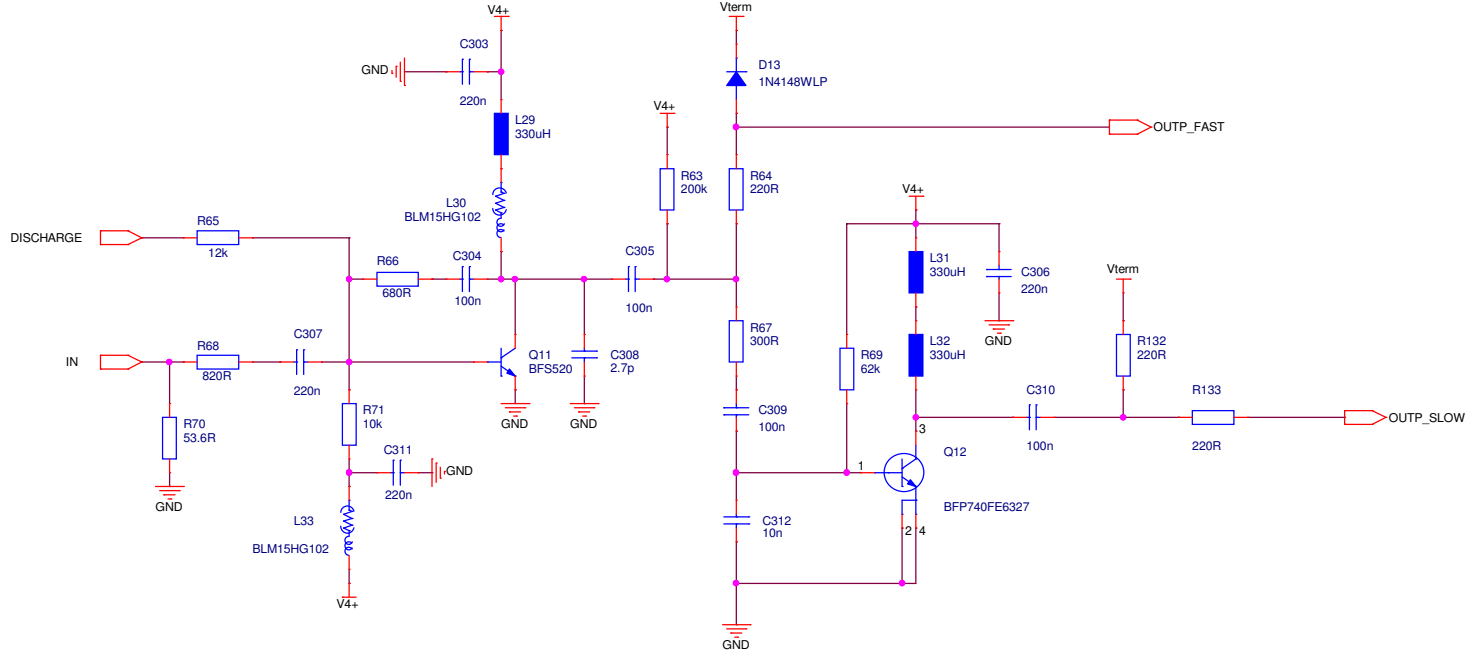
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Designer: M.Traxler	Layouter: S.Voltz	



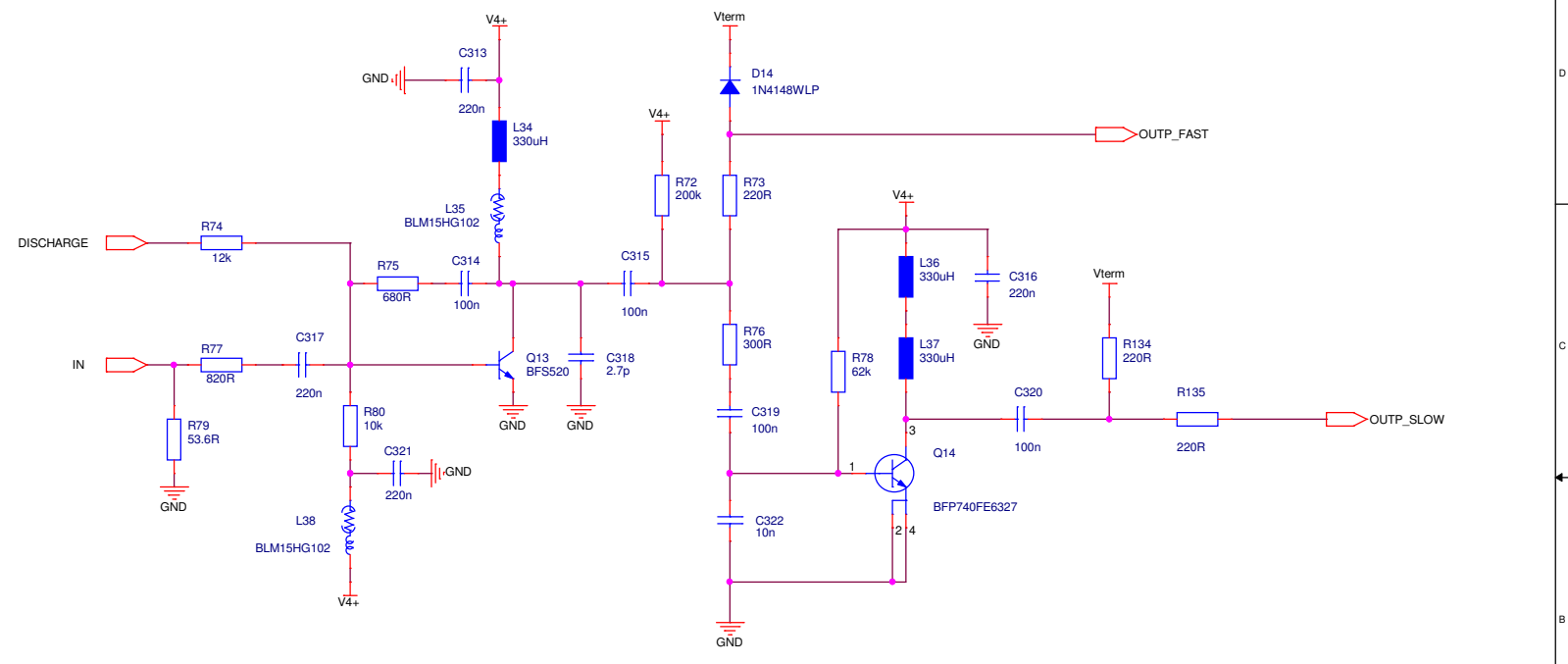
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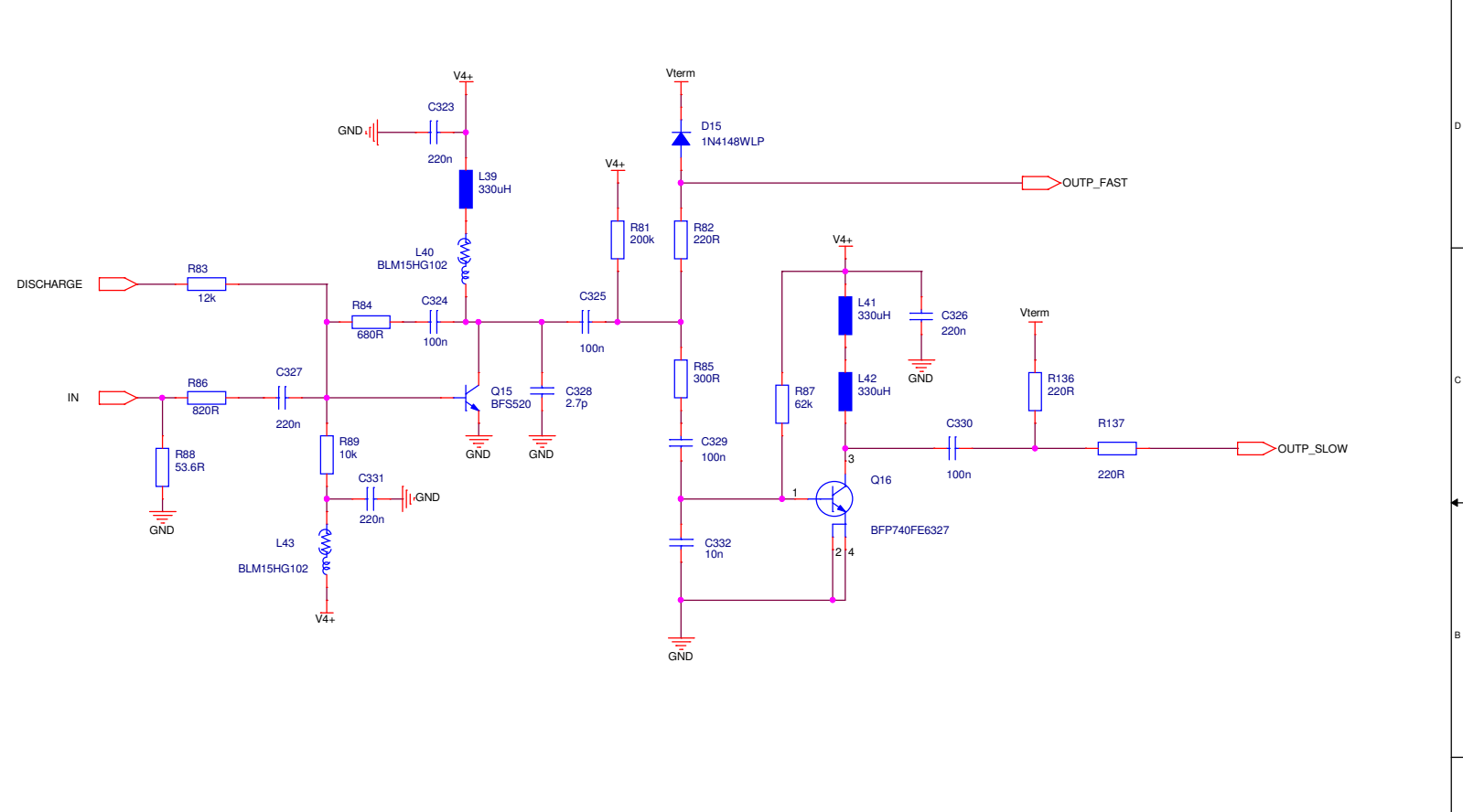
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Modified: Wednesday, May 15, 2013		Size: A4 Page: 8 / 27
Designer: M.Traxler		Layouter: S.Voltz



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Modified:	Wednesday, May 15, 2013	Size: A4
Designer:	M.Traxler	Layouter: S.Voltz

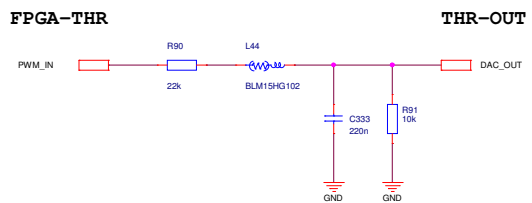


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Designer: M.Traxler		Layouter: S.Voltz



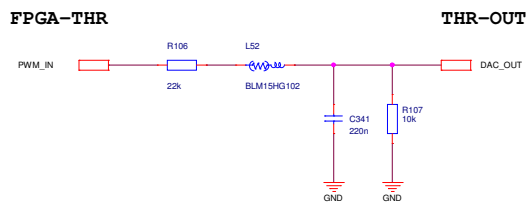
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Designer: M.Traxler		Layouter: S.Voltz

wire around FPGA, from output side to input side



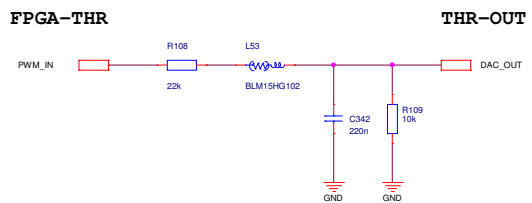
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	Design: KISSLOBCOME AND KISSPANDA-DIRC-WASAPADIWA-AMPS1IPAI Modified: Wednesday, May 16, 2018 Designer: B. Gröbe	Sheet: 01 Page: 127 of 22 Hübner & Vortz

wire around FPGA, from output side to input side



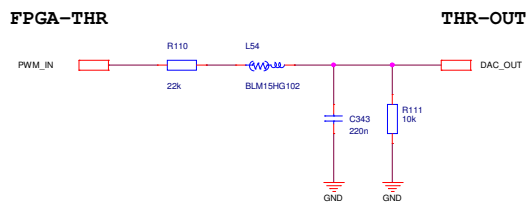
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wire around FPGA, from output side to input side

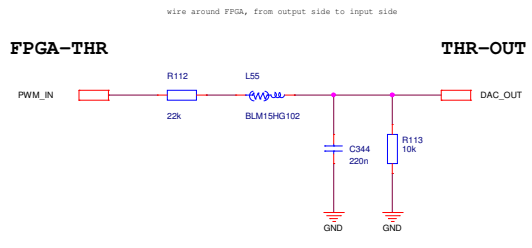


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wire around FPGA, from output side to input side

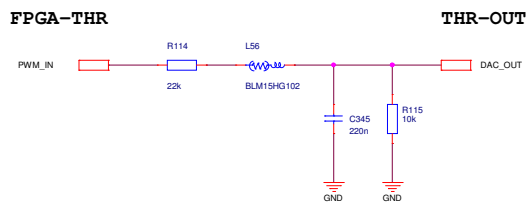


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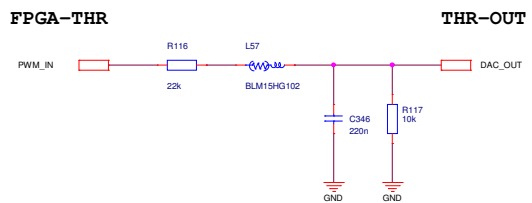
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wire around FPGA, from output side to input side



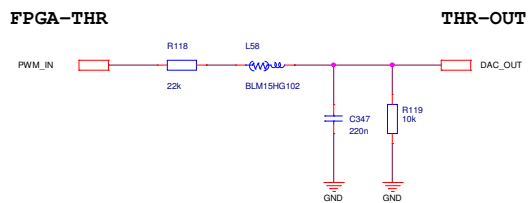
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wire around FPGA, from output side to input side

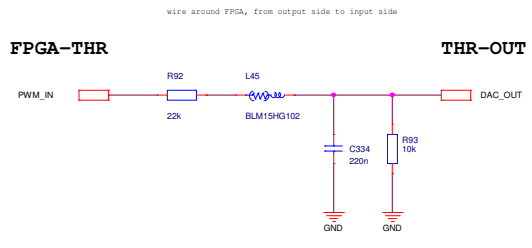


GSI DAC	Gesellschaft für Schwerionenforschung mbH Planckstrasse 1 D-64291 Darmstadt GERMANY www.gsi.de	
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wire around FPGA, from output side to input side

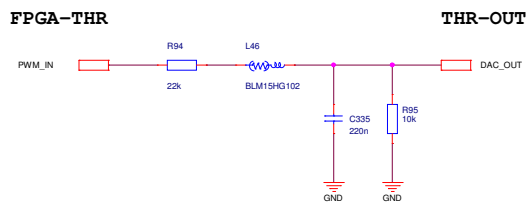


GSI DAC	Gesellschaft für Schwerionenforschung mbH Planckstrasse 1 D-64291 Darmstadt GERMANY www.gsi.de	
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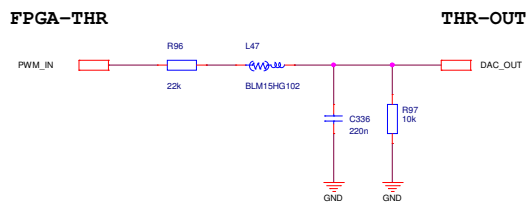
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DAC		
Design: KISSLOBCOME AND KISSPANDA-DIRC-WASAPADIWA-AMPSTIPAI	Sheet: 01	Page: 20 / 22
Modified: Wednesday, May 16, 2018	Designer: B. Gröbe	Reviewer: S. Vortz

wire around FPGA, from output side to input side



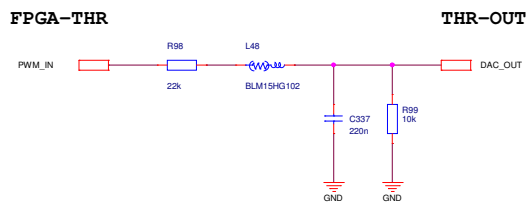
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wire around FPGA, from output side to input side



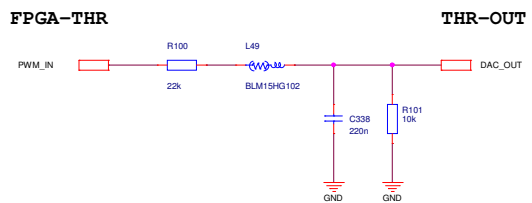
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wire around FPGA, from output side to input side



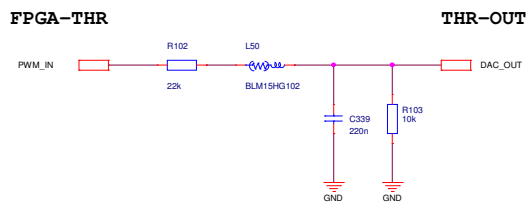
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Modified: Wednesday, May 16, 2018	Author: A3	Page: 2/2
Designer: B. Gröbe	Reviewer: A. Voltz	

wire around FPGA, from output side to input side



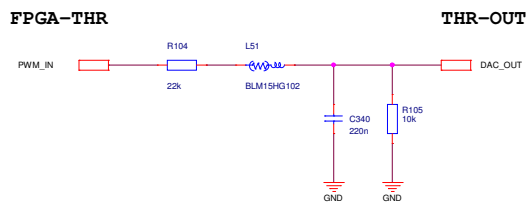
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wire around FPGA, from output side to input side



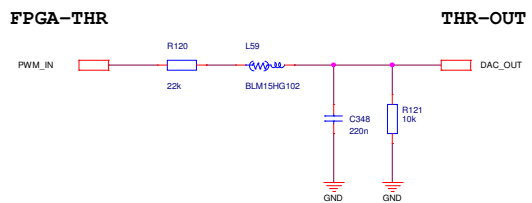
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wire around FPGA, from output side to input side



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	Design: KISSLOBOCOME AND KISSPANDA-DIRC-WASAPADIWA-AMPSTIPAI	Sheet: 01
Modified: Wednesday, May 16, 2018	Author: A. Völz	Page: 26 / 27
Designer: B. Völz		

wire around FPGA, from output side to input side



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Modified: Wednesday, May 16, 2018	Author: A. Völz	Page: 27 / 28
Designer: B. Gröbe		