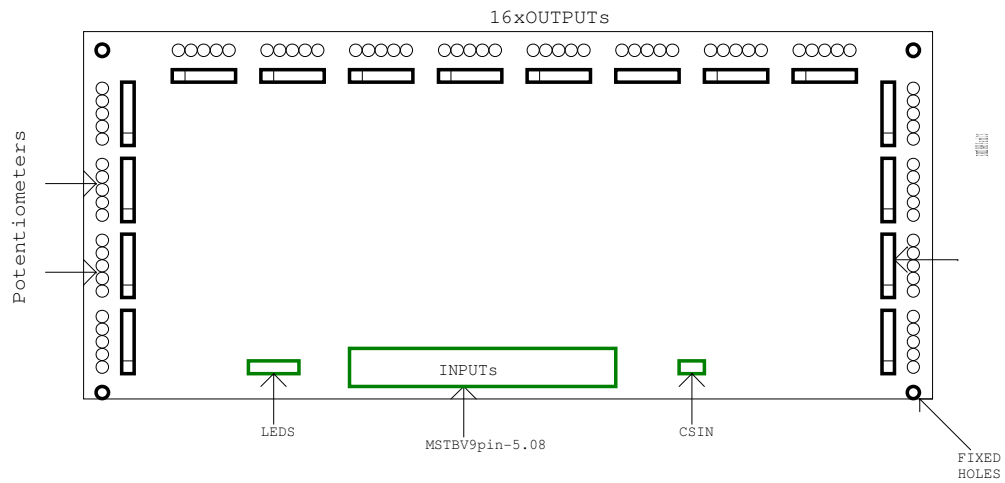


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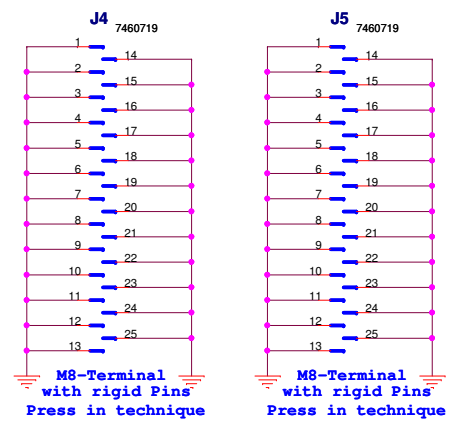
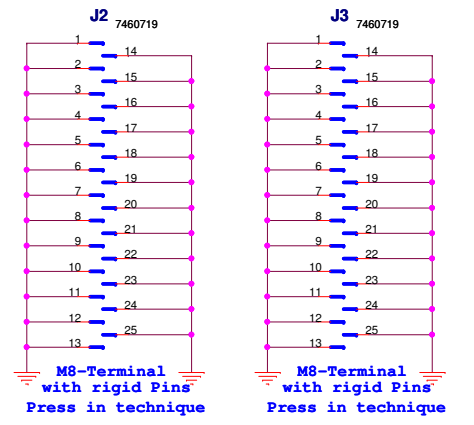
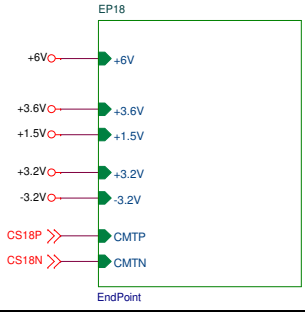
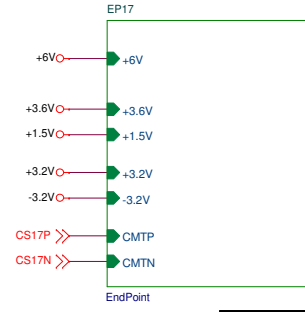
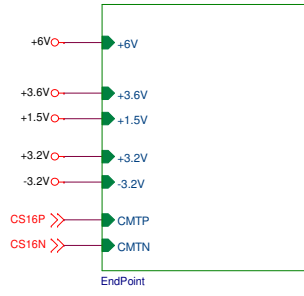
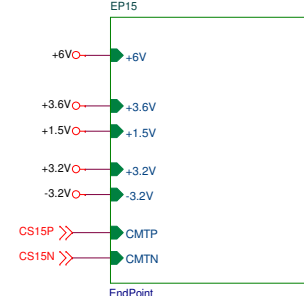
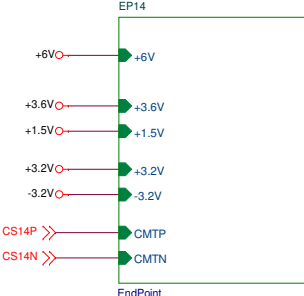
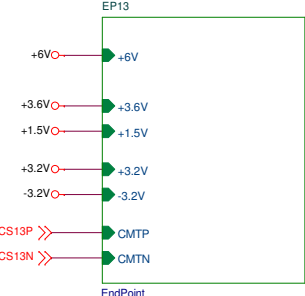
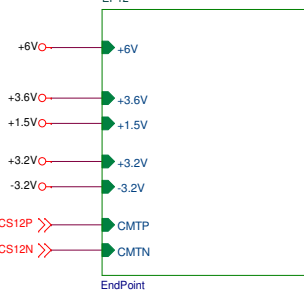
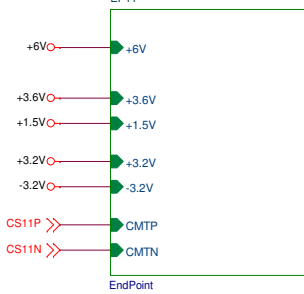
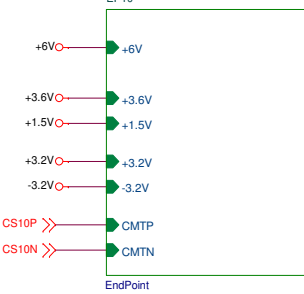
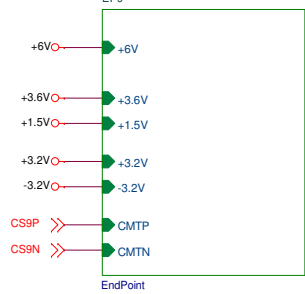
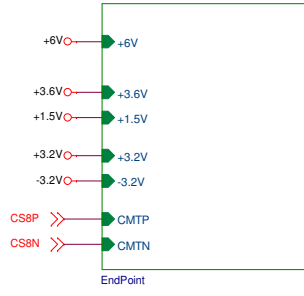
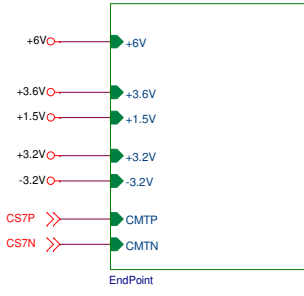
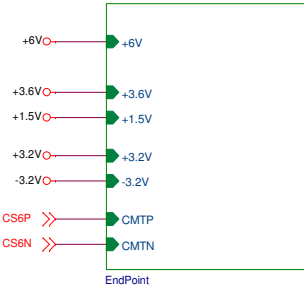
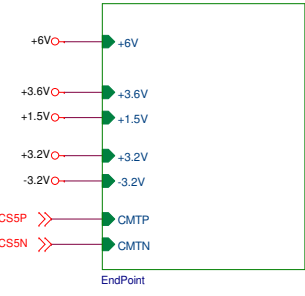
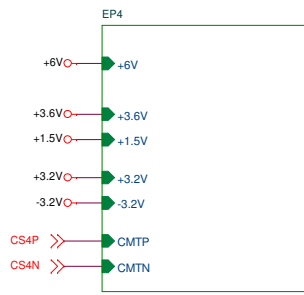
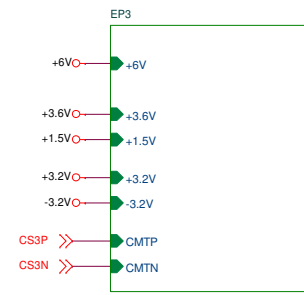
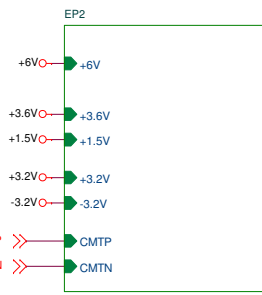
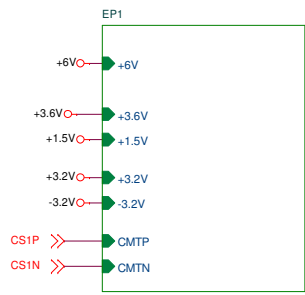
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Modified: Wednesday, October 14, 2009	Designer: <Designer>	Layoutler: <Layoutler>



Suggest 8 layers, top for +5V, bottom for GND, m1 for CST, other for each PW out!

Layout

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	<Title>		
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18xConnectors for 18xOEP

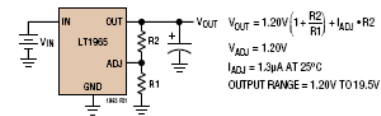
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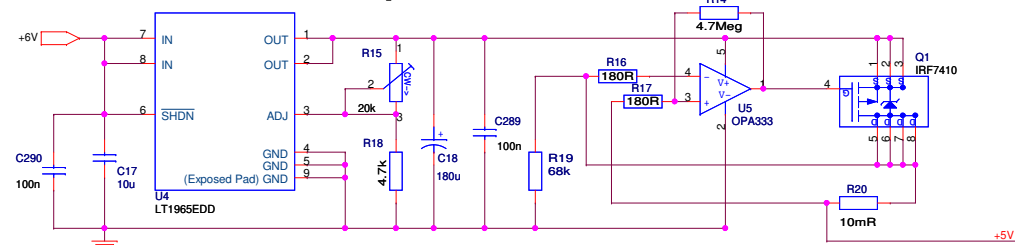
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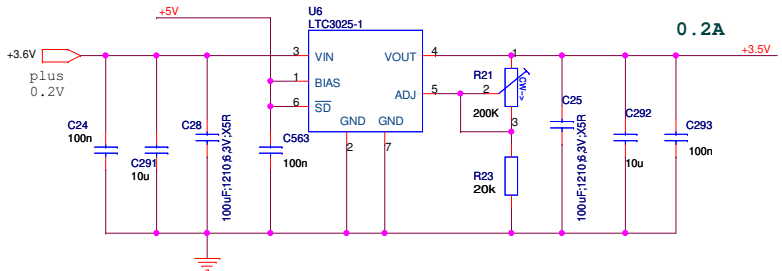


+5.0V/1.1A

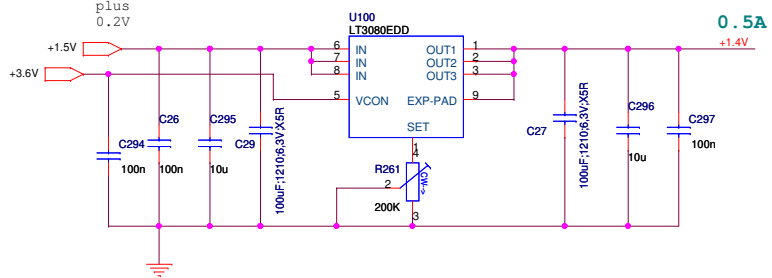


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

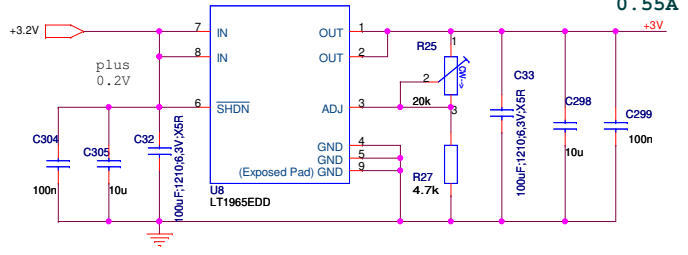
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A

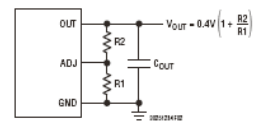
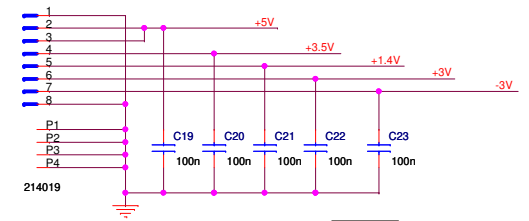


CSOUT1

+5V Current 0.95A limiter

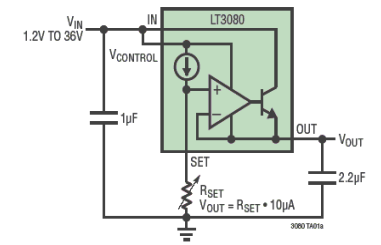
The PW inputs should be higher than the marked inputs about 0.2V

POW1

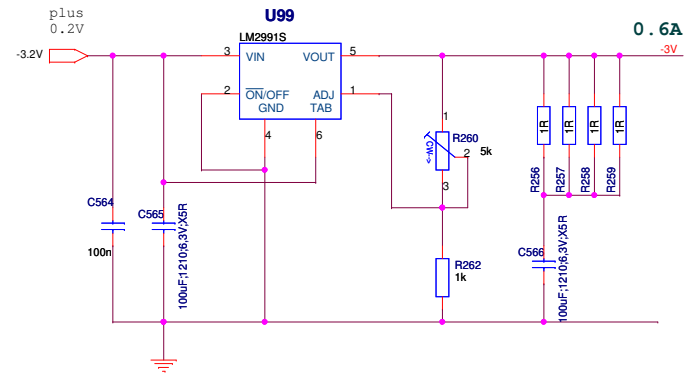


LTC3025-1

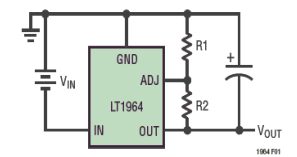
Variable Output Voltage 1.1A Supply



-3V/1A



0.6A



$V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA AT 25°C$
 OUTPUT RANGE = -1.22V TO -20V

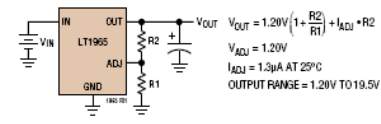
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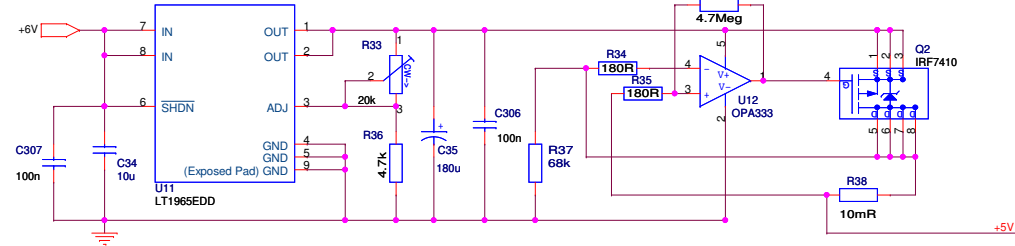
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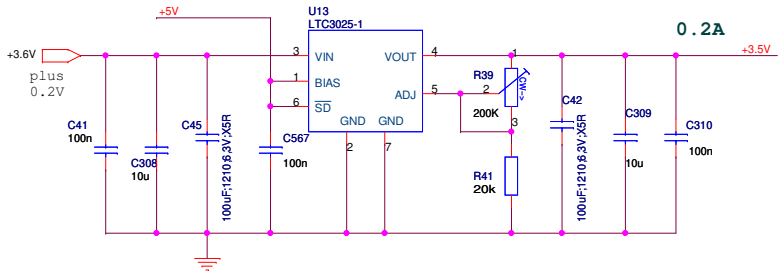


+5.0V/1.1A

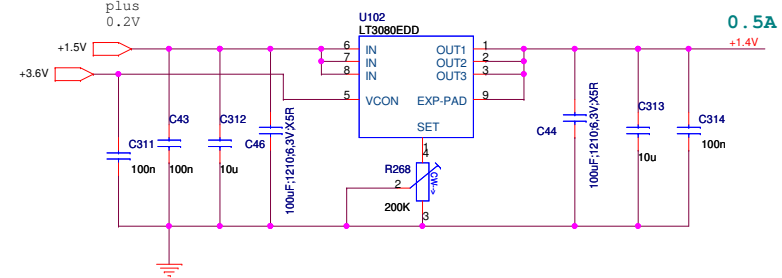


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

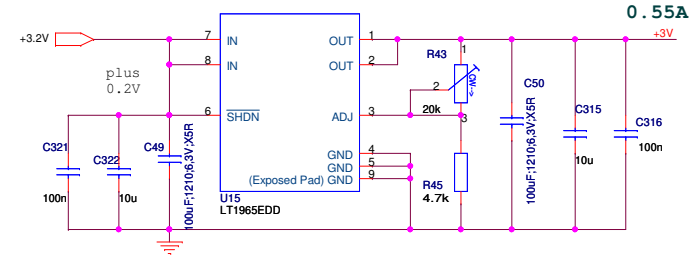
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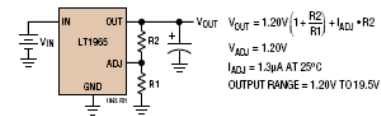


+1.4V/1.15A

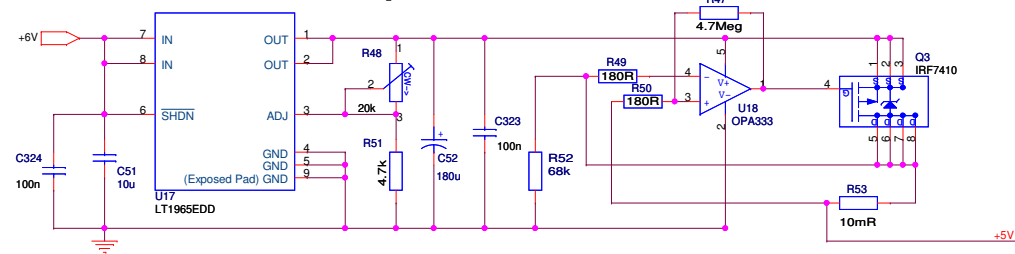


+3V/1.1A



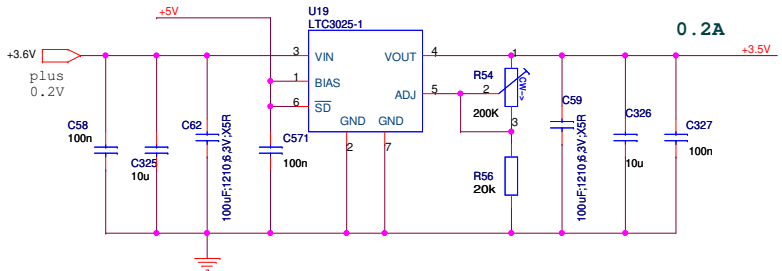


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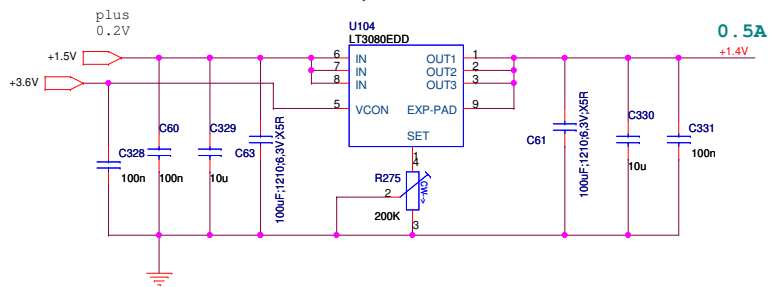


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

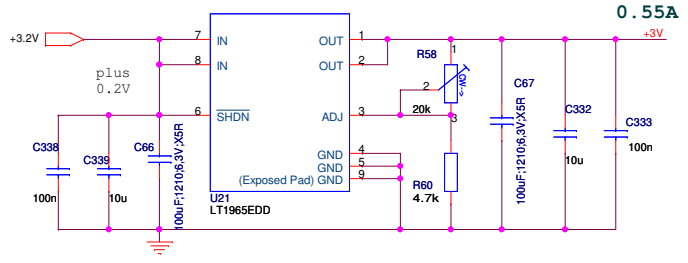
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



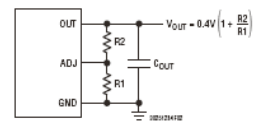
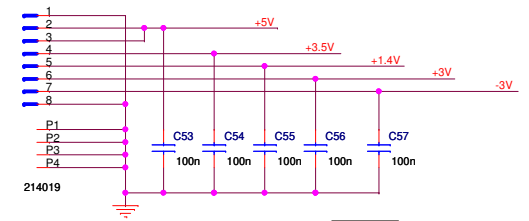
CSOUT3



+5V Current 0.95A limiter

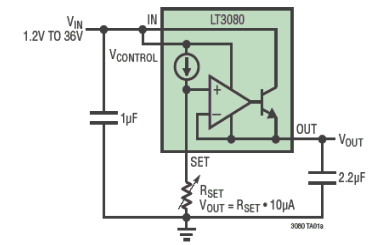
The PW inputs should be higher than the marked inputs about 0.2V

POW4

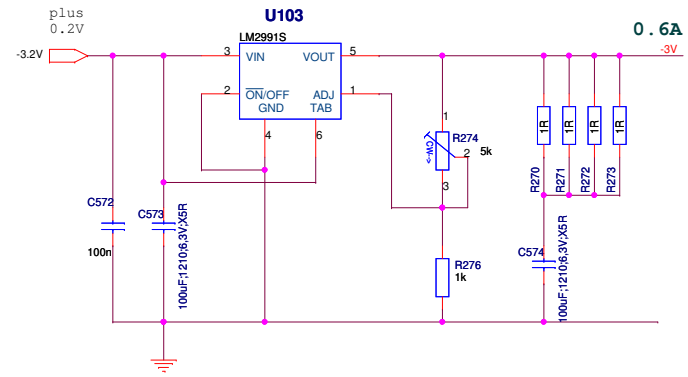


LTC3025-1

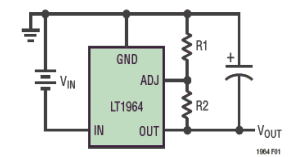
Variable Output Voltage 1.1A Supply



-3V/1A



0.6A



$$V_{OUT} = -1.22V \left(1 + \frac{R2}{R1}\right) - (I_{ADJ}) \cdot R2$$

$$V_{ADJ} = -1.22V$$

$$I_{ADJ} = 30nA \text{ AT } 25^\circ C$$

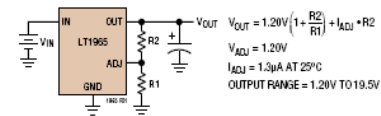
$$\text{OUTPUT RANGE} = -1.22V \text{ TO } -20V$$

GSI

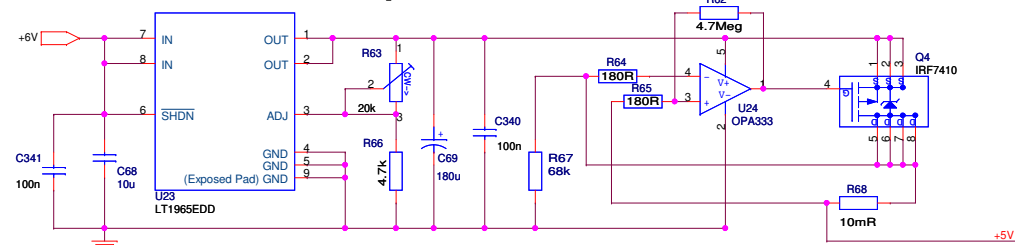
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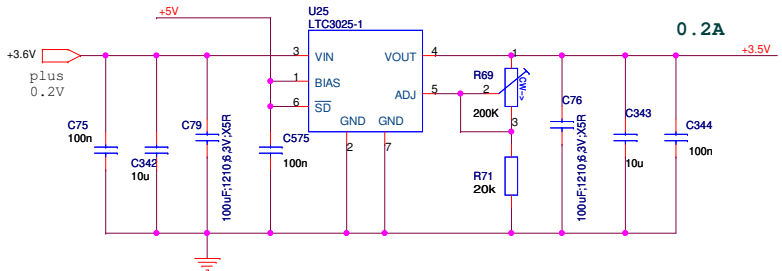


+5.0V/1.1A

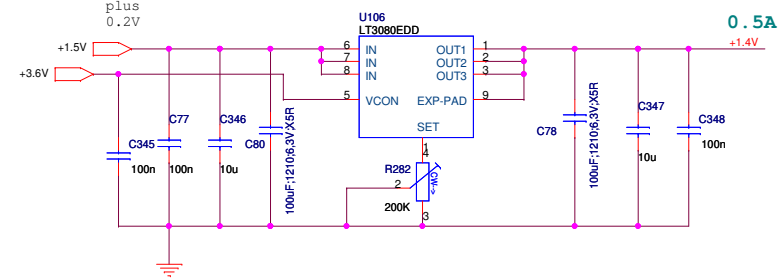


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

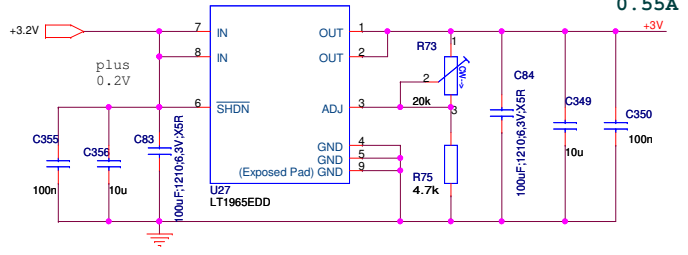
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



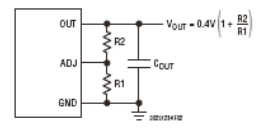
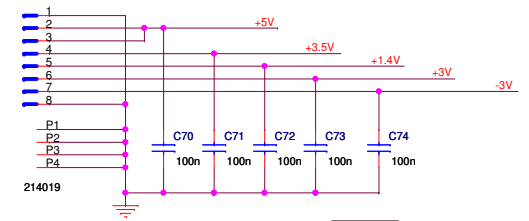
CSOUT4



+5V Current limiter

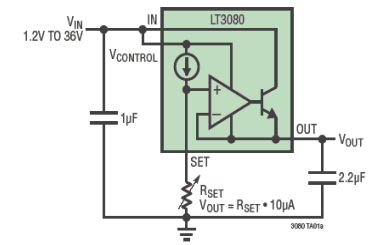
The PW inputs should be higher than the marked inputs about 0.2V

POW5

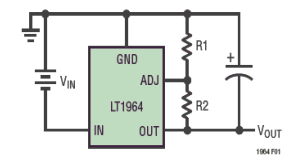
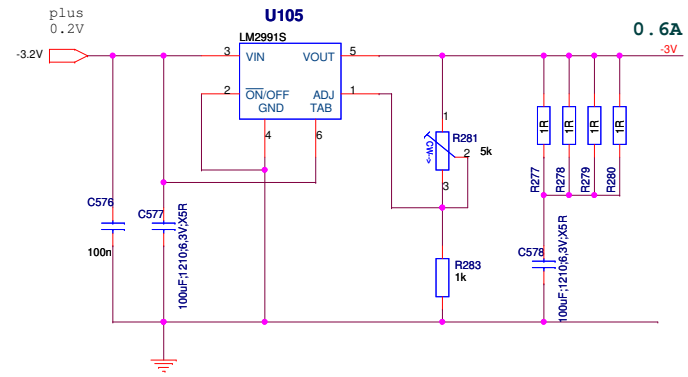


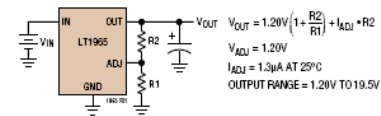
LTC3025-1

Variable Output Voltage 1.1A Supply

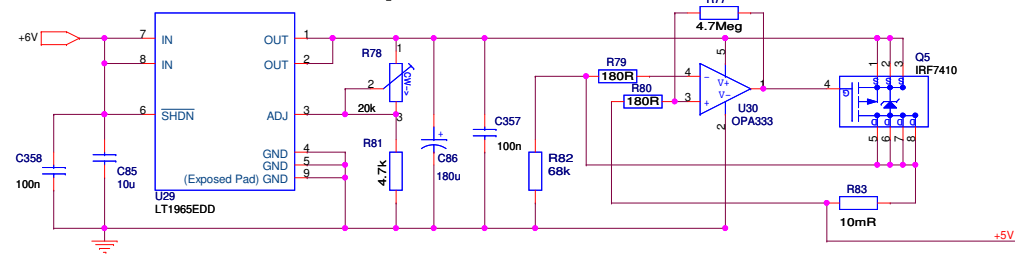


-3V/1A



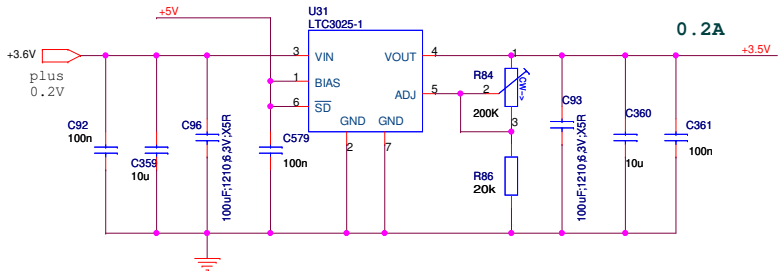


+5.0V/1.1A

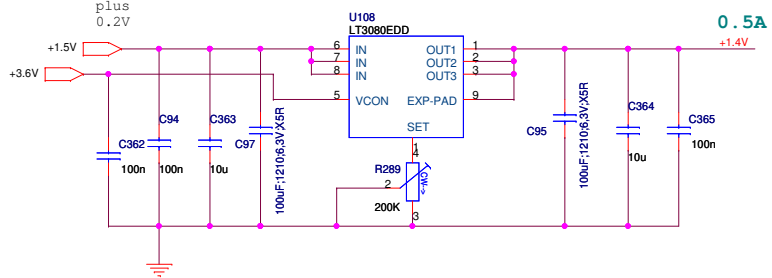


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

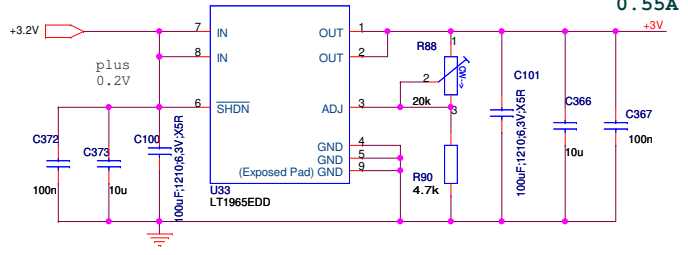
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



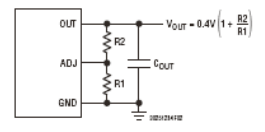
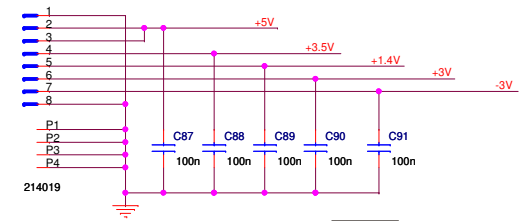
CSOUTS



+5V Current 0.95A limiter

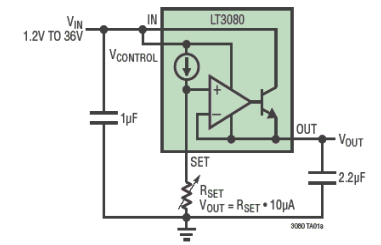
The PW inputs should be higher than the marked inputs about 0.2V

POW6

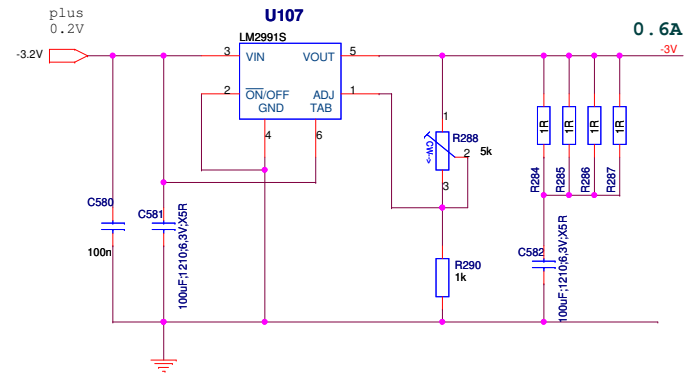


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



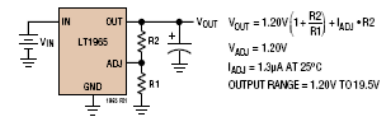
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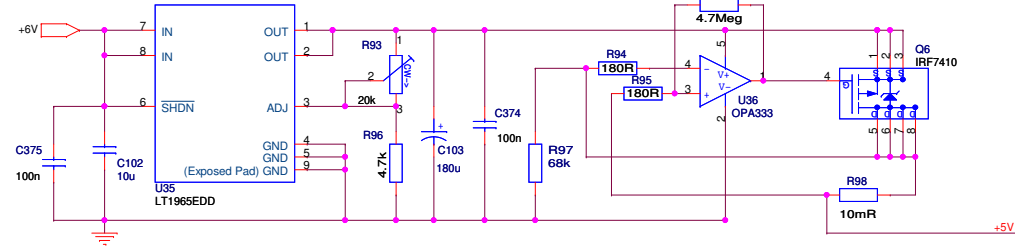
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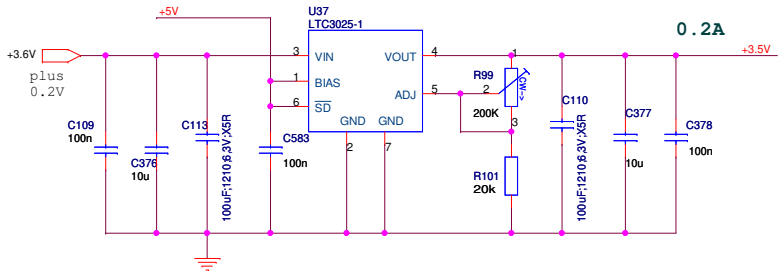


+5.0V/1.1A

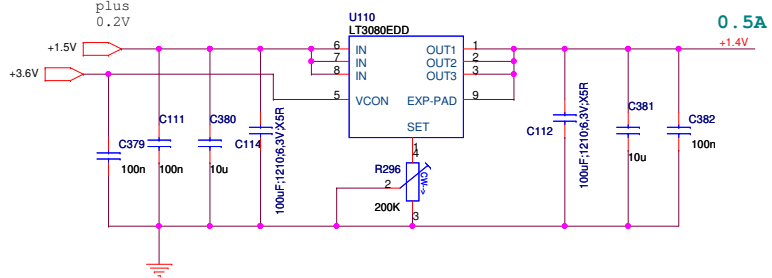


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

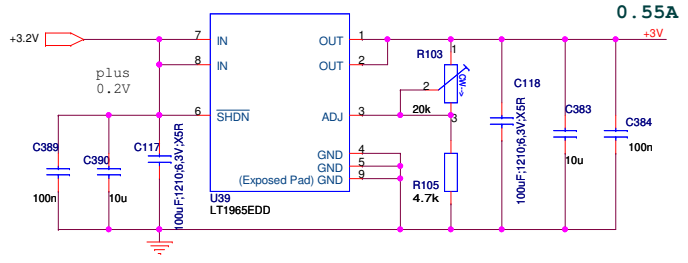
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



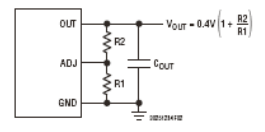
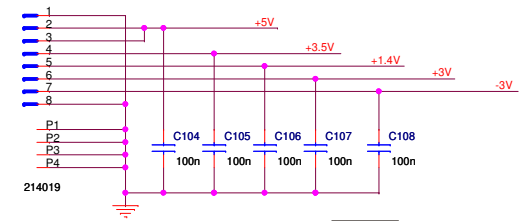
CSOUT6



+5V Current limiter

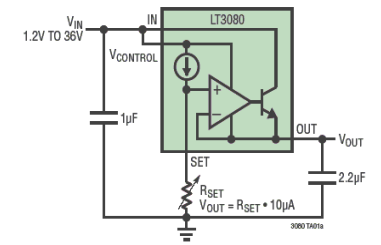
The PW inputs should be higher than the marked inputs about 0.2V

POW7

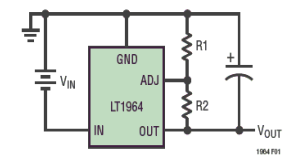
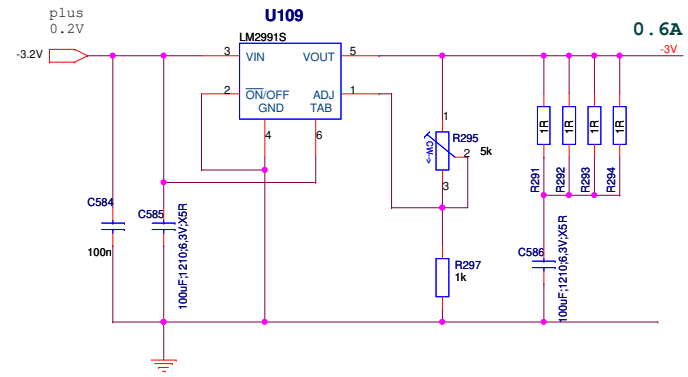


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



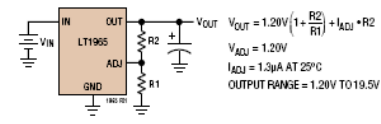
LT1964
 $V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA \text{ AT } 25^\circ C$
 OUTPUT RANGE = -1.22V TO -20V

GSI

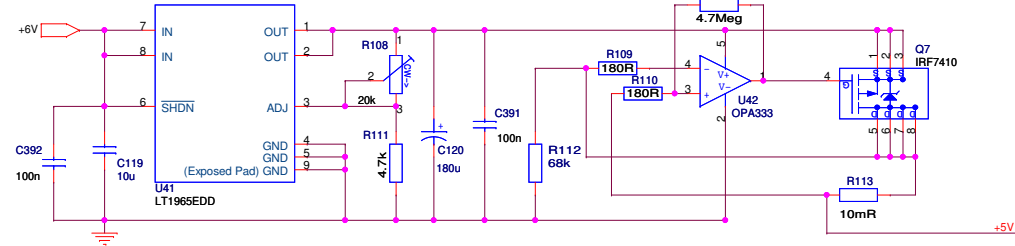
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Design: K1GSLIOBHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN
 Modified: Thursday, October 01, 2009 Size: A3 Page: 9 / 21
 Designer: <Designer> Layouter: <Layouter>

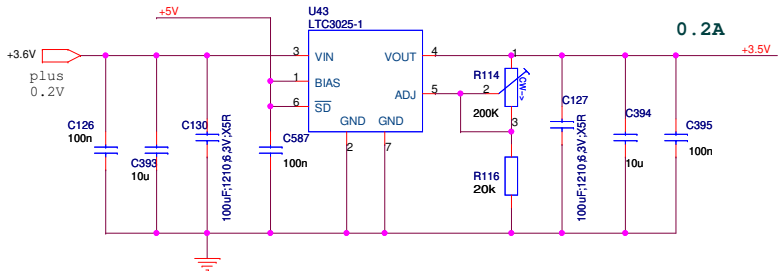


+5.0V/1.1A

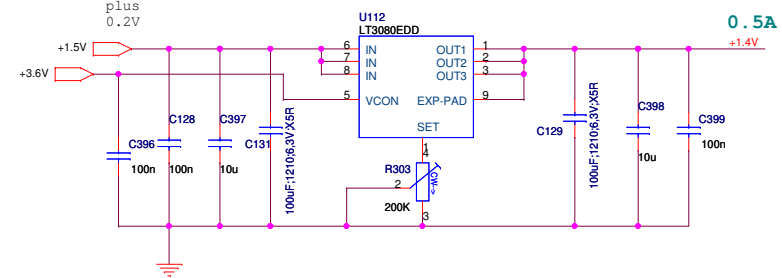


Since OEP uses LTC3025 for +1V2,+1V and +3V3,Here use same LT3025 for all

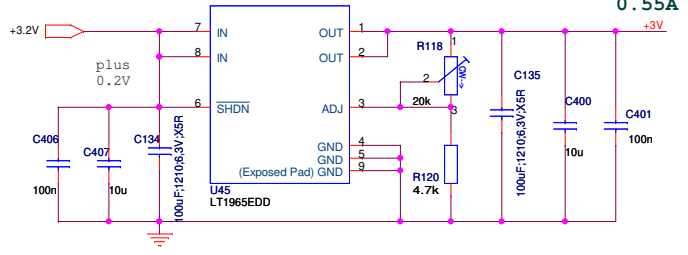
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



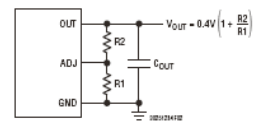
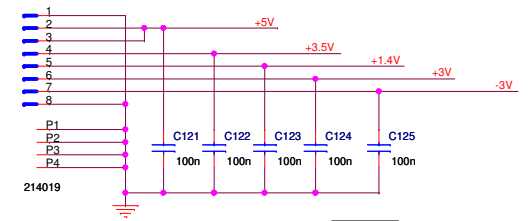
CSOUT7



+5V Current 0.95A limiter

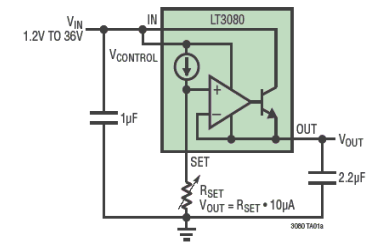
The PW inputs should be higher than the marked inputs about 0.2V

POW8

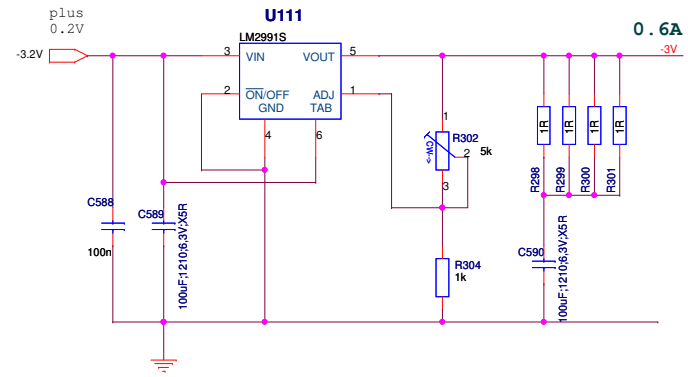


LTC3025-1

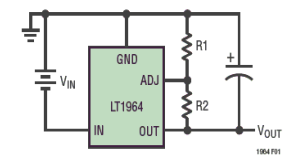
Variable Output Voltage 1.1A Supply



-3V/1A



0.6A



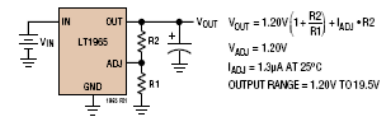
$V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA$ AT 25°C
 OUTPUT RANGE = -1.22V TO -20V

GSI

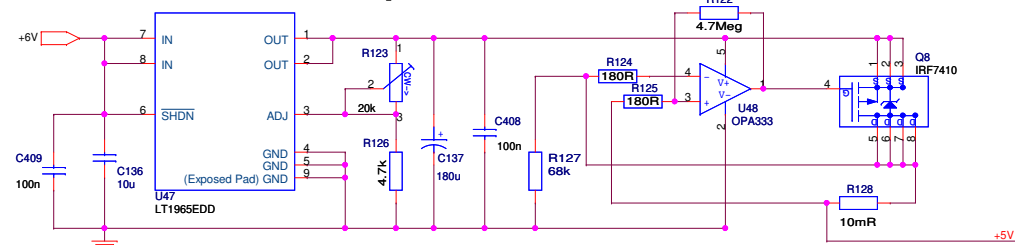
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN	Size: A3	Page: 10/ 21
Modified: Thursday, October 01, 2009	Designer: <Designer>	Layouter: <Layouter>

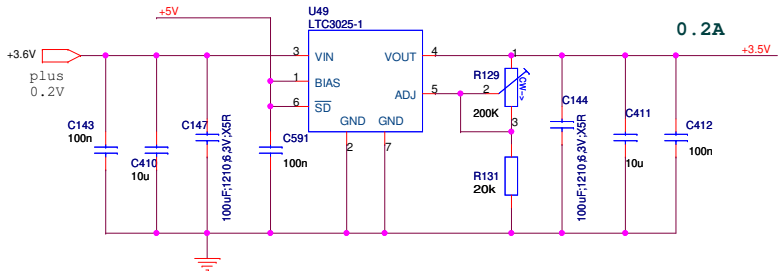


+5.0V/1.1A

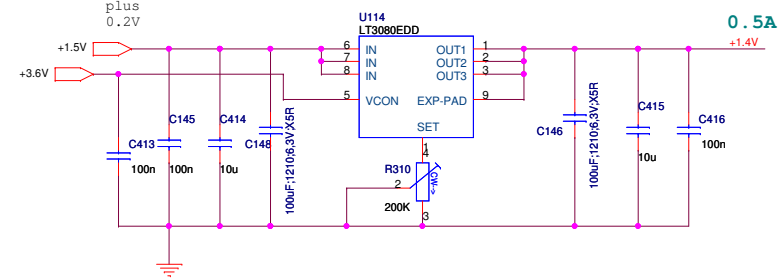


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

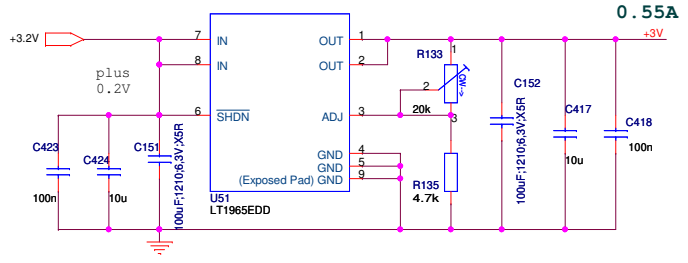
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



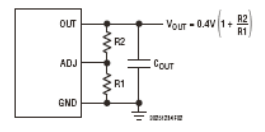
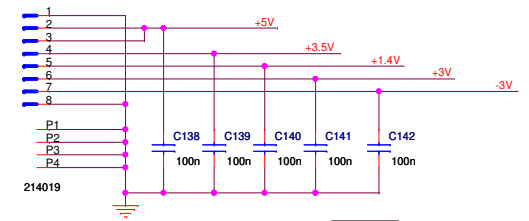
CSOUT8



+5V Current limiter 0.95A

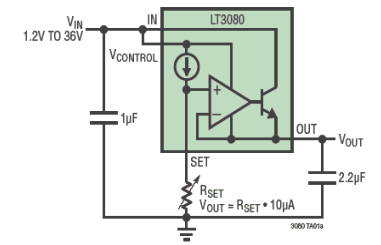
The PW inputs should be higher than the marked inputs about 0.2V

POW9

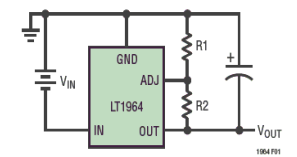
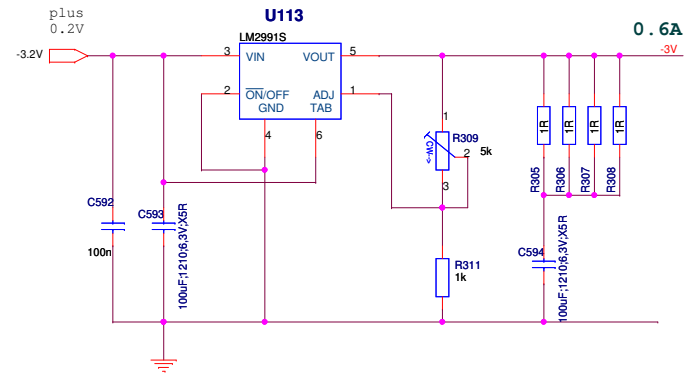


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A

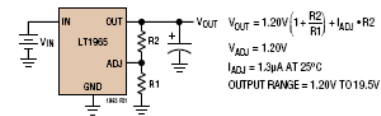


$V_{OUT} = -1.22V \left(1 + \frac{R2}{R1}\right) - (I_{ADJ}) \cdot R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA$ AT 25°C
 OUTPUT RANGE = -1.22V TO -20V

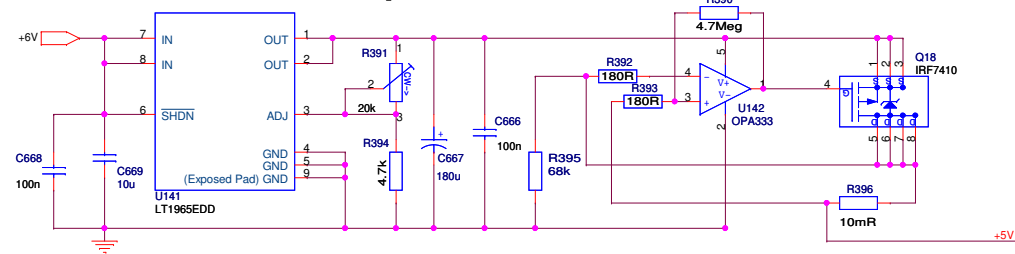
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 Modified: Thursday, October 01, 2009 Size: A3 Page: 11 / 21
 Designer: <Designer> Layouter: <Layouter>

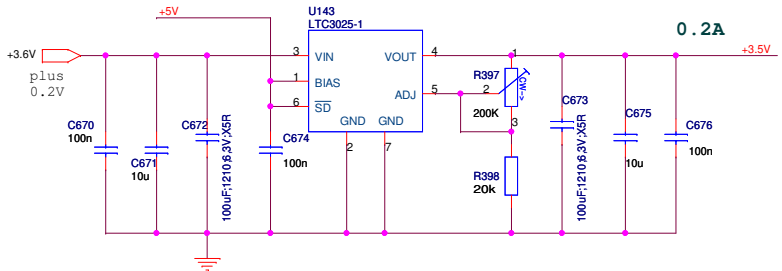


+5.0V/1.1A

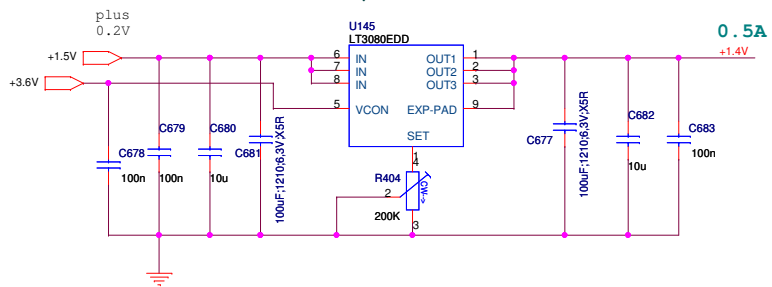


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

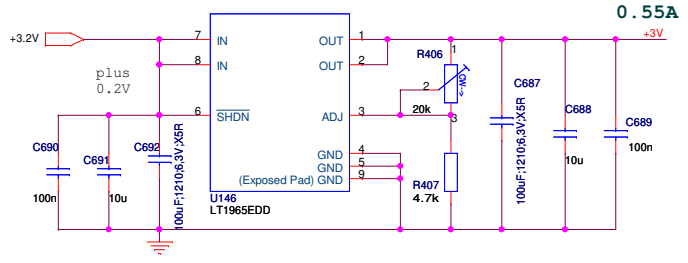
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



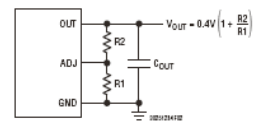
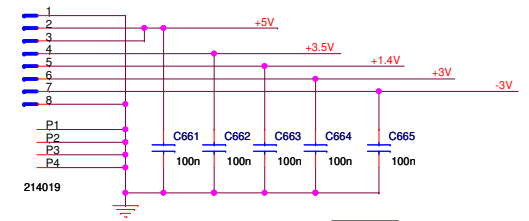
CSOUT17



+5V Current limiter

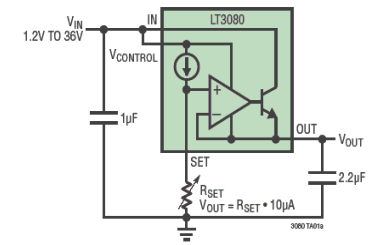
The PW inputs should be higher than the marked inputs about 0.2V

POW17

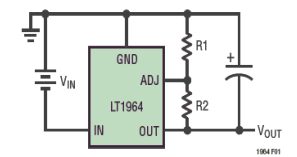
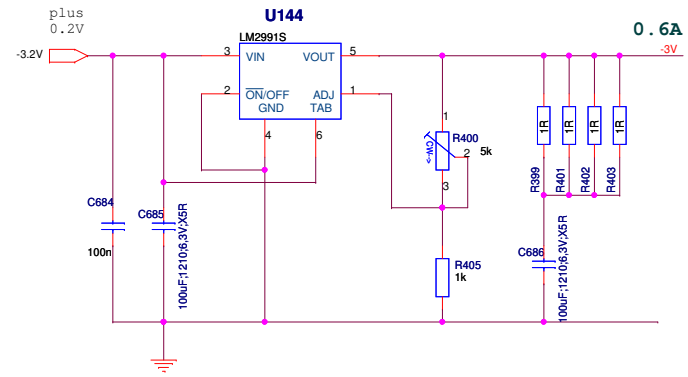


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



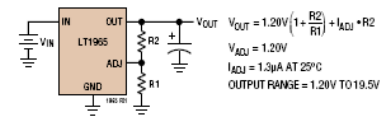
$V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})(R2)$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA AT 25°C$
 OUTPUT RANGE = -1.22V TO -20V

GSI

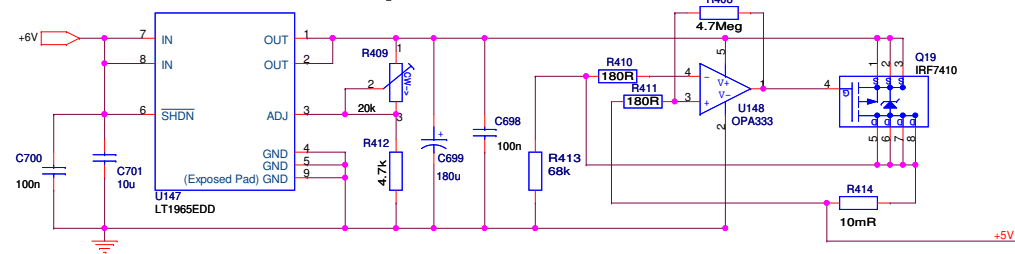
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWIMDC-FAN-PW3\FAN-PW3.DSN	Size: A3
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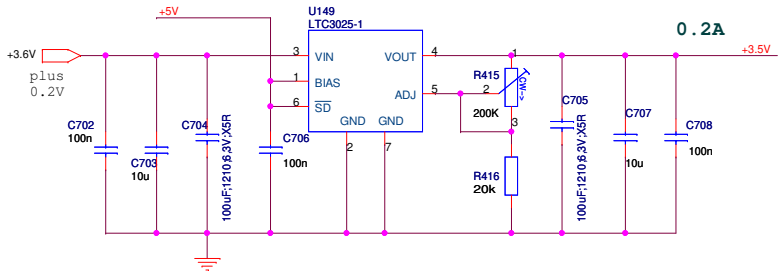


+5.0V/1.1A

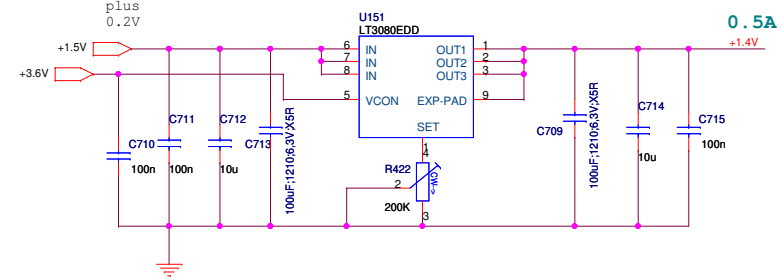


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

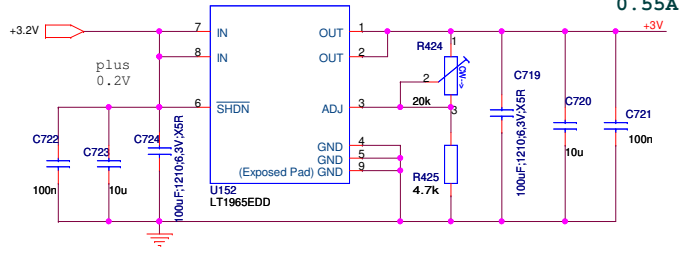
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



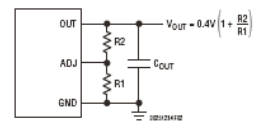
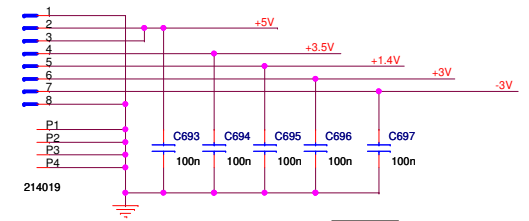
CSOUT18



+5V Current 0.95A limiter

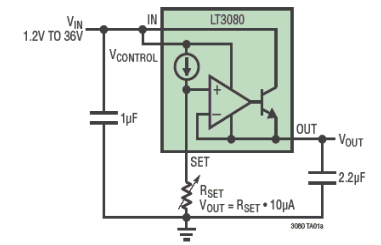
The PW inputs should be higher than the marked inputs about 0.2V

POW18

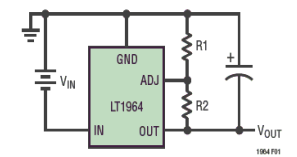
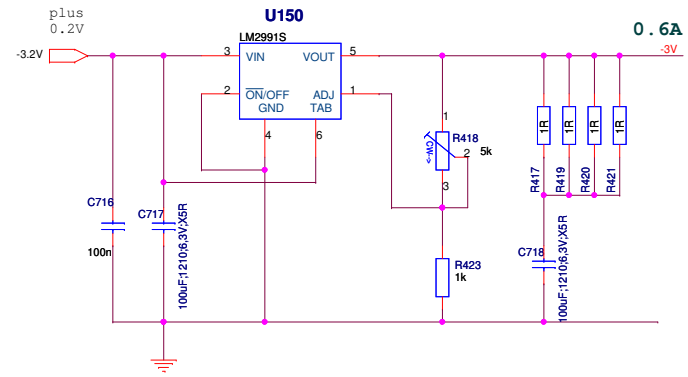


LTC3025-1

Variable Output Voltage 1.1A Supply



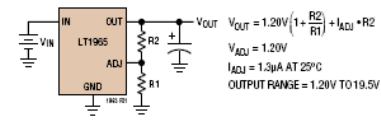
-3V/1A



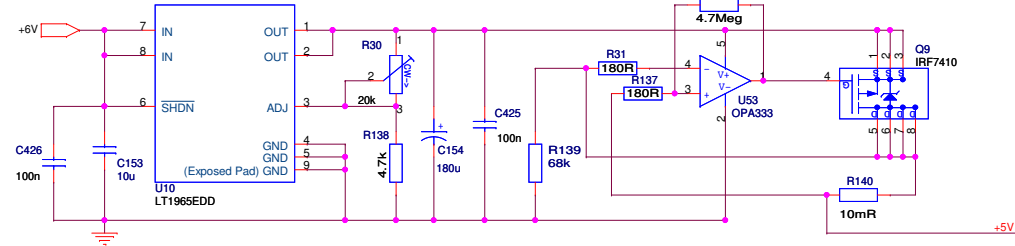
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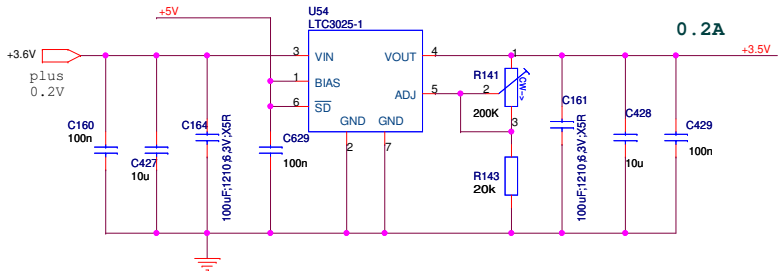


+5.0V/1.1A

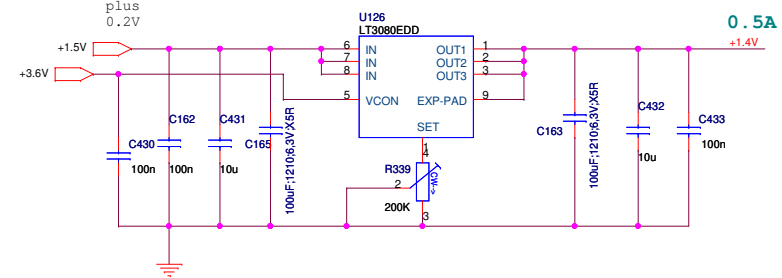


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

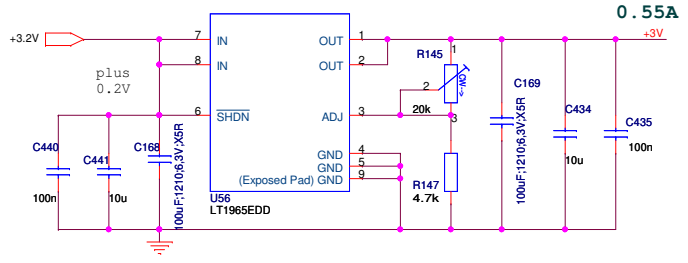
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



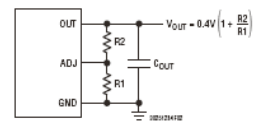
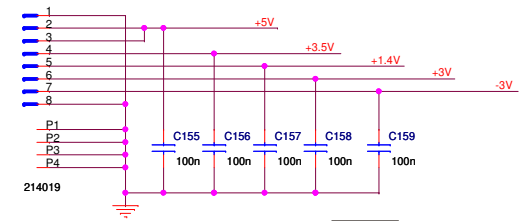
CSOUT9



+5V Current 0.95A limiter

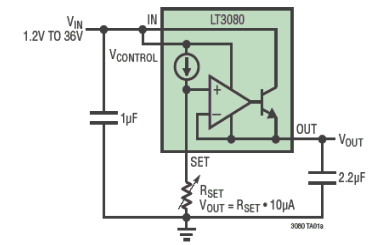
The PW inputs should be higher than the marked inputs about 0.2V

POW2

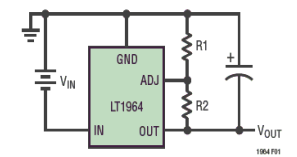
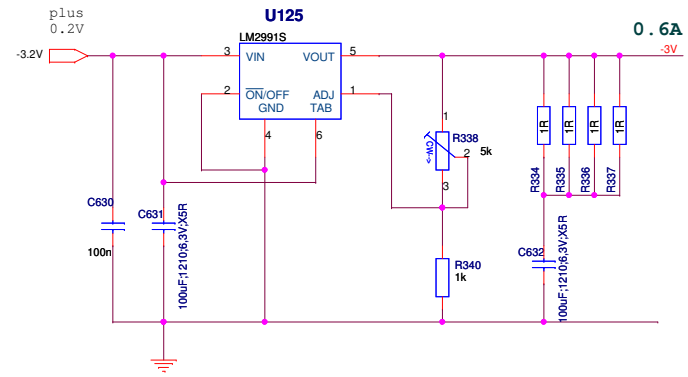


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



$$V_{OUT} = -1.22V \left(1 + \frac{R2}{R1}\right) - (I_{ADJ}) \cdot R2$$

$$V_{ADJ} = -1.22V$$

$$I_{ADJ} = 30nA \text{ AT } 25^\circ C$$

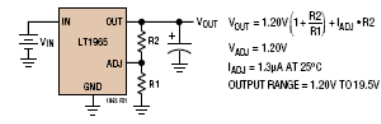
$$\text{OUTPUT RANGE} = -1.22V \text{ TO } -20V$$

GSI

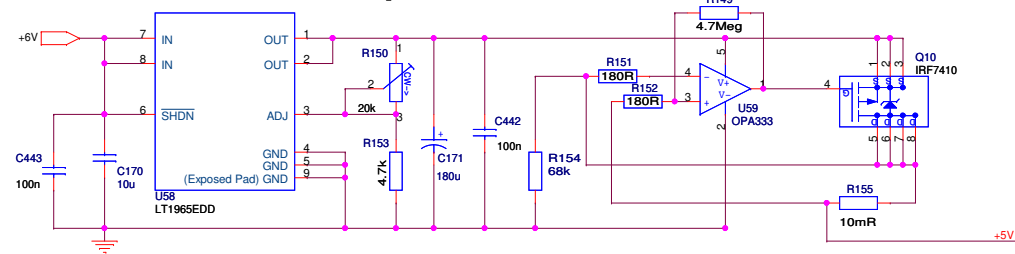
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN	Size: A3
Modified: Thursday, October 01, 2009	Page: 14 / 21
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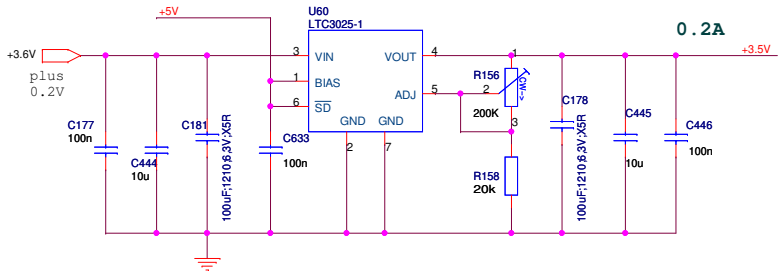


+5.0V/1.1A

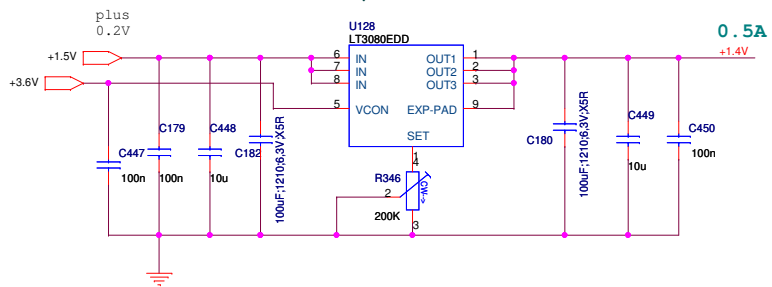


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

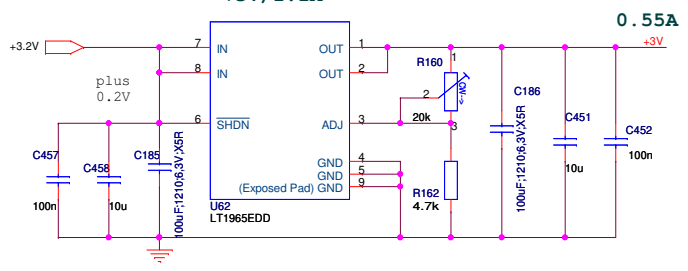
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



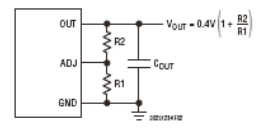
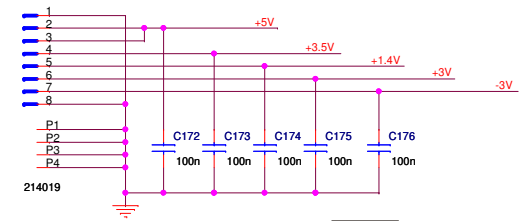
CSOUT10



+5V Current 0.95A limiter

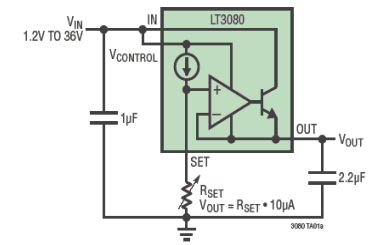
The PW inputs should be higher than the marked inputs about 0.2V

POW10

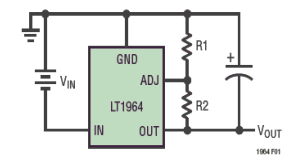
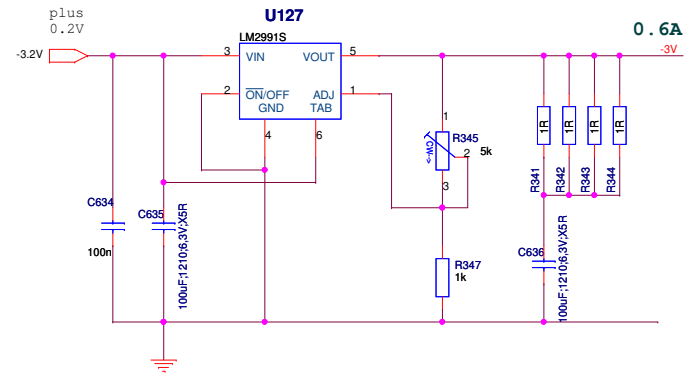


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



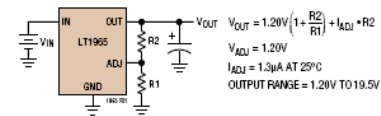
$V_{OUT} = -1.22V \left(1 + \frac{R2}{R1}\right) - (I_{ADJ}) \cdot R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA$ AT 25°C
 OUTPUT RANGE = -1.22V TO -20V

GSI

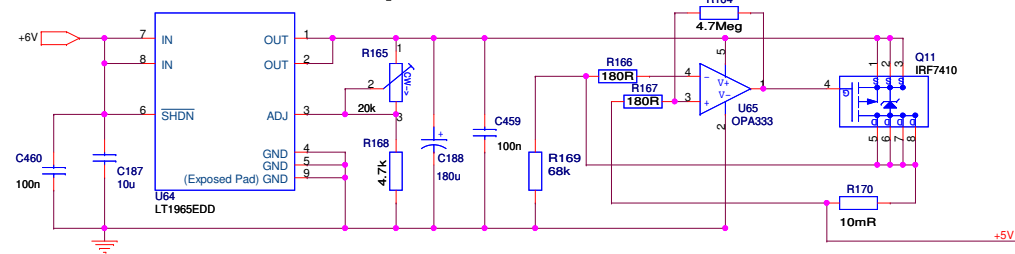
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN	Size: A3	Page: 15/ 21
Modified: Thursday, October 01, 2009	Designer: <Designer>	Layouter: <Layouter>

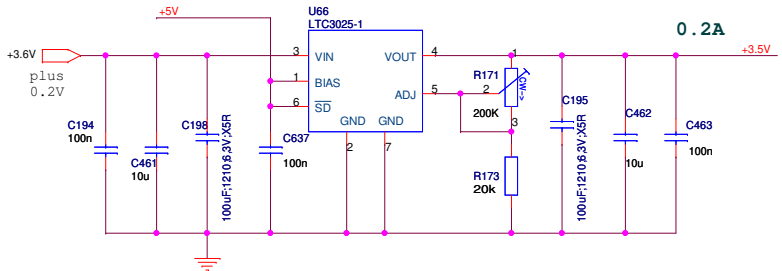


+5.0V/1.1A

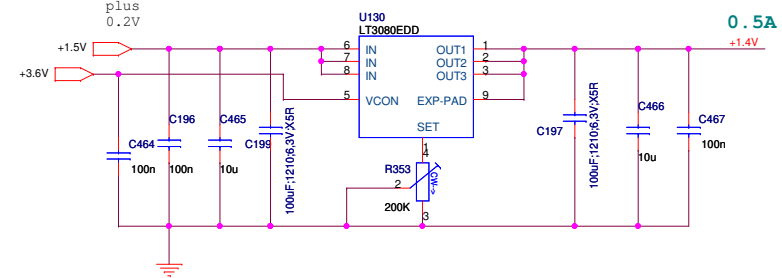


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

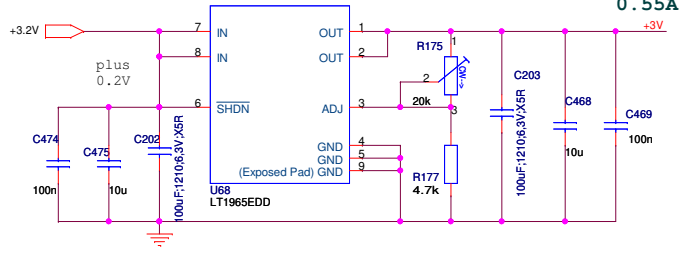
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



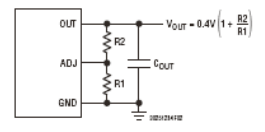
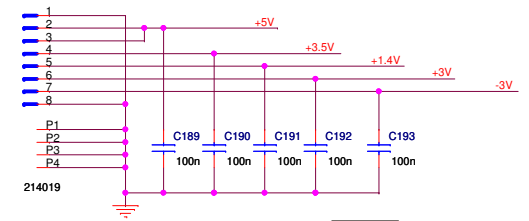
CSOUT11



+5V Current limiter

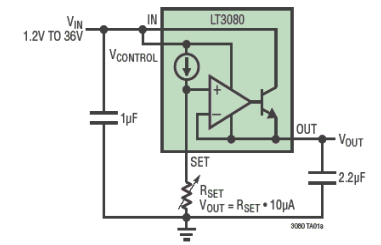
The PW inputs should be higher than the marked inputs about 0.2V

POW11

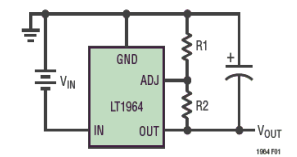
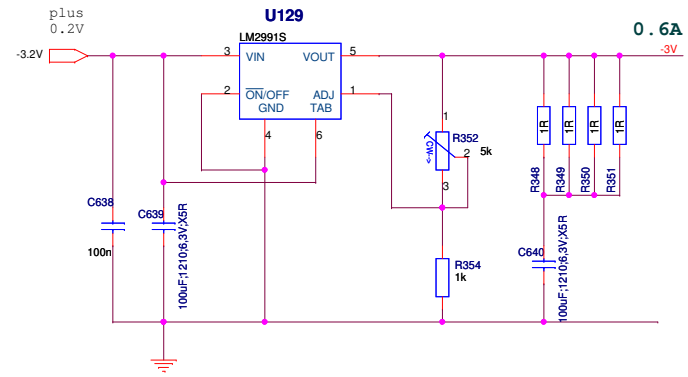


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A

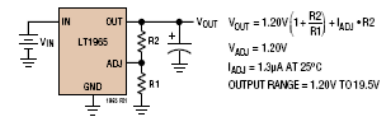


GSI

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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN
 Modified: Thursday, October 01, 2009
 Designer: <Designer>

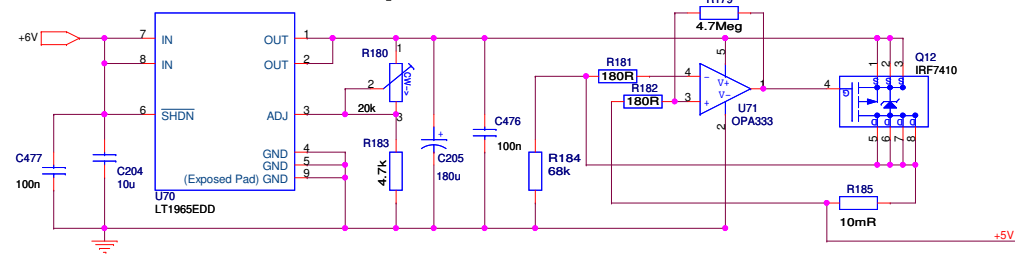
Size: A3
 Page: 16 / 21
 Layouter: <Layouter>



$$V_{out} = 1.20V \left(1 + \frac{R2}{R1}\right) + I_{ADJ} \cdot R2$$

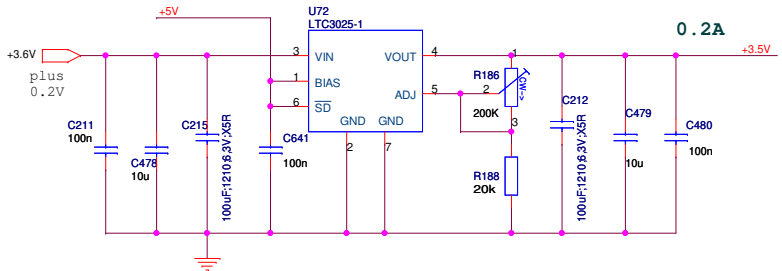
$V_{ADJ} = 1.20V$
 $I_{ADJ} = 1.3\mu A$ AT 25°C
 OUTPUT RANGE = 1.20V TO 19.5V

+5.0V/1.1A

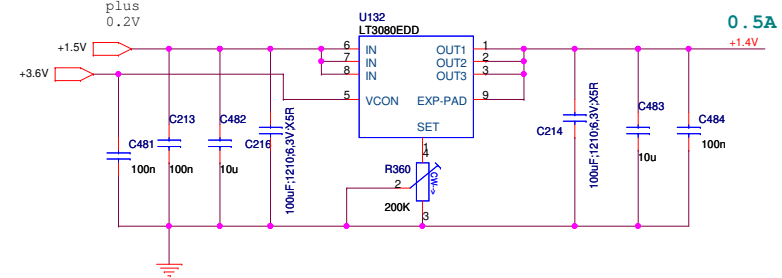


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

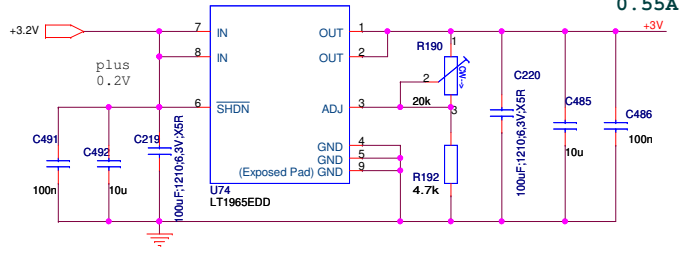
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



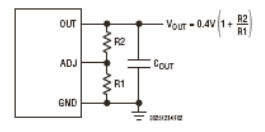
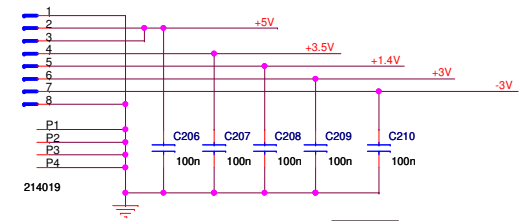
CSOUT12



+5V Current limiter

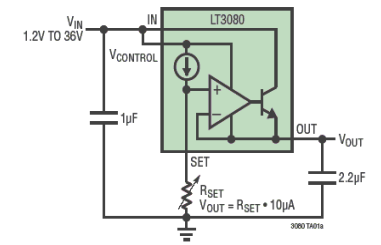
The PW inputs should be higher than the marked inputs about 0.2V

POW12

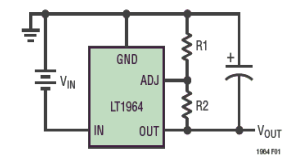
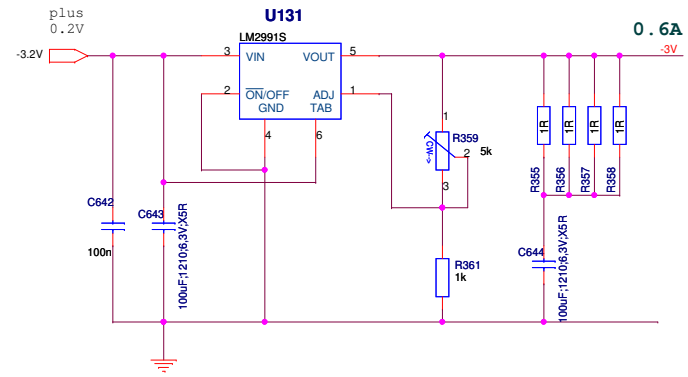


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



$$V_{out} = -1.22V \left(1 + \frac{R2}{R1}\right) - (I_{ADJ}) \cdot R2$$

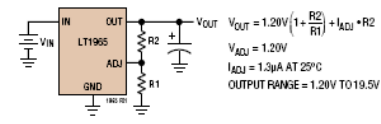
$V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA$ AT 25°C
 OUTPUT RANGE = -1.22V TO -20V

GSI

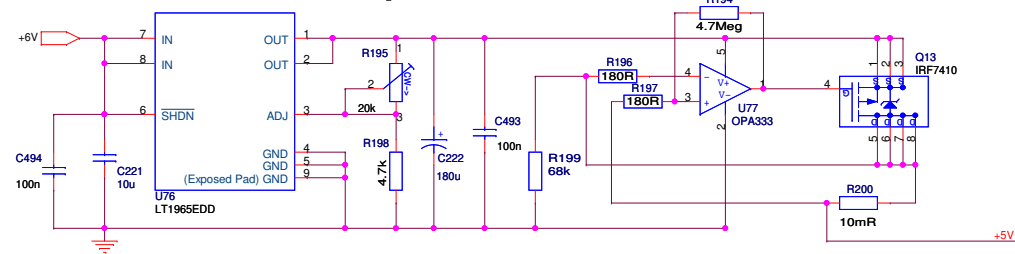
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN	Size: A3	Page: 17 / 21
Modified: Thursday, October 01, 2009	Designer: <Designer>	Layouter: <Layouter>

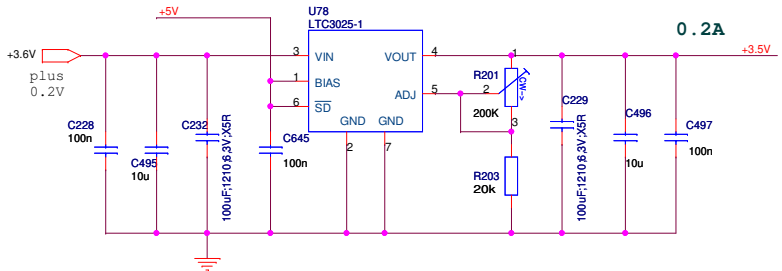


+5.0V/1.1A

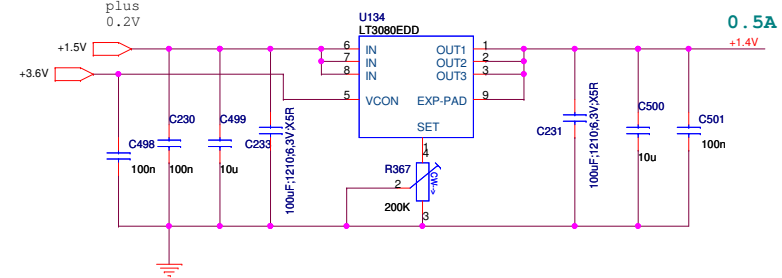


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

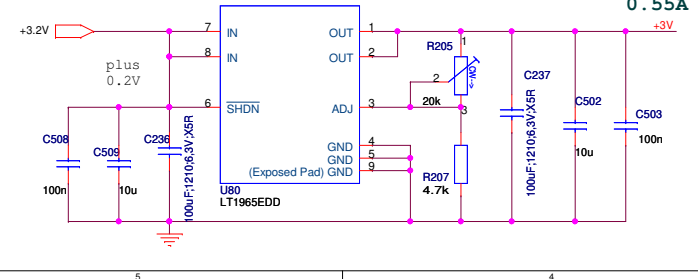
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



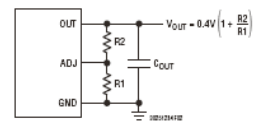
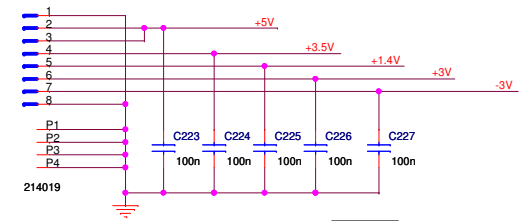
CSOUT13



+5V Current limiter

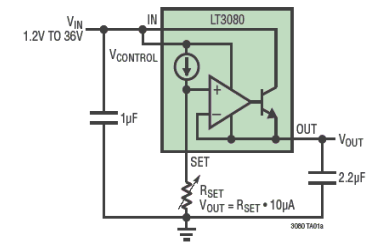
The PW inputs should be higher than the marked inputs about 0.2V

POW13

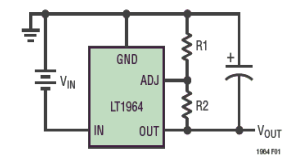
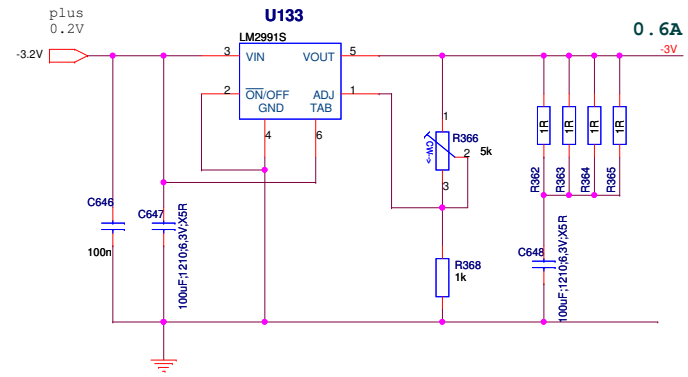


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A

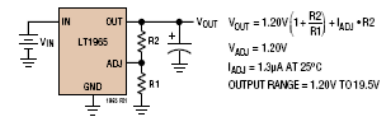


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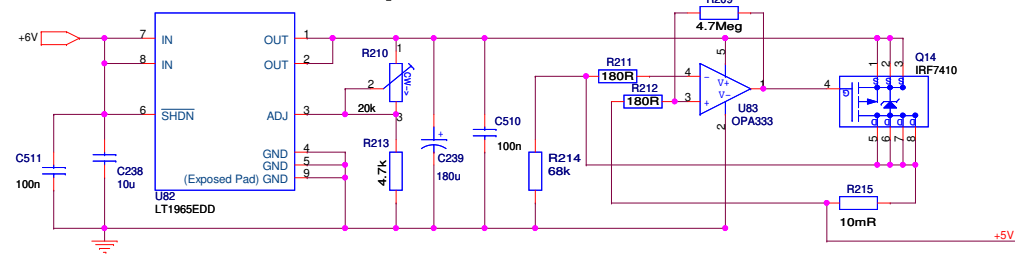
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 Modified: Thursday, October 01, 2009
 Designer: <Designer>

Size: A3
 Page: 18 / 21
 Layouter: <Layouter>

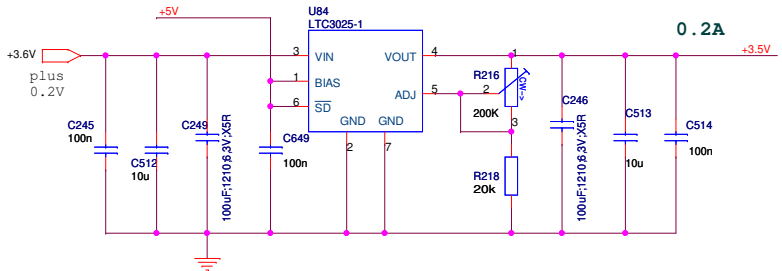


+5.0V/1.1A

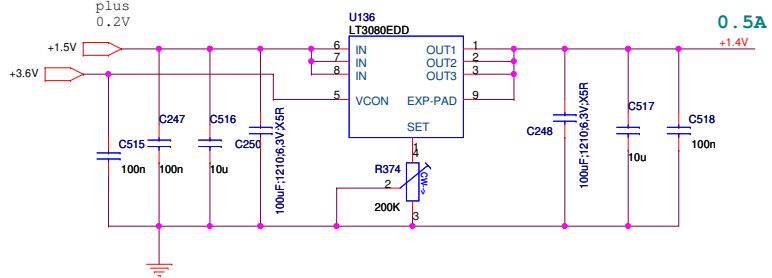


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

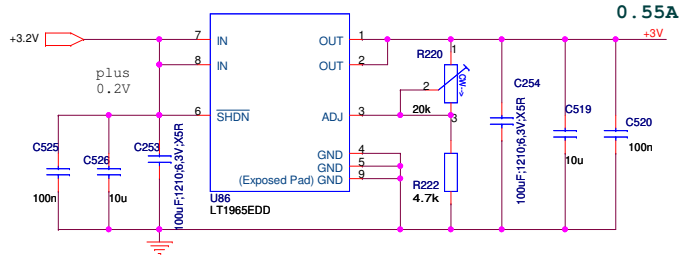
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



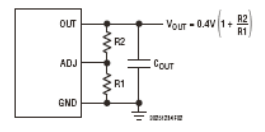
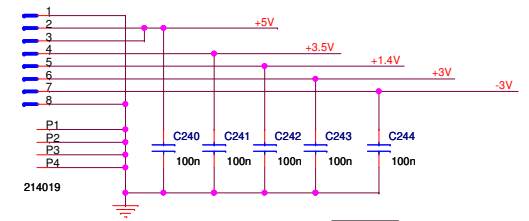
CSOUT14



+5V Current 0.95A limiter

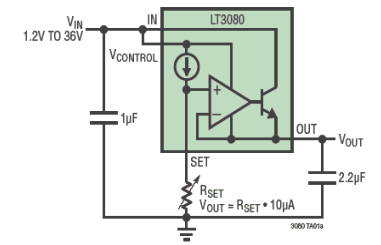
The PW inputs should be higher than the marked inputs about 0.2V

POW14

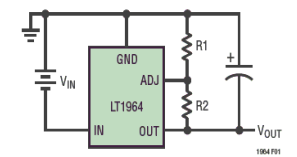
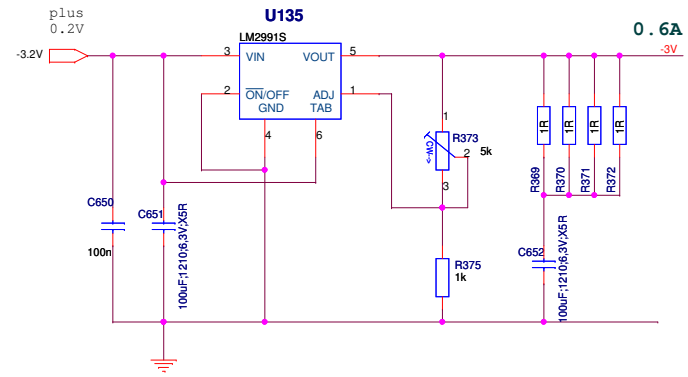


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A

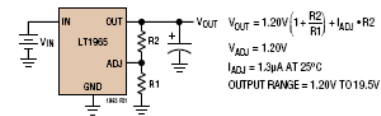


$V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA \text{ AT } 25^\circ C$
 OUTPUT RANGE = -1.22V TO -20V

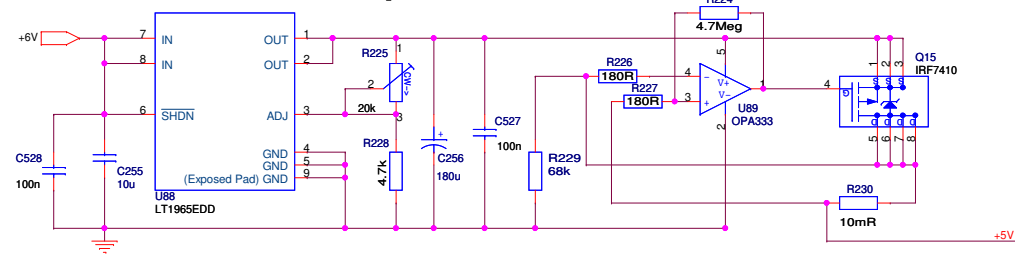
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN
 Modified: Thursday, October 01, 2009 Size: A3 Page: 19 / 21
 Designer: <Designer> Layouter: <Layouter>

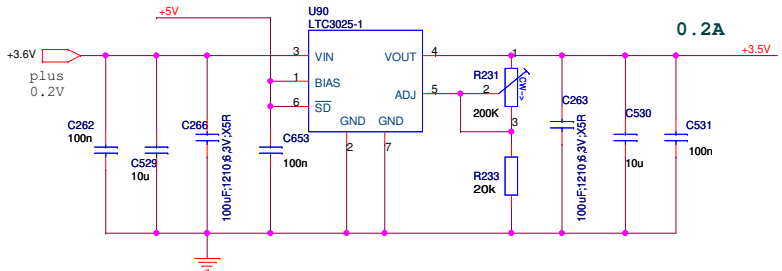


+5.0V/1.1A

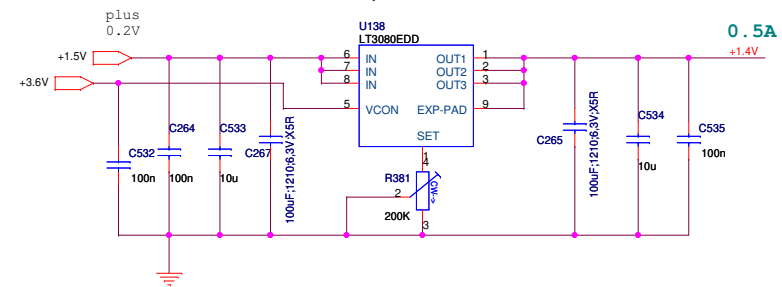


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

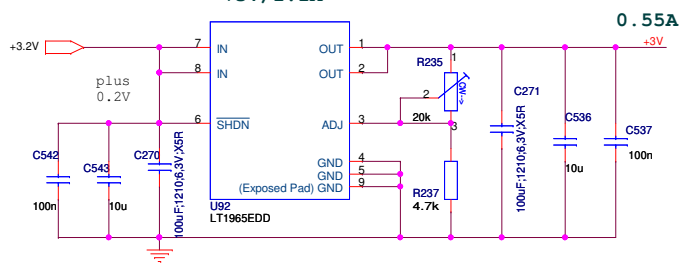
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



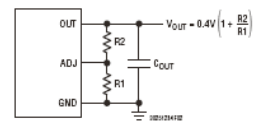
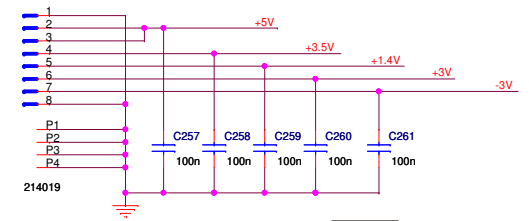
CSOUT15



+5V Current 0.95A limiter

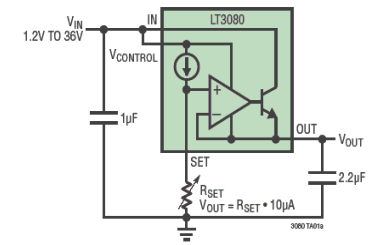
The PW inputs should be higher than the marked inputs about 0.2V

POW15

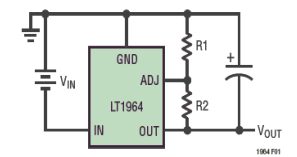
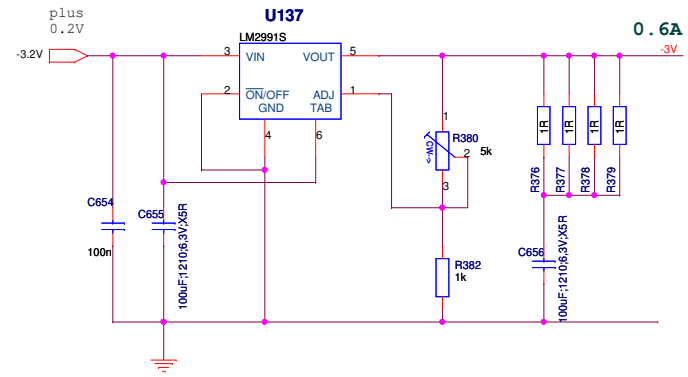


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



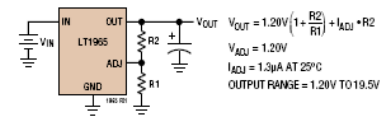
$V_{OUT} = -1.22V(1 + \frac{R2}{R1}) - (I_{ADJ})/R2$
 $V_{ADJ} = -1.22V$
 $I_{ADJ} = 30nA AT 25°C$
 OUTPUT RANGE = -1.22V TO -20V

GSI

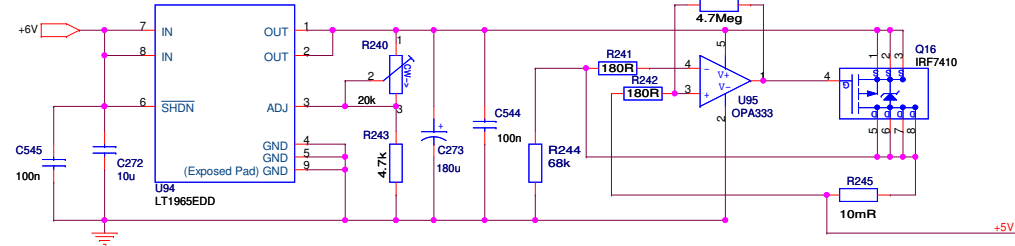
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Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN	Size: A3
Modified: Thursday, October 01, 2009	Page: 20 / 21
Designer: <Designer>	Layouter: <Layouter>

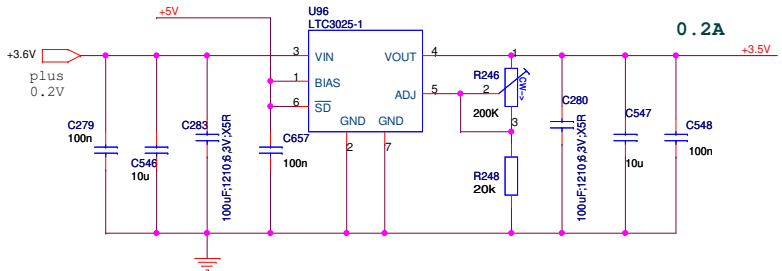


+5.0V/1.1A

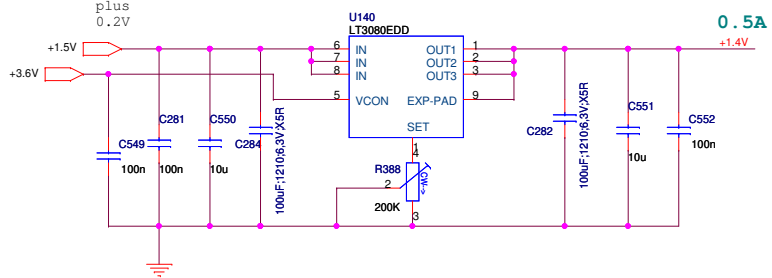


Since OEP uses LTC3025 for +1V2, +1V and +3V3, Here use same LT3025 for all

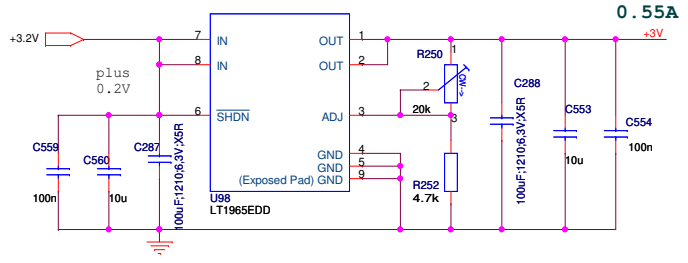
+3.5V/0.5A



+1.4V/1.15A



+3V/1.1A



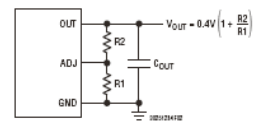
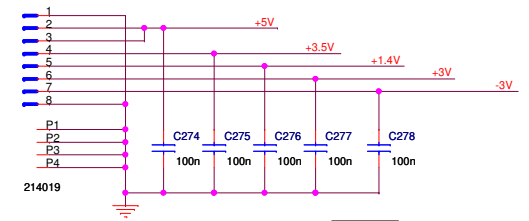
CSOUT16



+5V Current 0.95A limiter

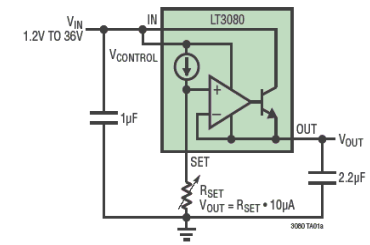
The PW inputs should be higher than the marked inputs about 0.2V

POW16

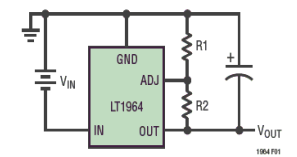
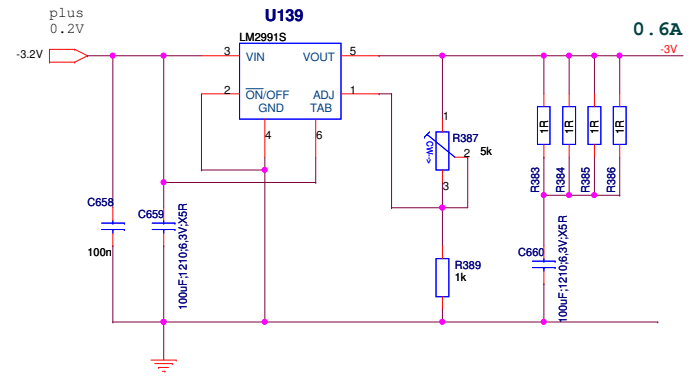


LTC3025-1

Variable Output Voltage 1.1A Supply



-3V/1A



GSI

<Title>

Design: K1GSL0BHADES-MDCIMDC-FAN-PWMDC-FAN-PW3\FAN-PW3.DSN
 Modified: Thursday, October 01, 2009
 Designer: <Designer>

Size: A3
 Page: 21 / 21
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