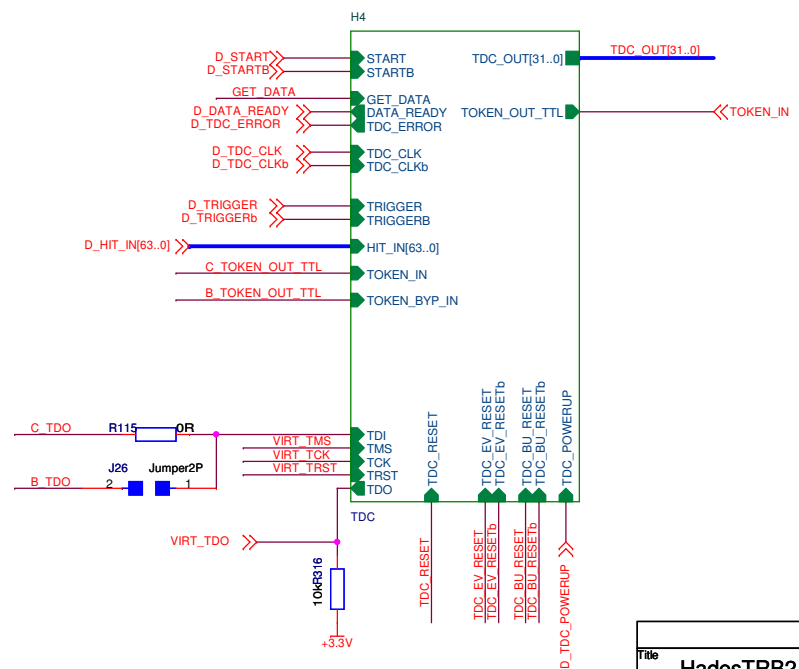
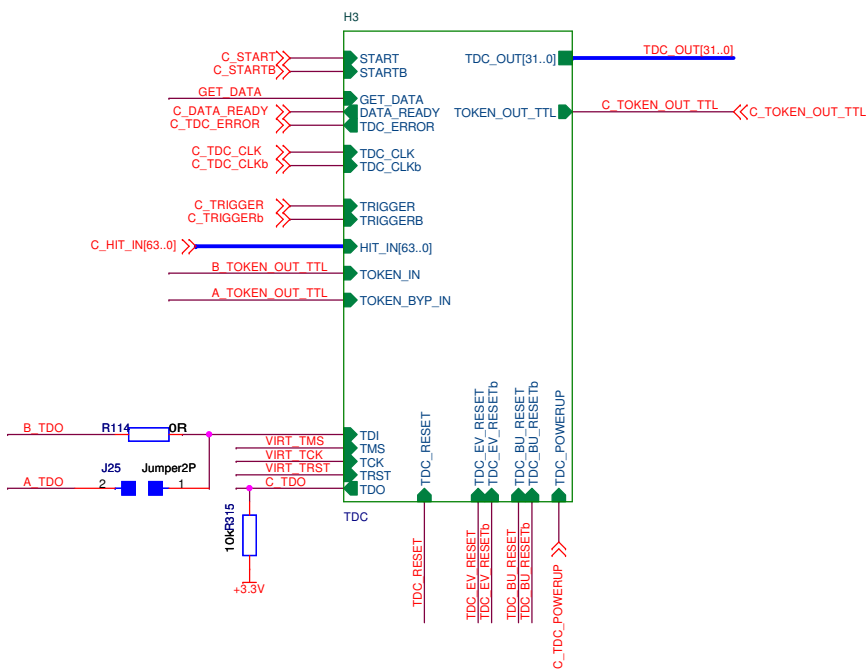
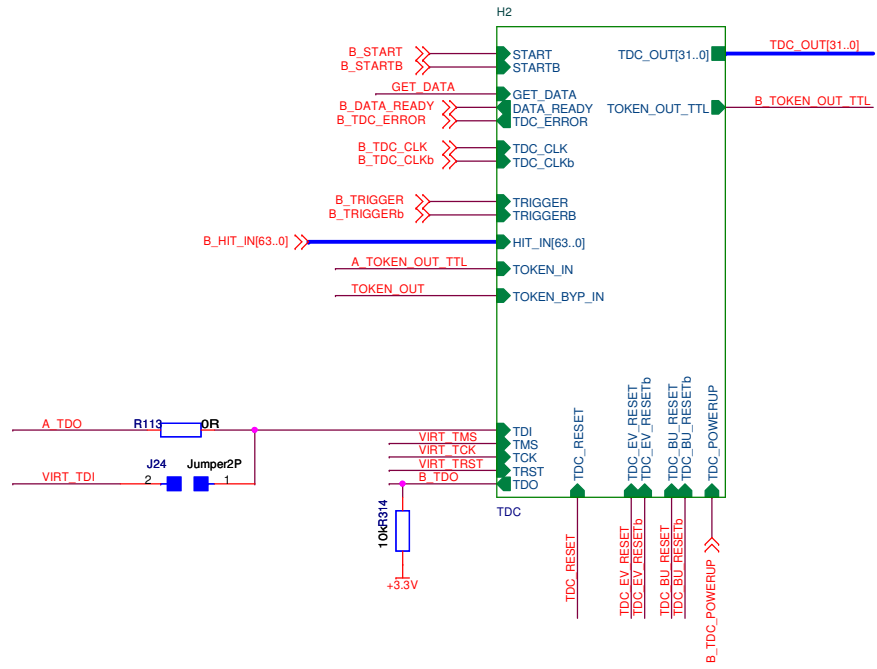
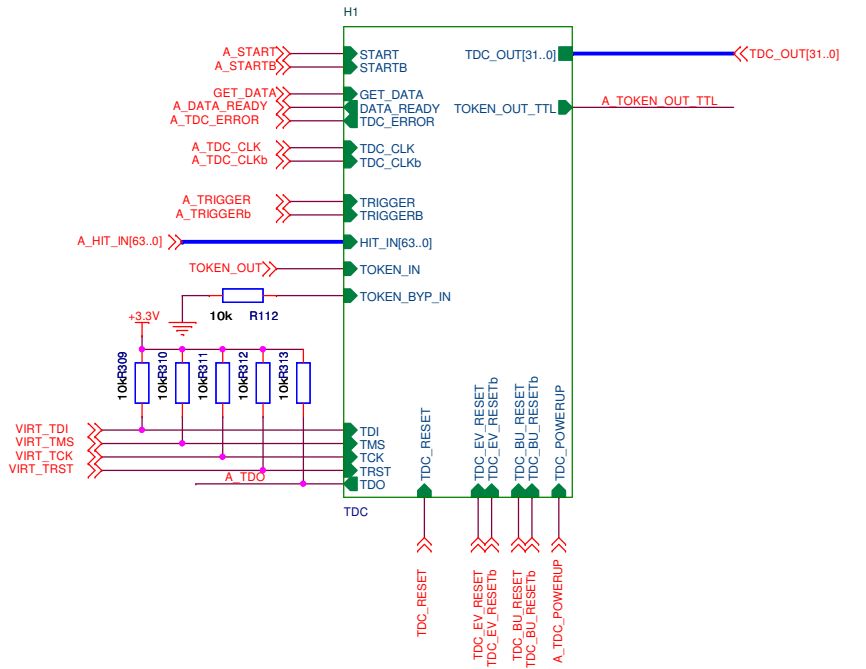
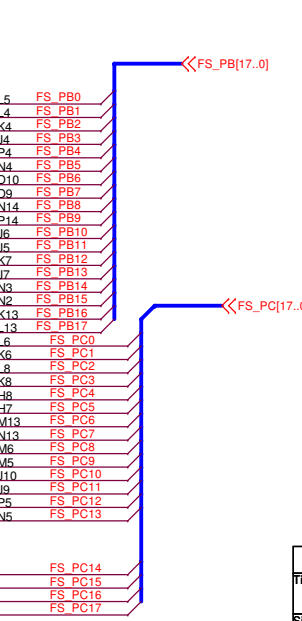
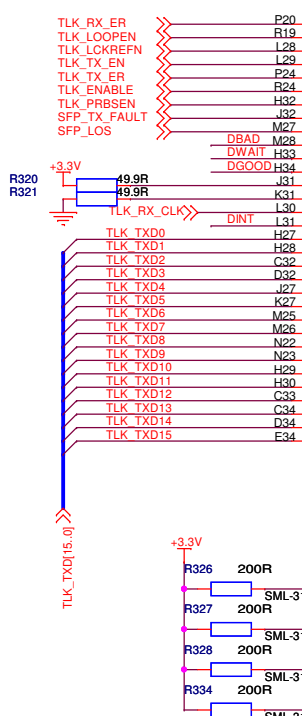
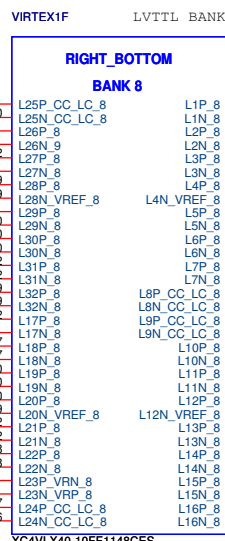
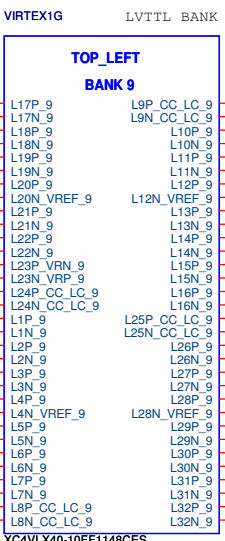
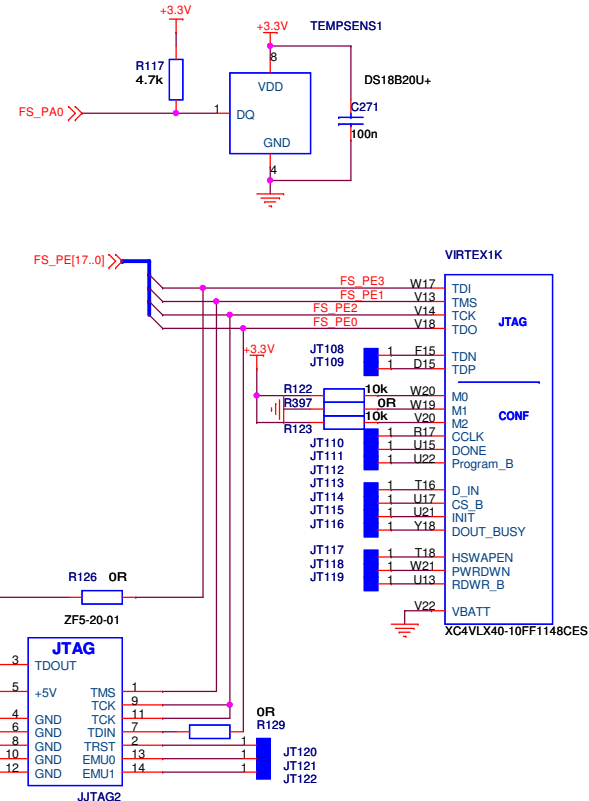
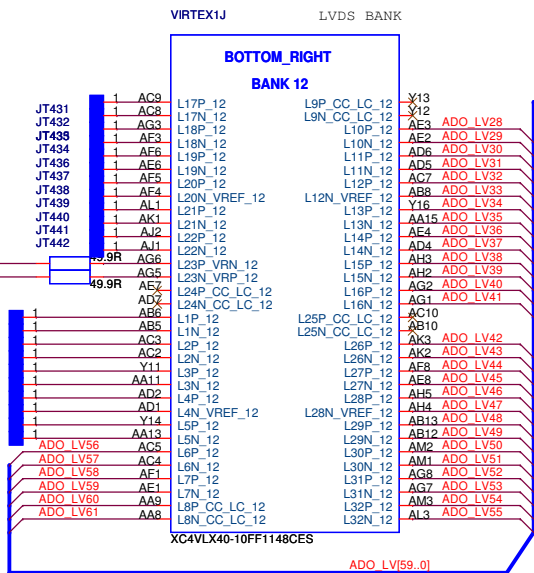
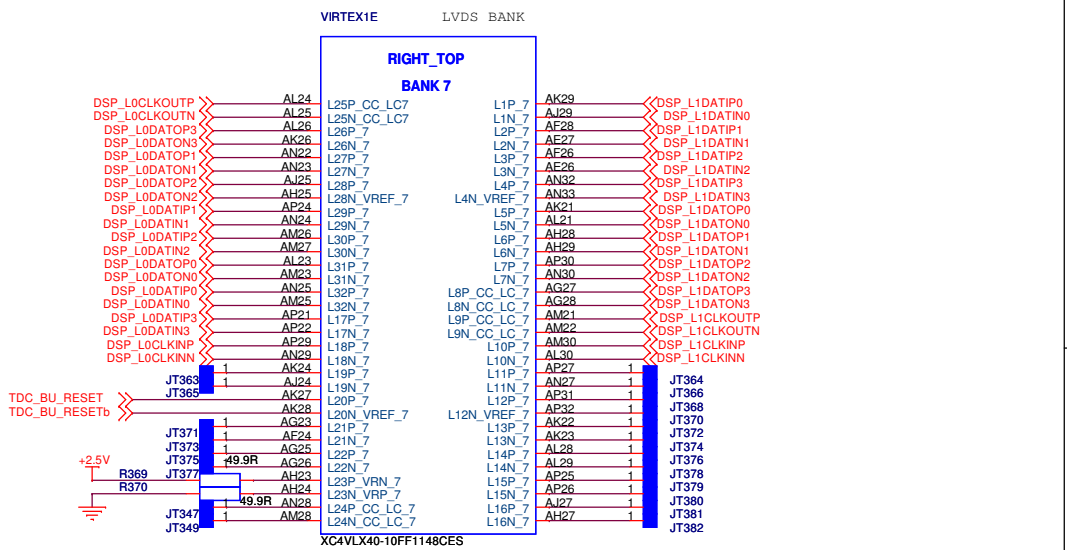
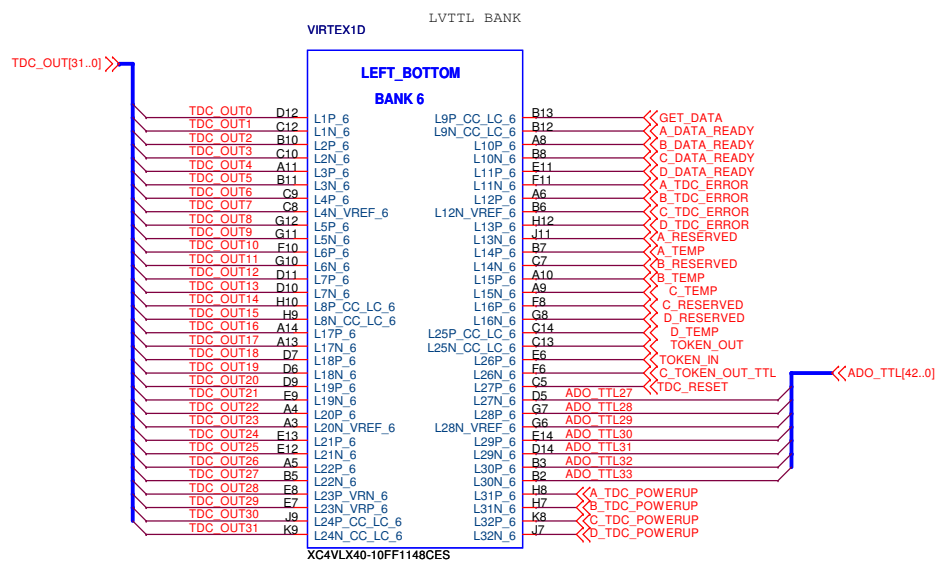
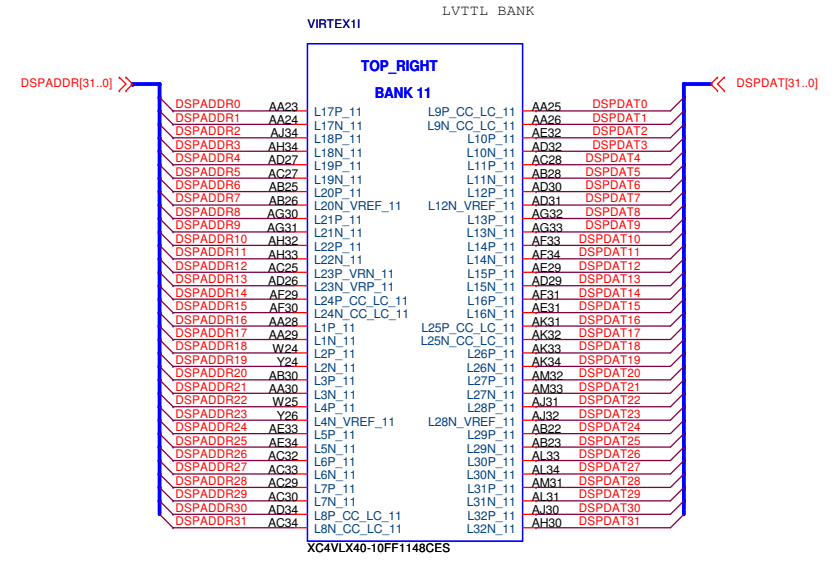
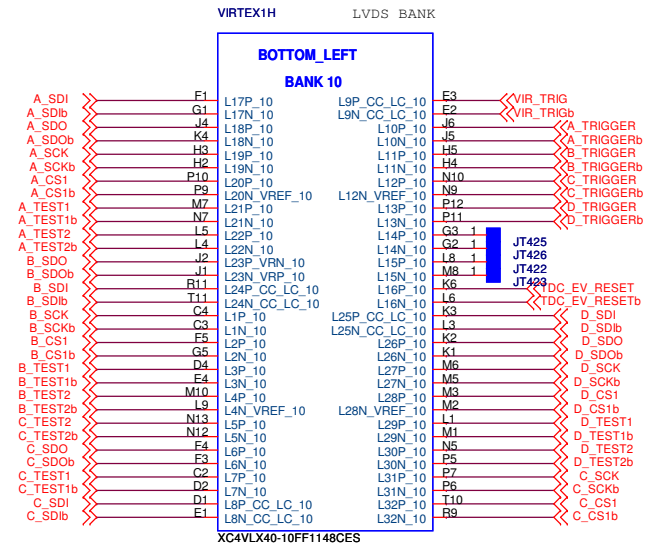


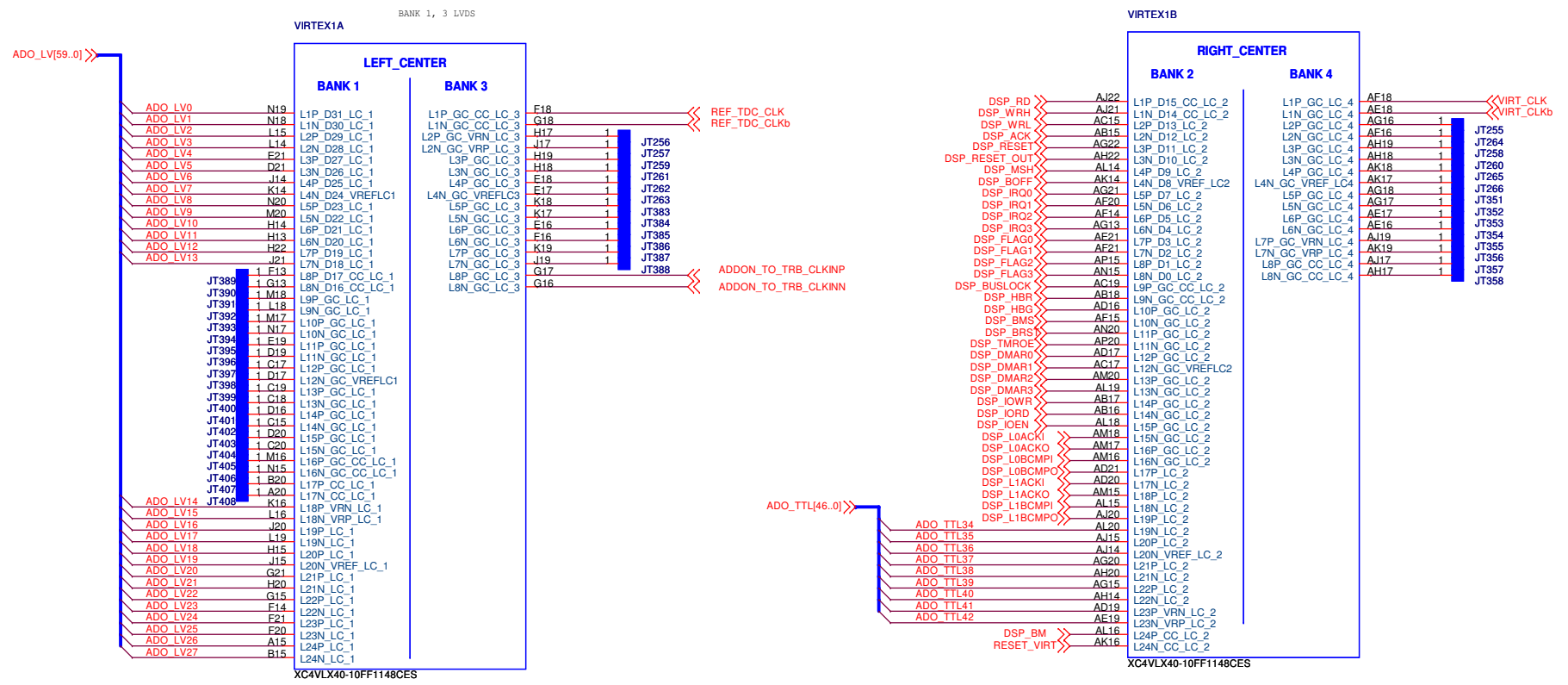
Peter, please make each next START diff. lines 0.5cm longer then the former ones. e.g. if A\_START and A\_STARTb have 35 mm then B\_START and B\_STARTb should have 40mm



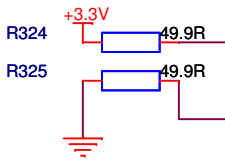
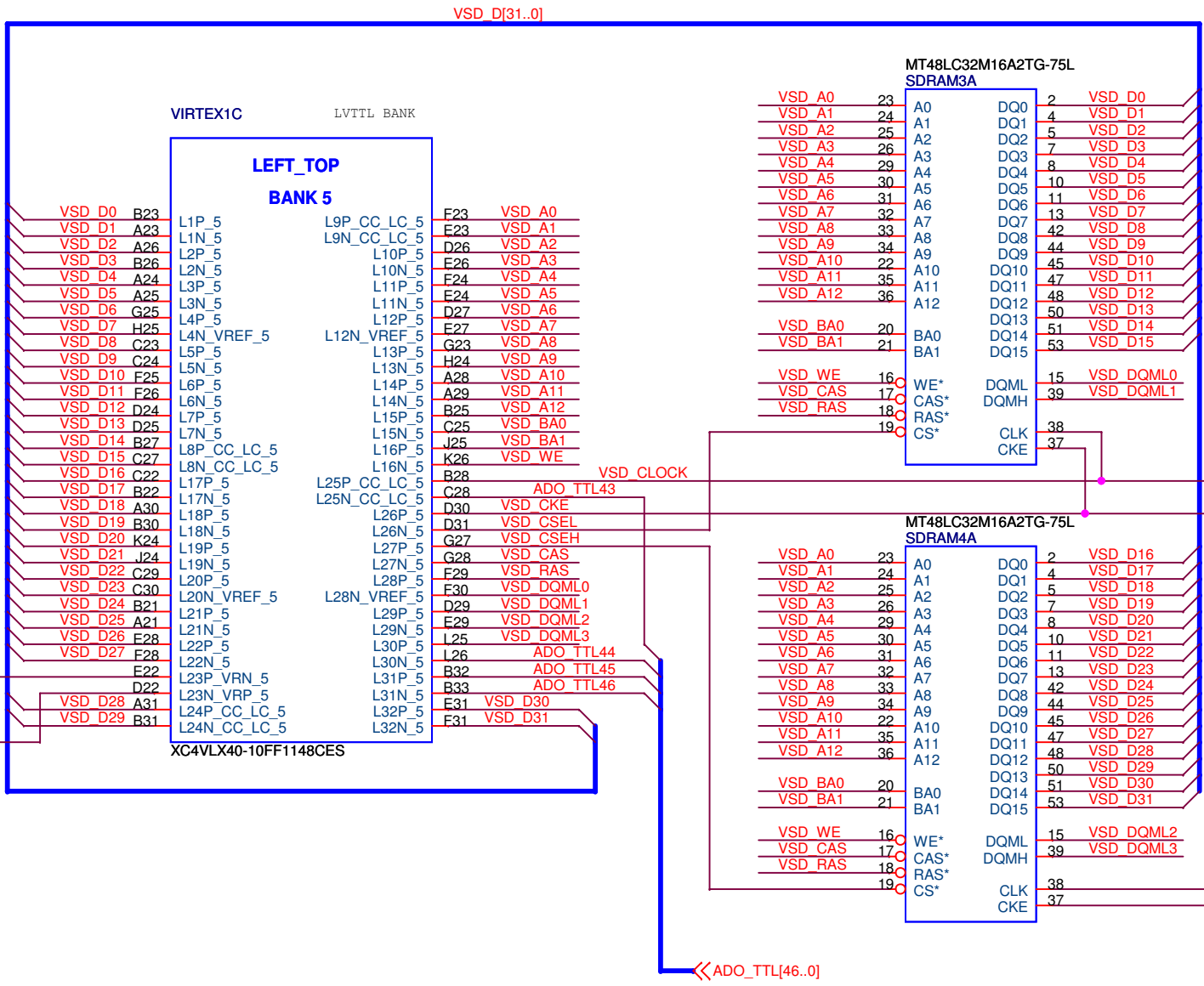
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HadesTRB2 - TIME		
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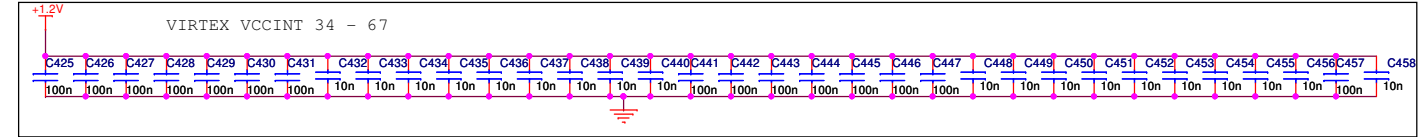
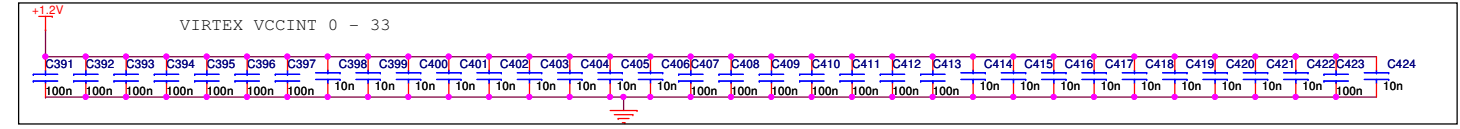
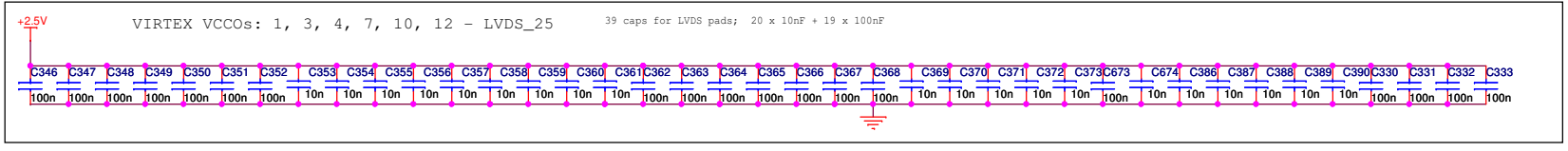
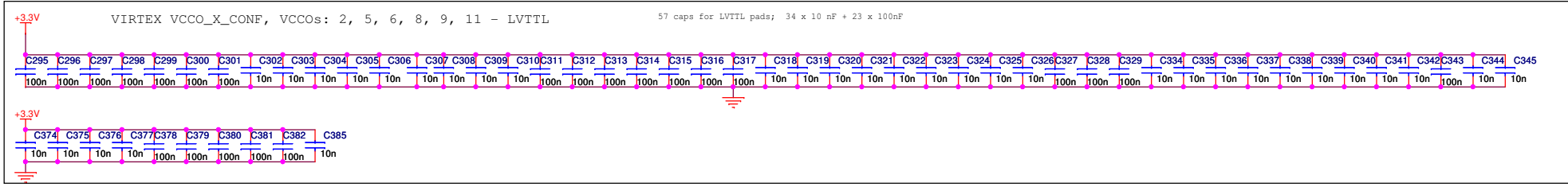
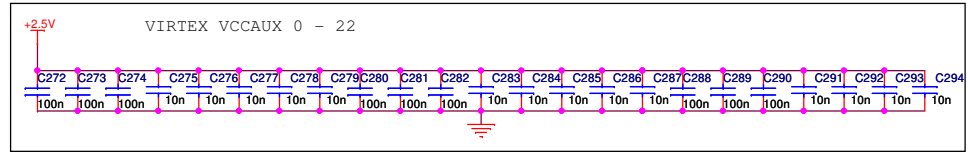
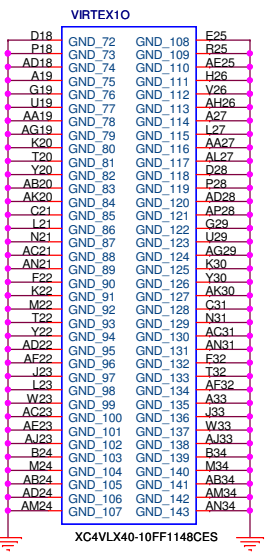
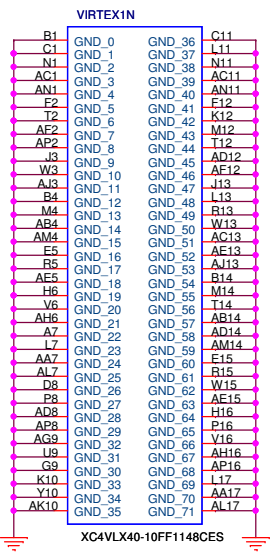
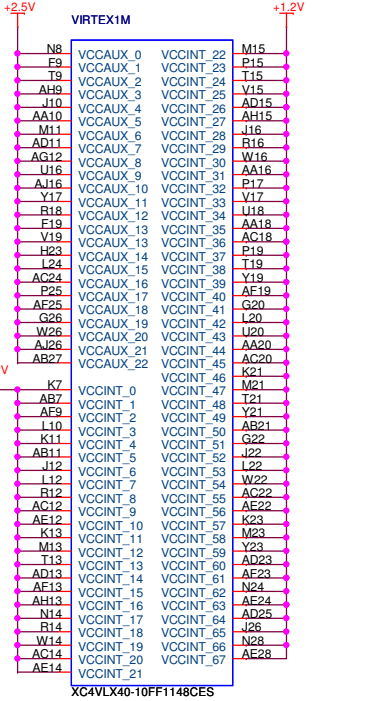
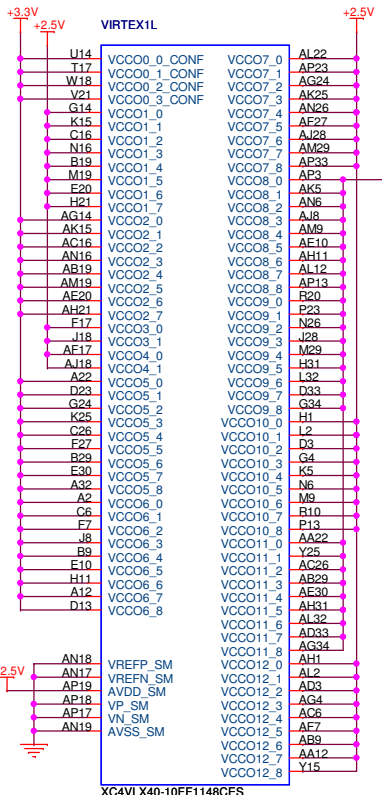






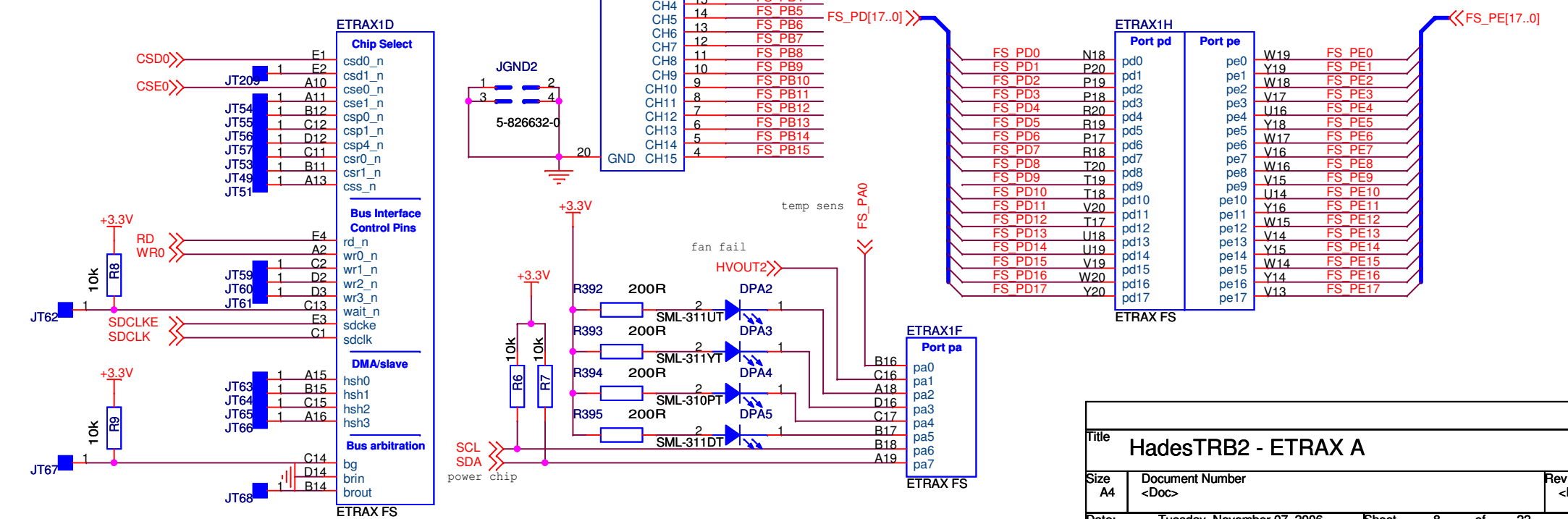
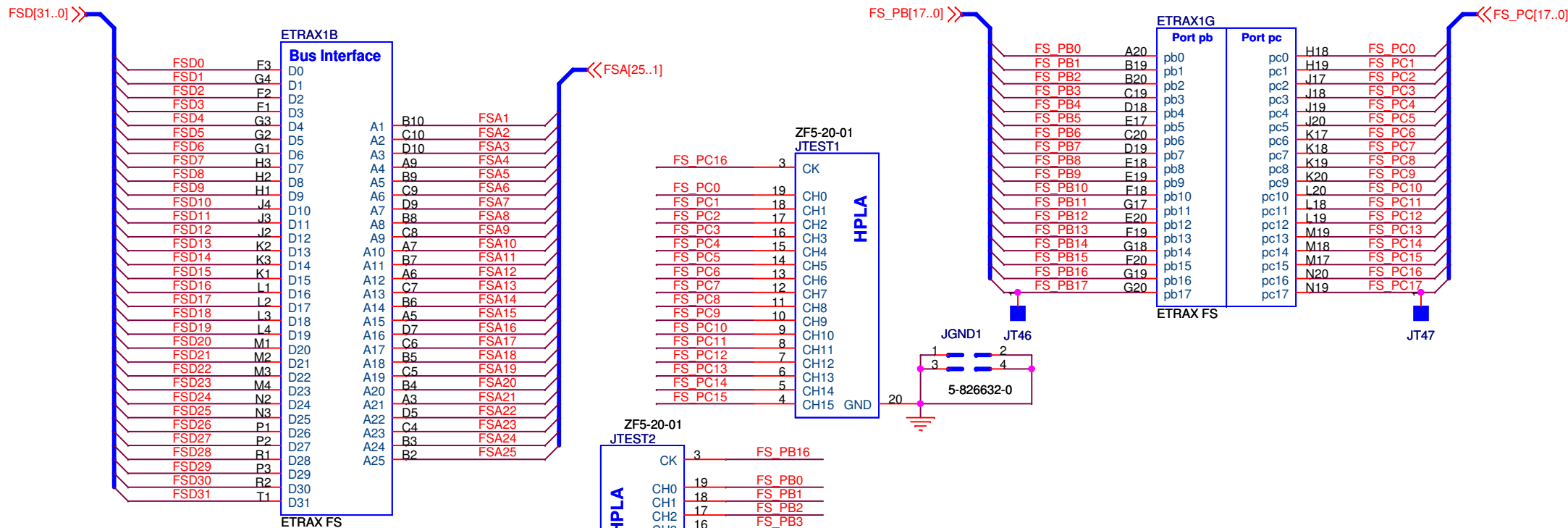
DSP control - banks 2, 7, 11





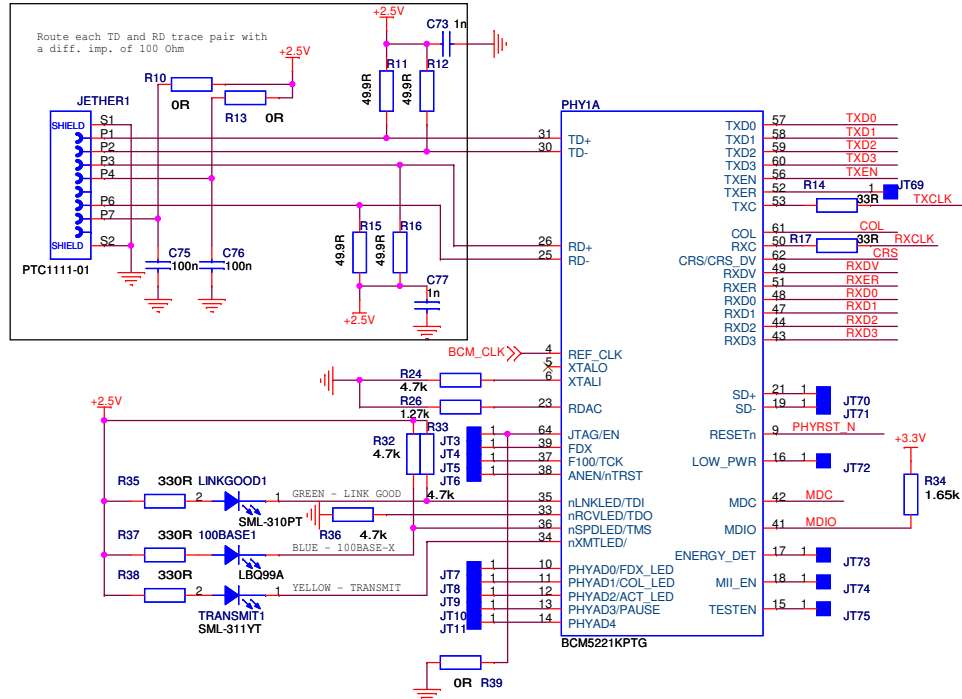
Title			HadesTRB2 - VIRTEX SUPPLY		
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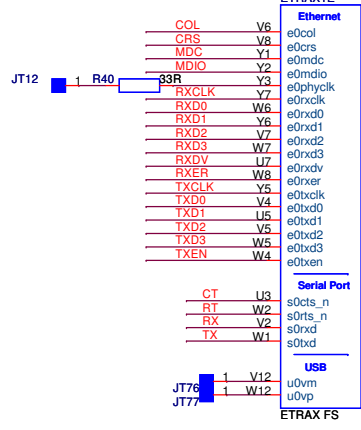
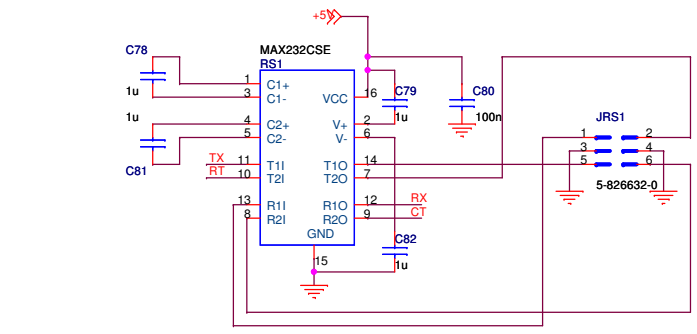
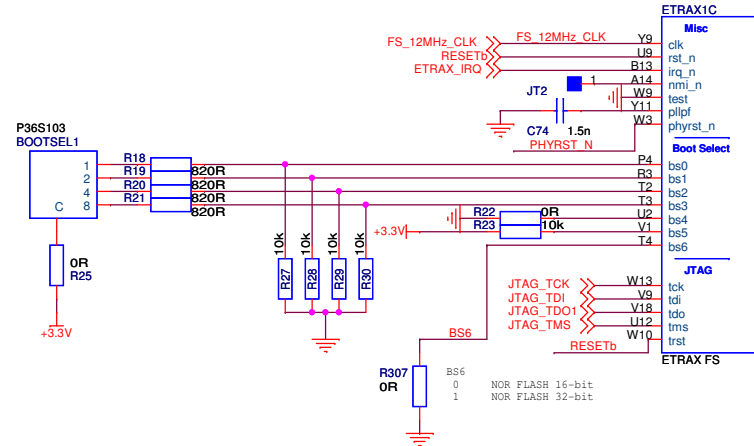


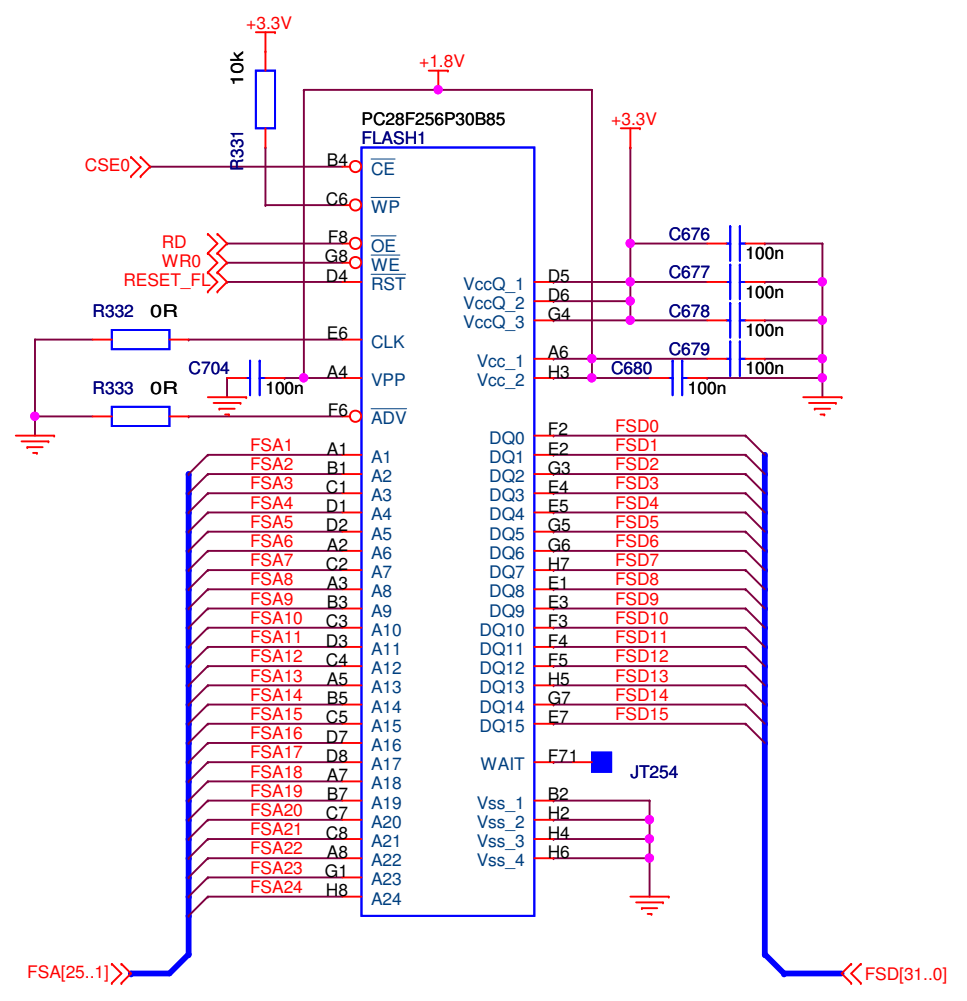


Place the termination network and RJ45 connector close to the BCM5221

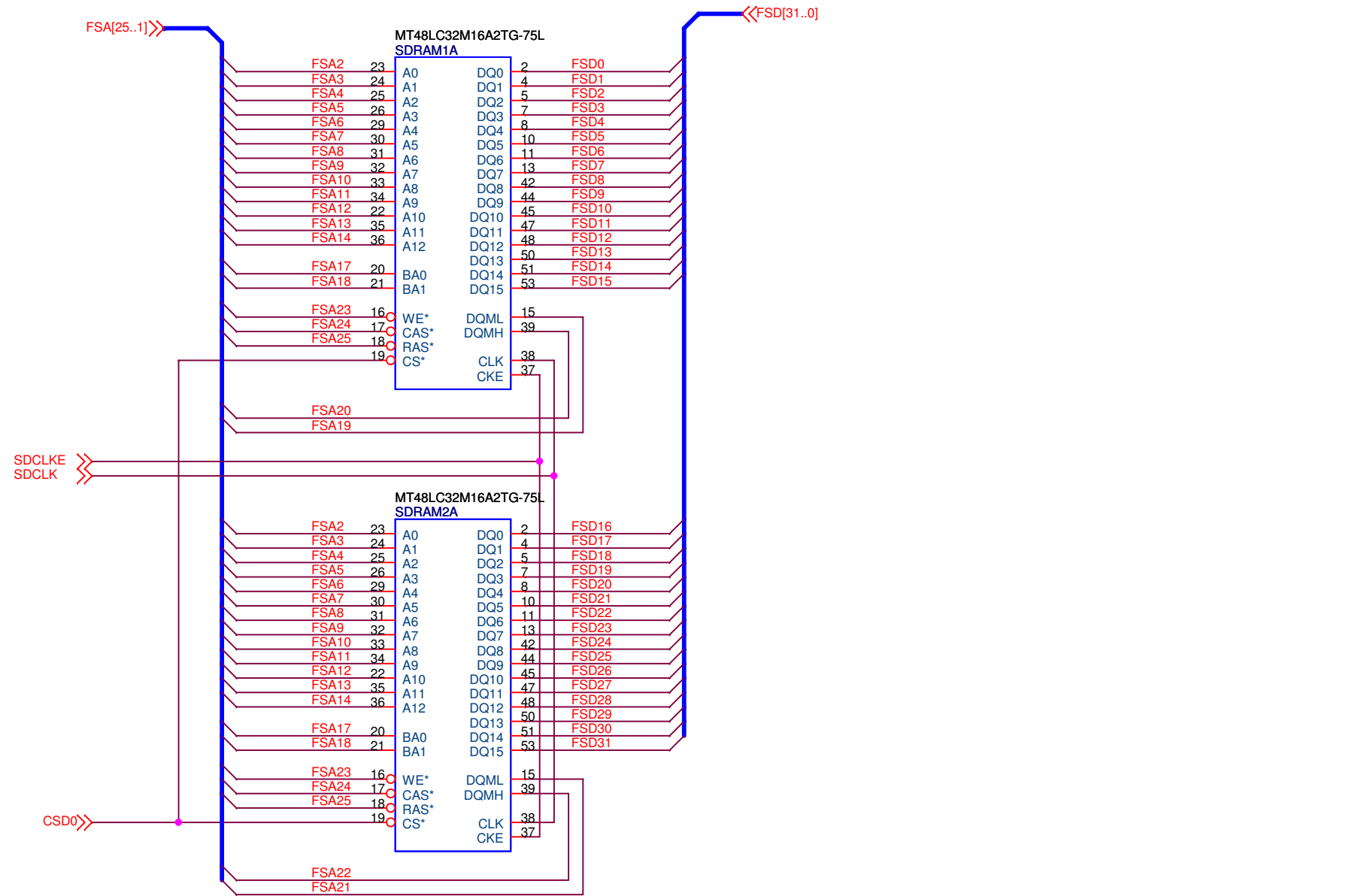


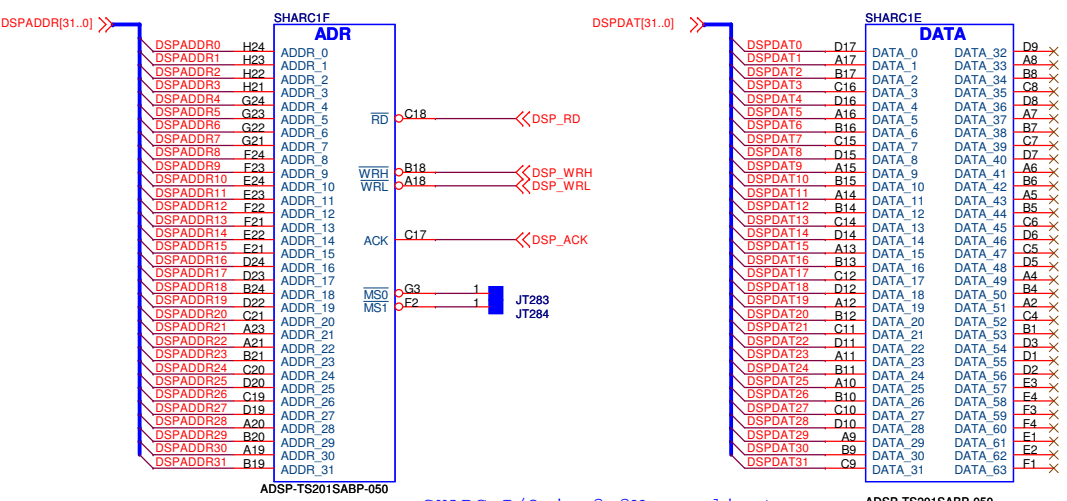
BS3	BS2	BS1	BS0	
-	0	0	1	NOR Flash
-	0	0	1	Network RX only
-	0	1	0	Network RX/TX
-	0	1	1	NAND Flash
-	1	0	0	Serial
-	1	0	1	Master boot
-	1	1	0	Slave boot
-	1	1	1	No boot
0	-	-	-	Ethernet Halv-Duplex
1	-	-	-	Ethernet Full-Duplex





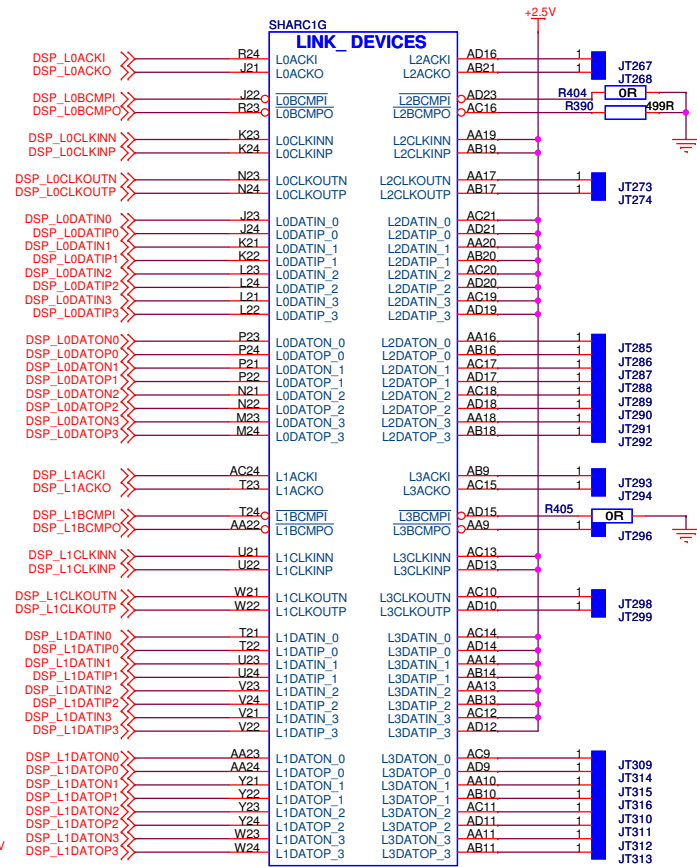
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HadesTRB2 - FLASH MEMORY		
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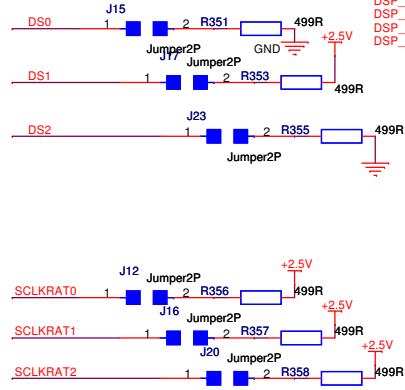


SHARC I/O is 3.3V compliant

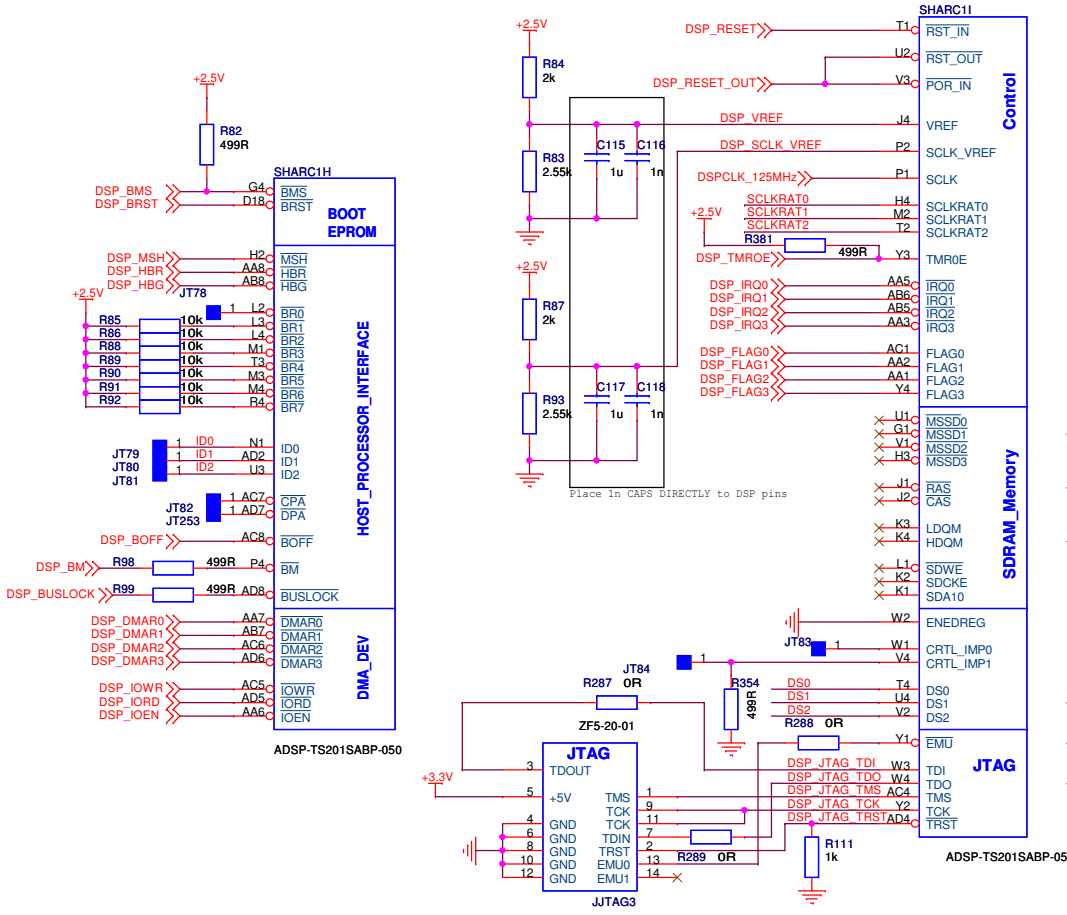
Routing:  
 order should be: DAT0, DAT1, CLK, DAT2, DAT3,  
 no via in between those signals,  
 only 45° turns (no 90°),  
 length should be matched for one direction and one port  
 (they are full duplex)  
 Peter, please read EE-179 System Design Guidelines.



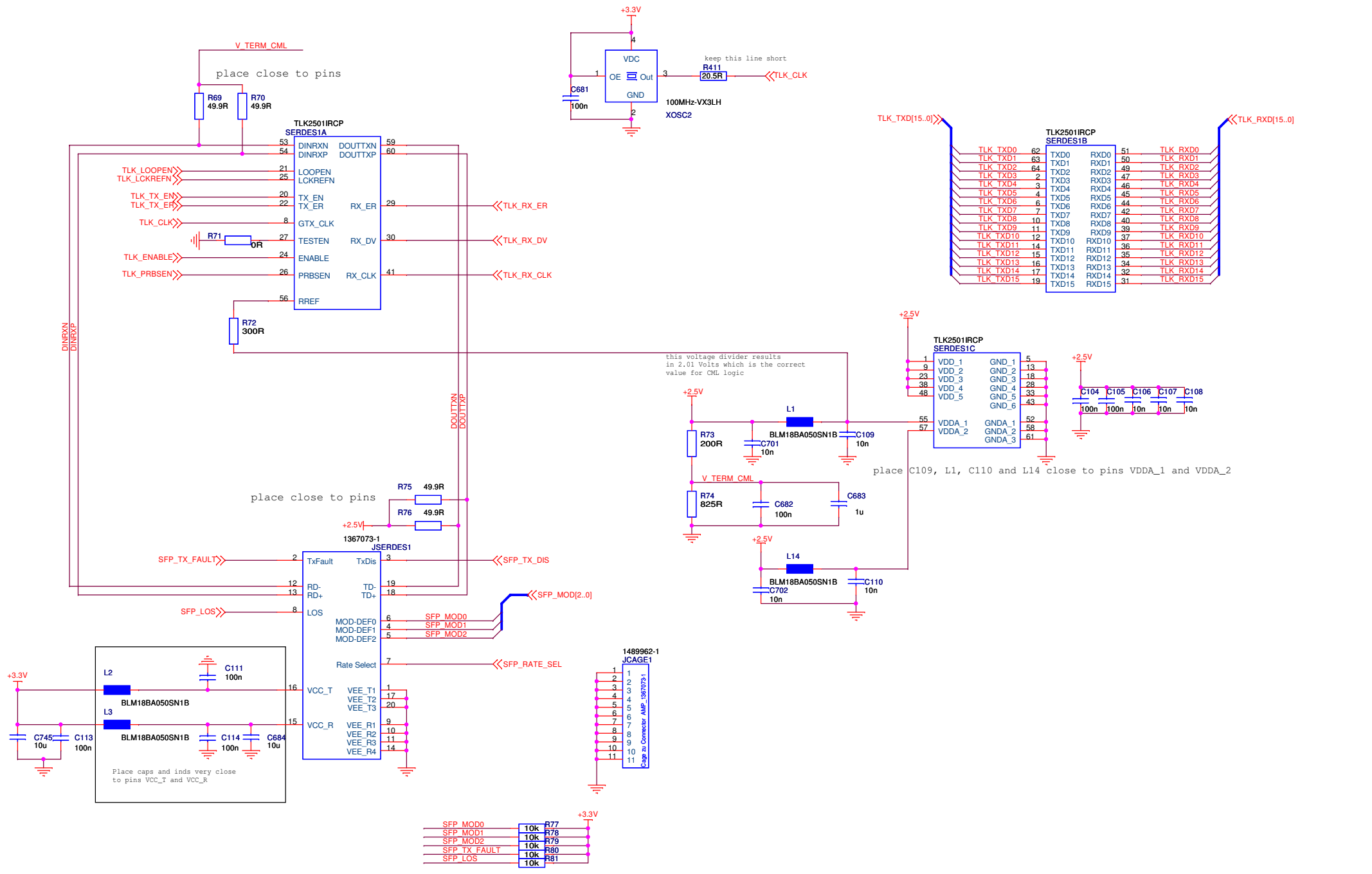
DS - drive strength  
 default 101



Signal	Ratio	Frequency
SCLKRAT[2..0]	ratio (CCLK=ratio * SCLK)	
0 0 0 (default)	4	CCLK = 600 MHz
0 1 0	6	CCLK = 500 MHz

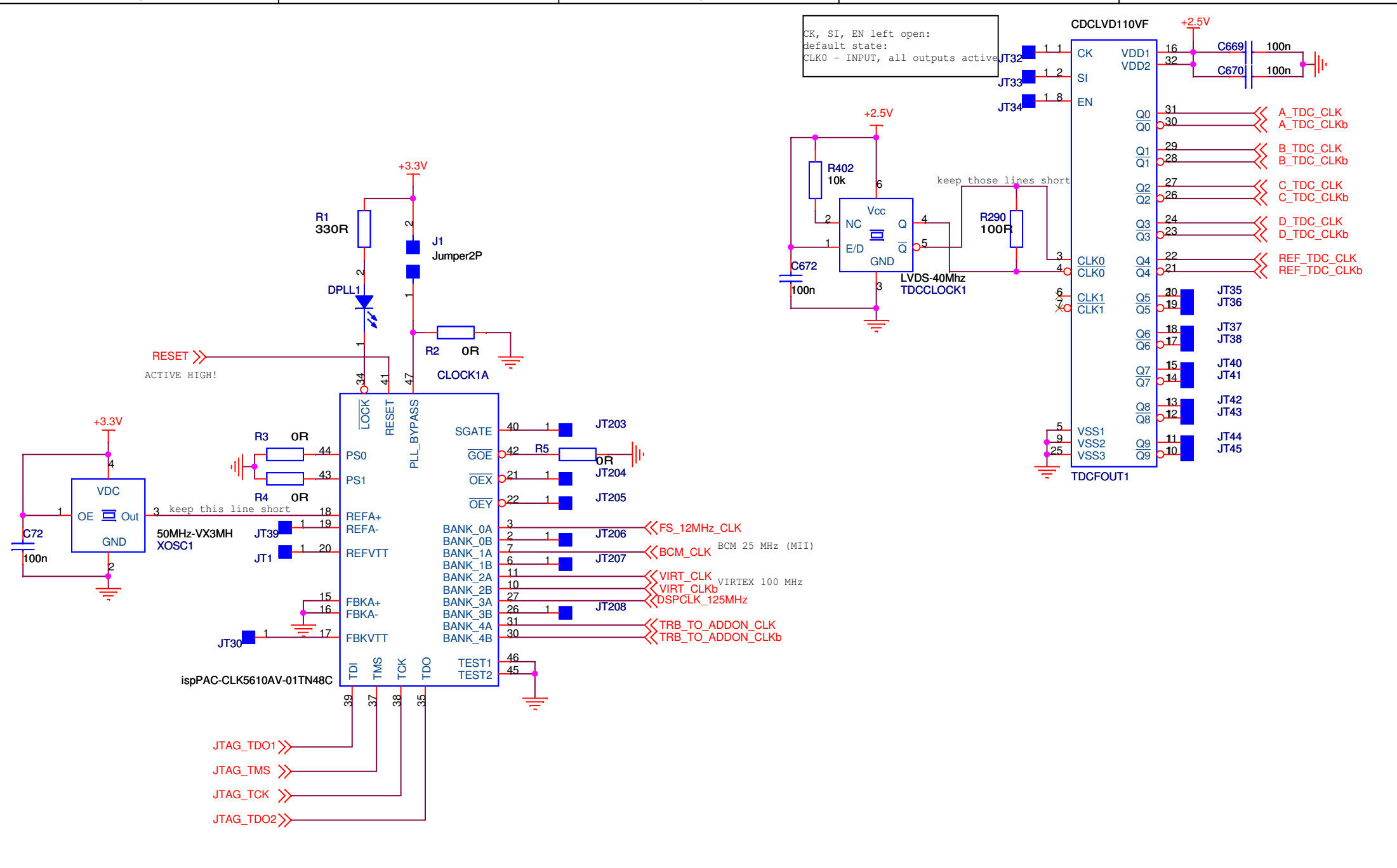




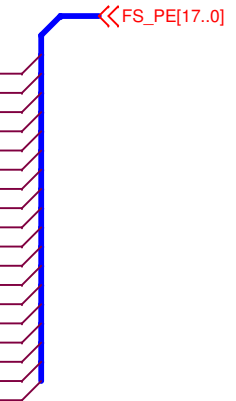
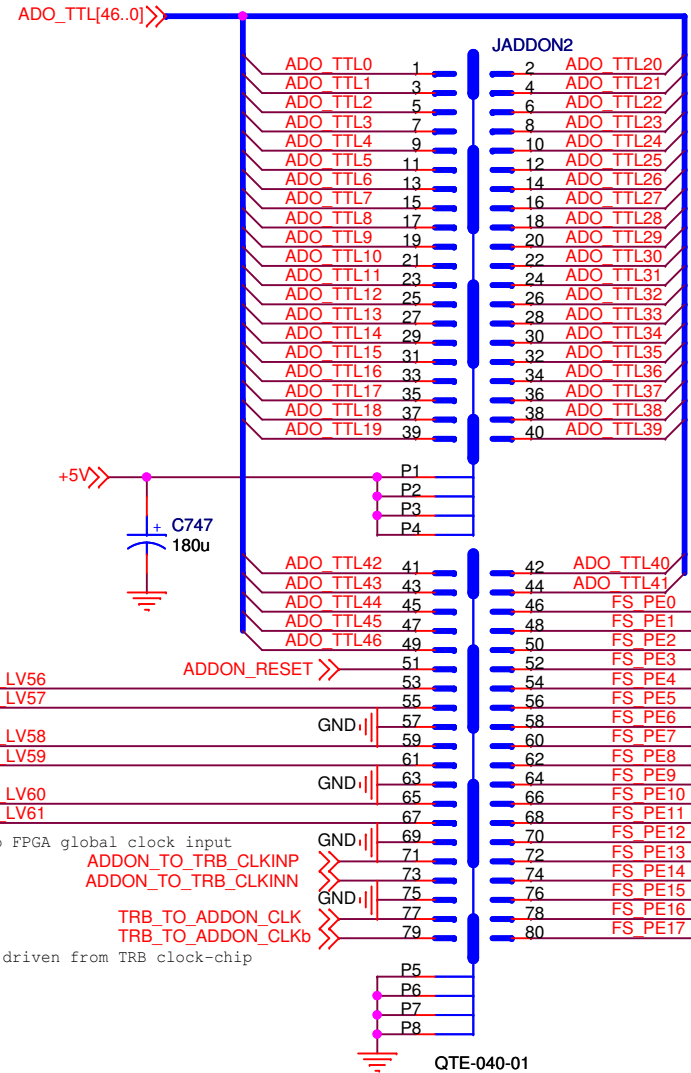
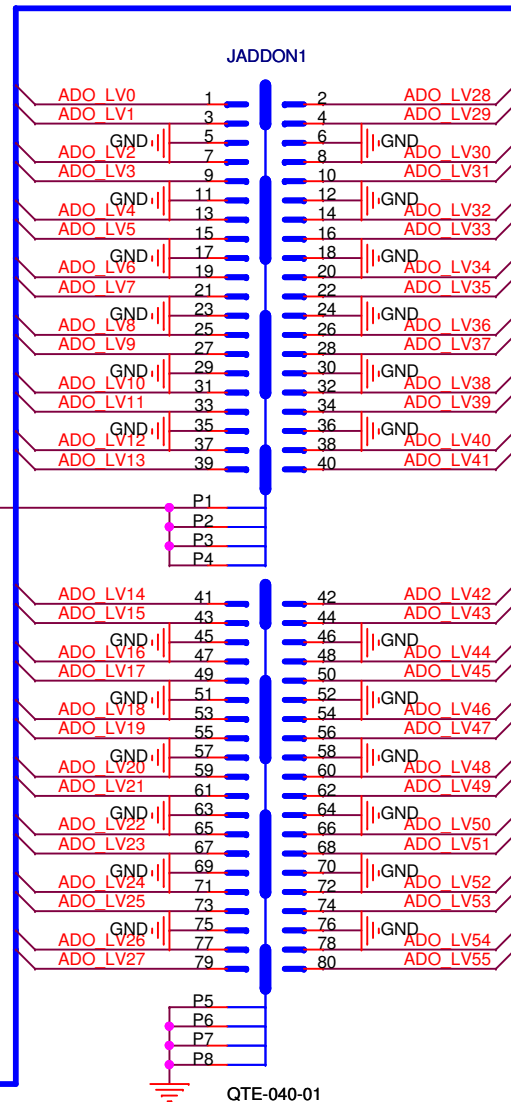
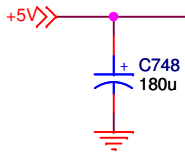
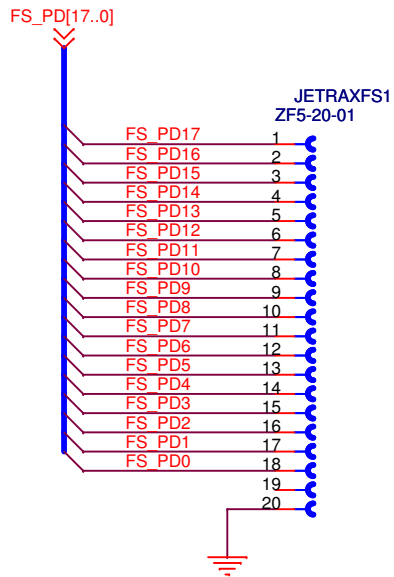


MODDEF0 is grounded by module when inserted

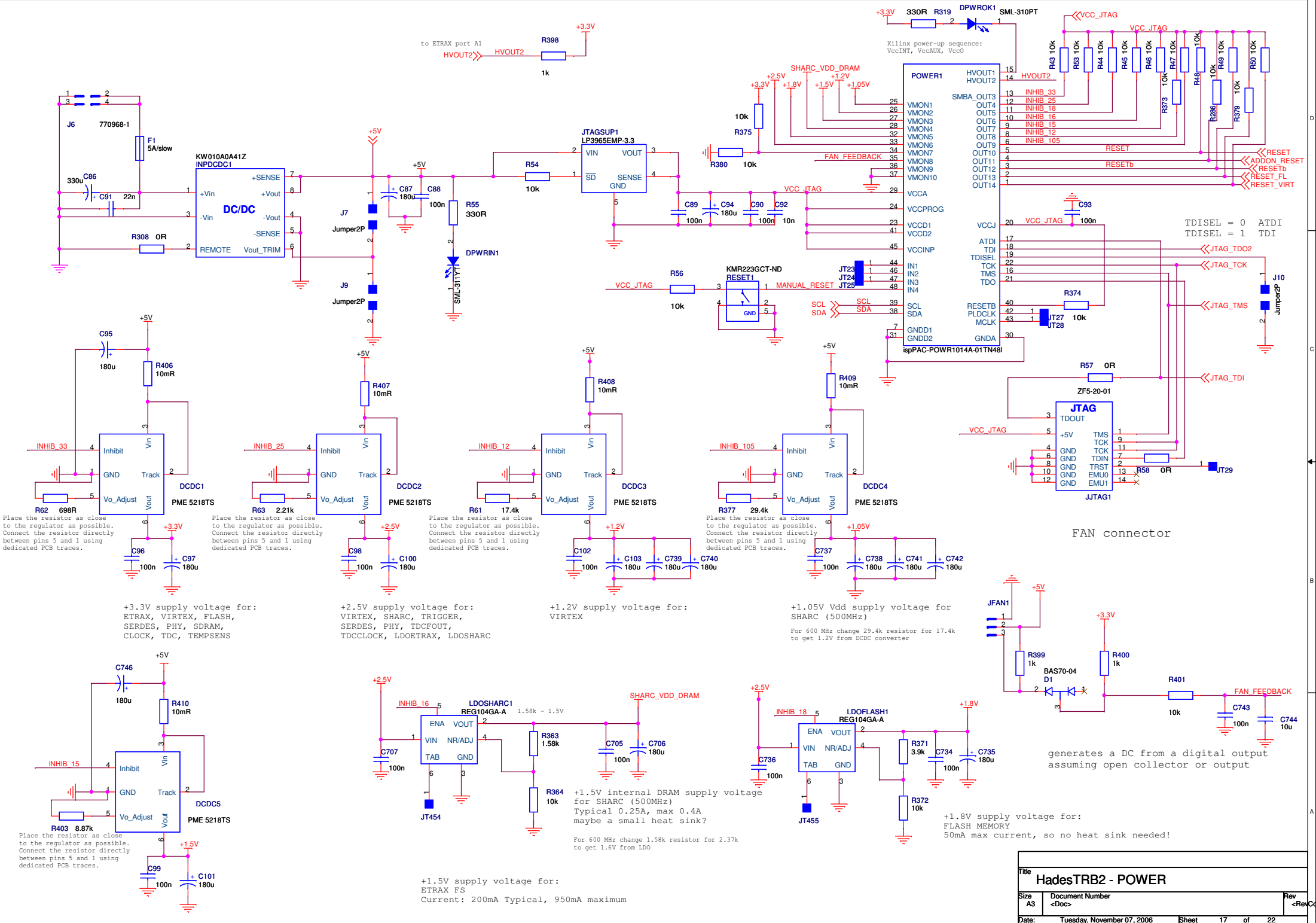
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<b>HadesTRB2 - SERDES</b>		
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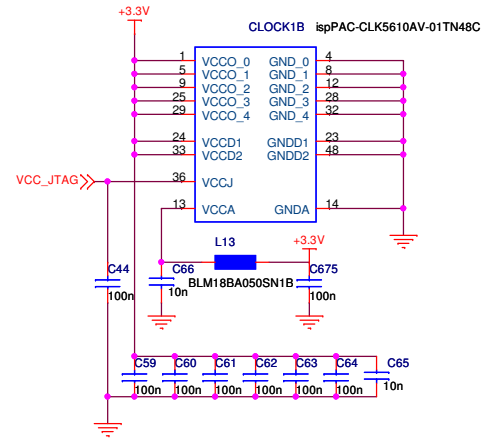
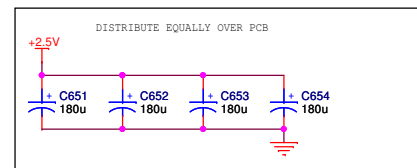
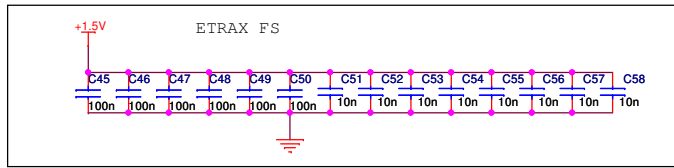
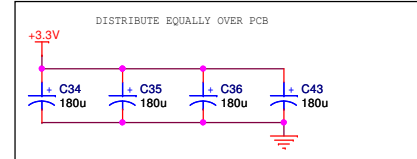
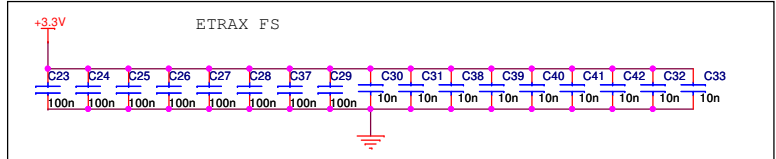
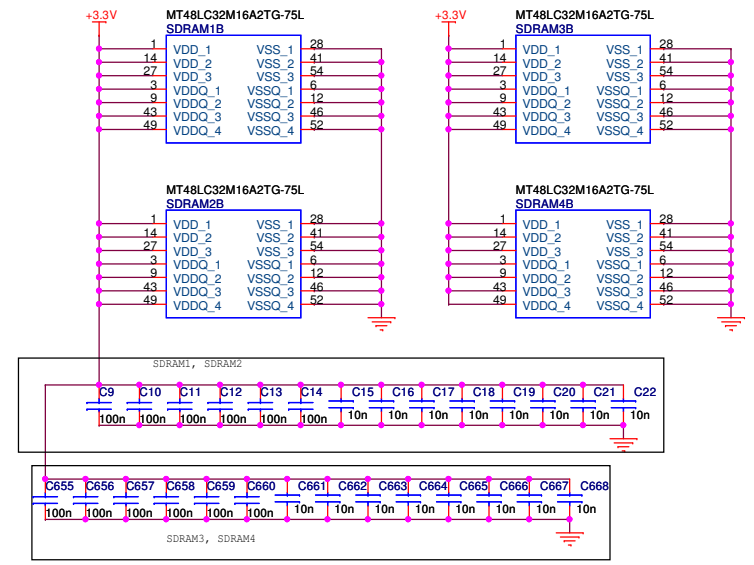
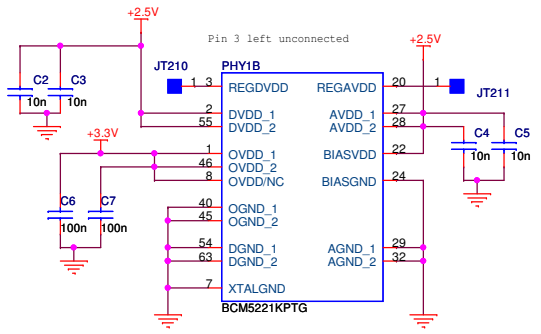
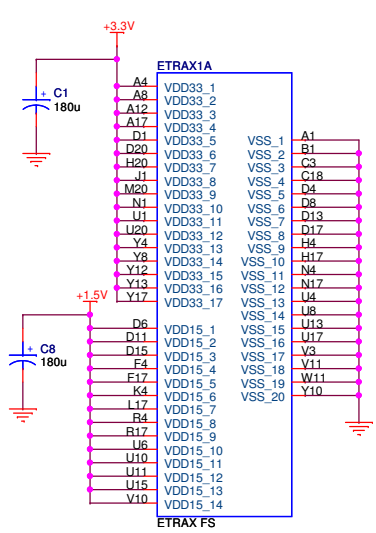


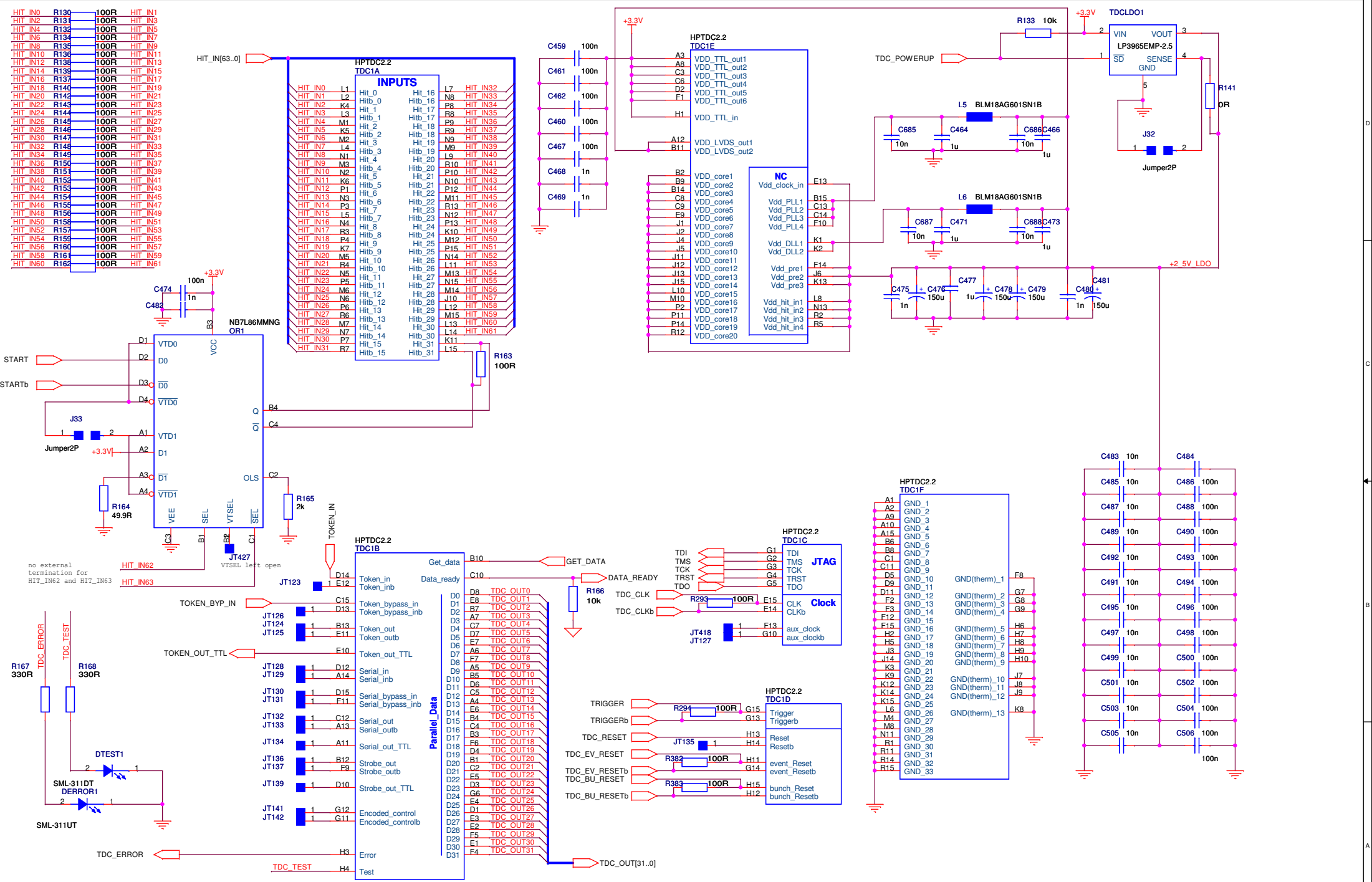


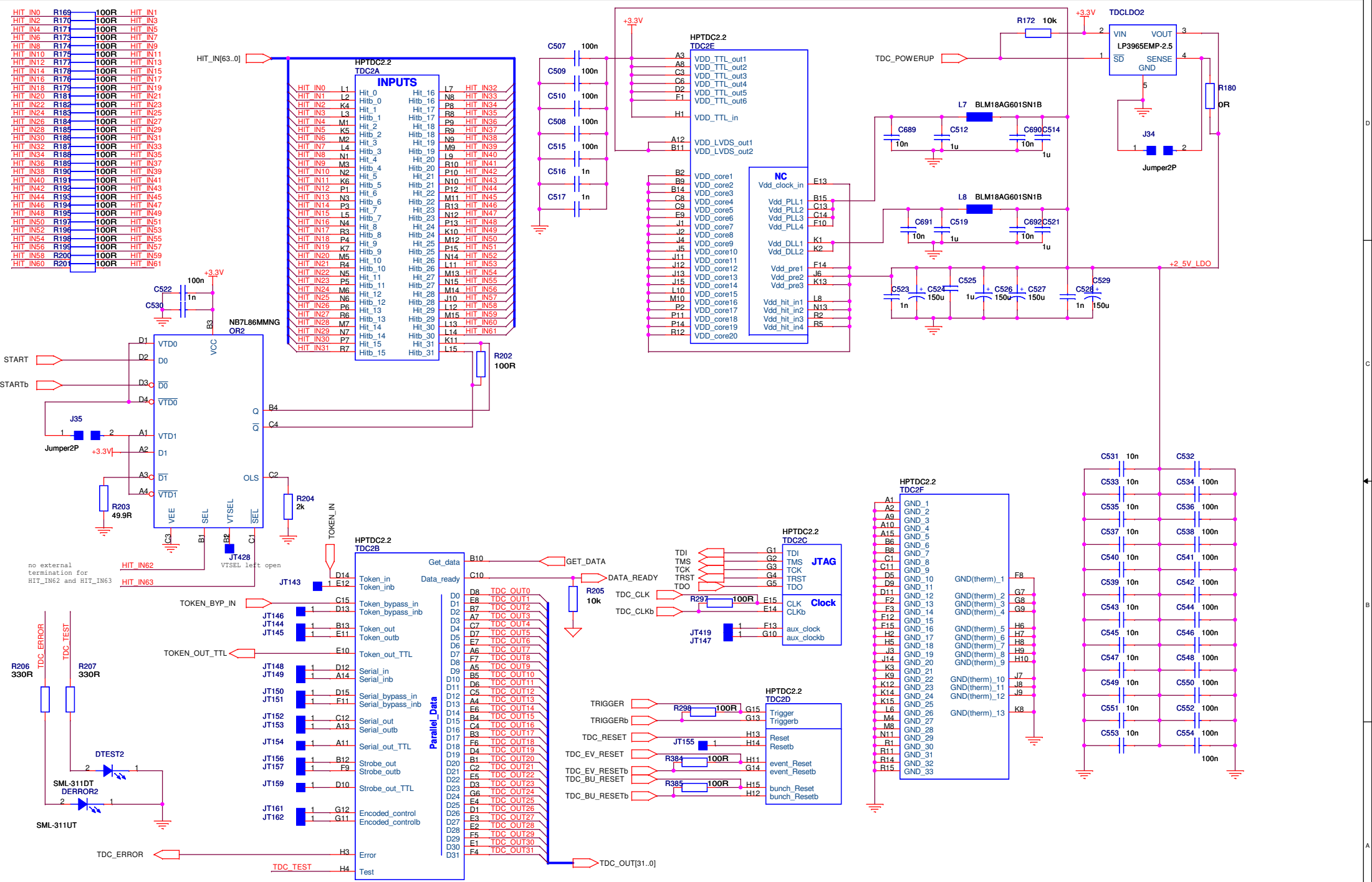
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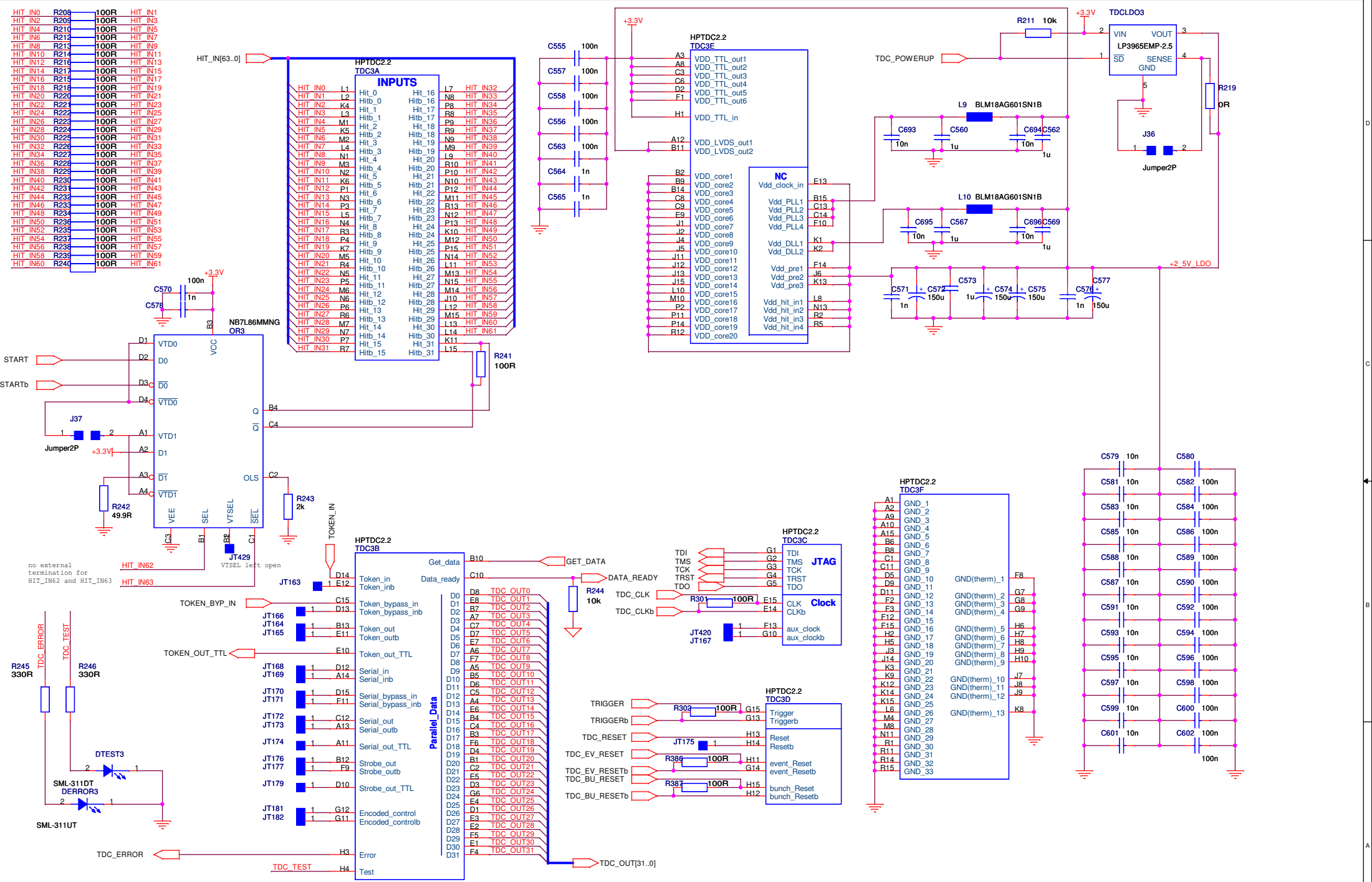


Title		
HadesTRB2 - POWER		
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